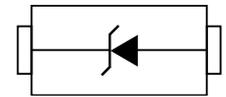


## Description

The PESDUC9D5VU ESD protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PESDUC9D5VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PESDUC9D5VU is available in a SOD-923 package with working voltages of 5 volt. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical. Additionally, it may be "sprinkled" around the board in applications where board space is at a premium. It may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 ( $\pm 15\text{kV}$  air,  $\pm 8\text{kV}$  contact discharge)



## Feature

- 40W Peak pulse power per line ( $t_P = 8/20\mu\text{s}$ )
- SOD-923 package
- Replacement for MLV(0402)
- Unidirectional configurations
- Response time is typically  $< 1\text{ ns}$
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)  $\pm 15\text{kV}$ (air),  $\pm 8\text{kV}$ (contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

## Applications

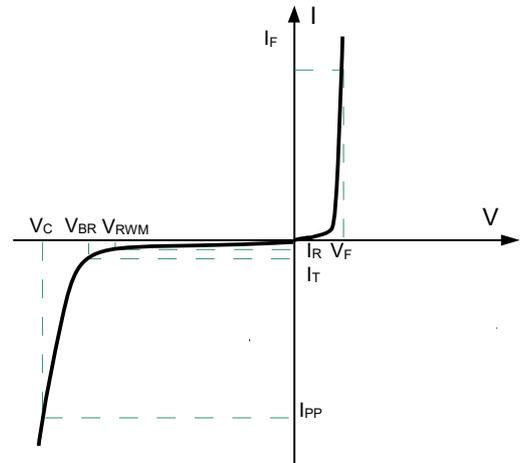
- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

## Mechanical Characteristics

- Lead finish: 100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:  $260^\circ\text{C}$
- Device meets MSL 1 requirements
- Pure tin plating:  $7 \sim 17\ \mu\text{m}$
- Pin flatness:  $\leq 3\text{mil}$

Electronics Parameter

Symbol	Parameter
$V_{RWM}$	Peak Reverse Working Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$P_{PP}$	Peak Pulse Power
$C_J$	Junction Capacitance
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



Electrical characteristics per line@25°C ( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	$V_{RWM}$				5	V
Breakdown Voltage	$V_{BR}$	$I_T = 1\text{mA}$	5.6		9.0	V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5\text{V}$			1	$\mu\text{A}$
Forward Voltage	$V_F$	$I_F = 10\text{mA}$		0.77		V
Clamping Voltage	$V_C$	$I_{PP} = 3.5\text{A}$ $t_P = 8/20\mu\text{s}$		12.0	14.0	V
Junction Capacitance	$C_J$	$V_R = 0\text{V}$ $f = 1\text{MHz}$		0.5	0.98	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power ( $t_P = 8/20\mu\text{s}$ )	$P_{PP}$	40	W
Lead Soldering Temperature	$T_L$	260 (10 sec)	°C
Operating Temperature	$T_J$	-55 to +125	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

Typical Characteristics

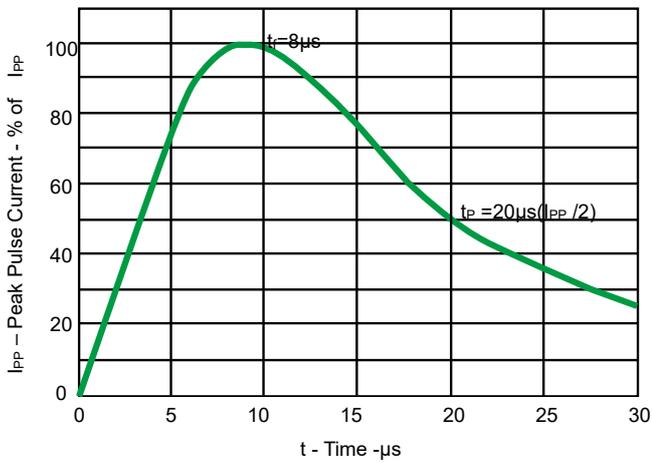


Fig 1. Pulse Waveform

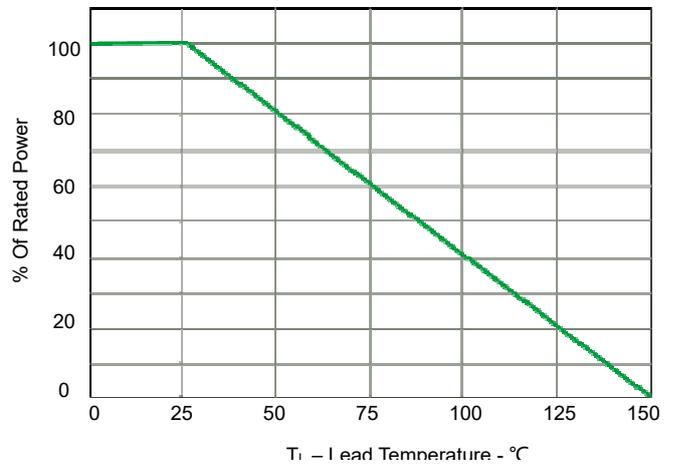


Fig 2. Power Derating Curve

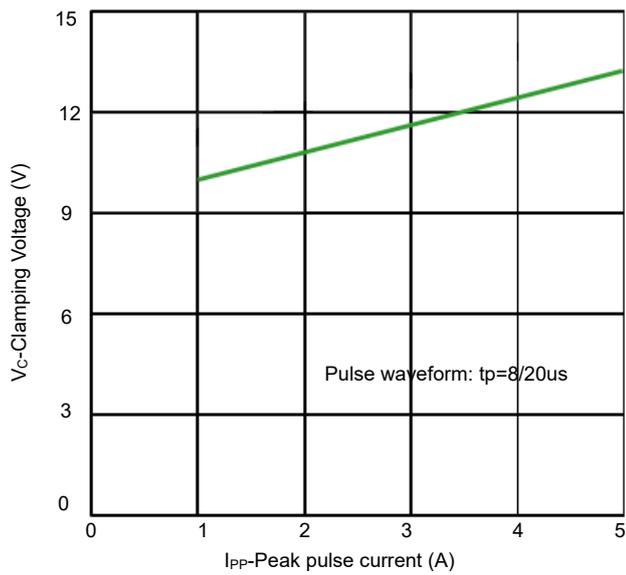


Fig 3. Clamping voltage vs. Peak pulse current

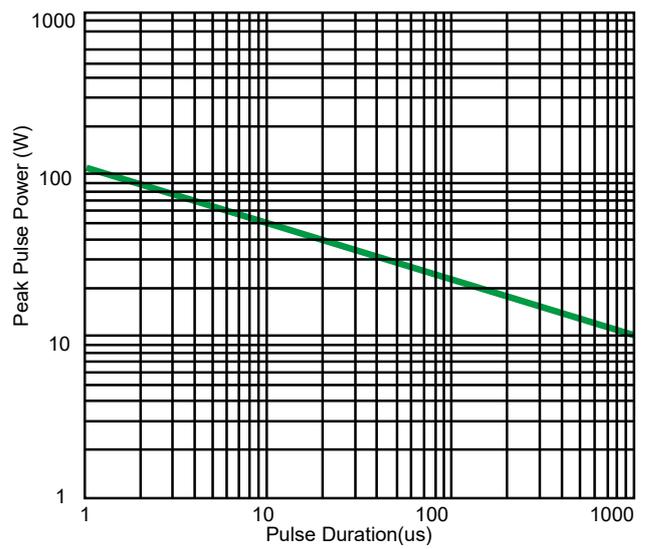
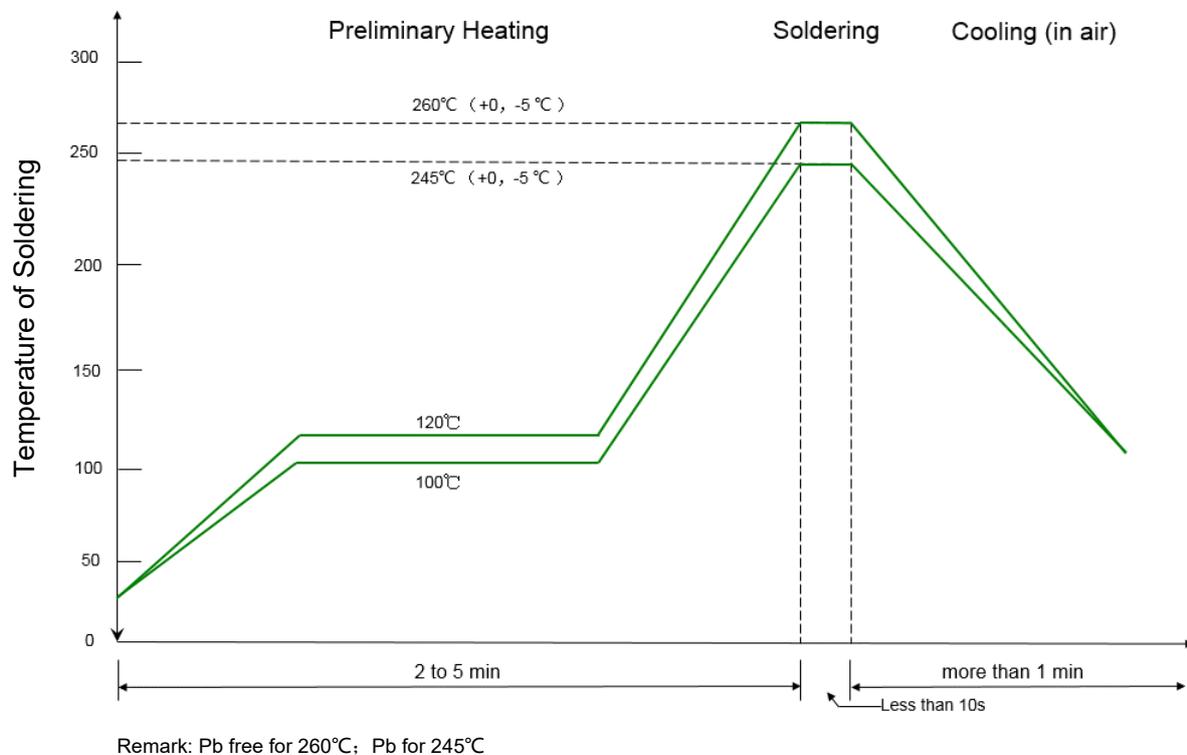


Fig 4. Non Repetitive Peak Pulse Power vs. Pulse time

## Solder Reflow Recommendation

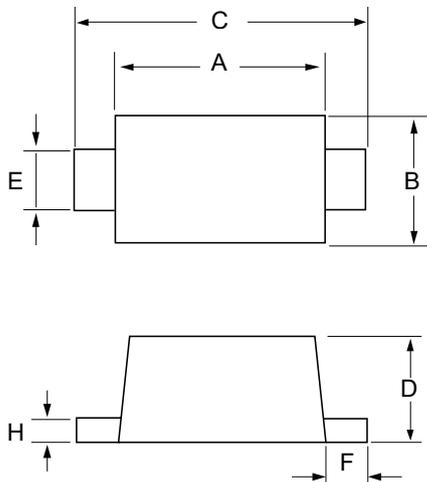


## PCB Design

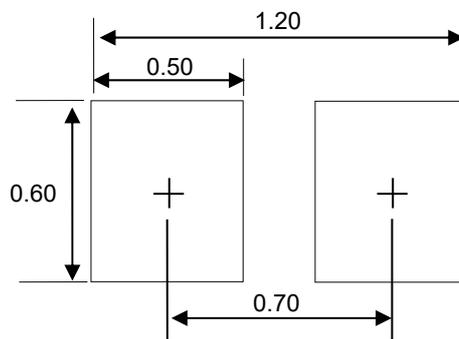
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (SOD-923)



Dim	Inches		Millimeters	
	MIN	MAX	MIN	MAX
A	0.030	0.033	0.75	0.85
B	0.022	0.026	0.55	0.65
C	0.037	0.041	0.95	1.05
D	0.014	0.017	0.36	0.43
E	0.006	0.010	0.15	0.25
F	0.002	0.006	0.05	0.15
H	0.003	0.007	0.07	0.17



Unit:mm

Suggested PCB Layout

Ordering information

Device	Package	MPQ
PESDUC9D5VU	SOD-923 (Pb-Free)	8000 / Tape & Reel

**IMPORTANT NOTICE**

 and **Prisemi**<sup>®</sup> are registered trademarks of **Prisemi Electronics Co., Ltd** (Prisemi). Prisemi reserves the right to make changes without further notice to any products herein. Prisemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Prisemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. “Typical” parameters which may be provided in Prisemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including “Typicals” must be validated for each customer application by customer’s technical experts. Prisemi does not convey any license under its patent rights nor the rights of others. The products listed in this document are designed to be used with ordinary electronic equipment or devices, Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

Website: <http://www.prisemi.com>

For additional information, please contact your local Sales Representative.

©Copyright 2009, Prisemi Electronics

 **Prisemi**<sup>®</sup> is a registered trademark of Prisemi Electronics.

All rights are reserved.