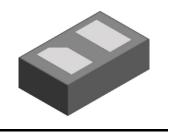


Bi-directional Low Capacitance ESD Protector

Description

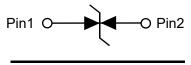
The PESDRCH2XD20VBI protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



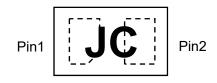
DFN0603-2L(Bottom View)

Feature

- \triangleright 90W peak pulse power per line (t_p = 8/20µs)
- Low clamping voltage
- ➤ DFN0603-2L package
- > Response time is typically < 1 ns
- > Bidirectional configurations
- > RoHS compliant
- ➤ Transient protection for data lines to IEC 61000-4-2(ESD) ±15kV(air), ± 15kV(contact); IEC 61000-4-5 (Lightning) 3A (8/20us)



Circuit Diagram



Marking (Top View)

Applications

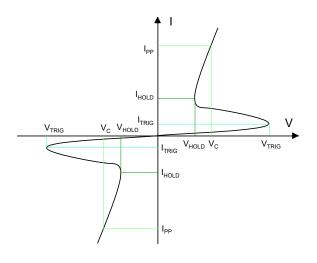
- Cellular phones
- > Portable devices
- Digital cameras
- Power supplies
- ➤ USB 2.0 and USB 3.0
- ➤ HDMI 1.3 and HDMI 1.4

Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements

Electronics Parameter

Symbol	Parameter		
V_{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I _T		
I _T	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
V _C	Clamping Voltage @ I _{PP}		
I _{TRIG}	Reverse Trigger Current		
V_{TRIG}	Reverse Trigger Voltage		
I _{HOLD}	Reverse Holding Current		
V _{HOLD}	Reverse Holding Voltage		



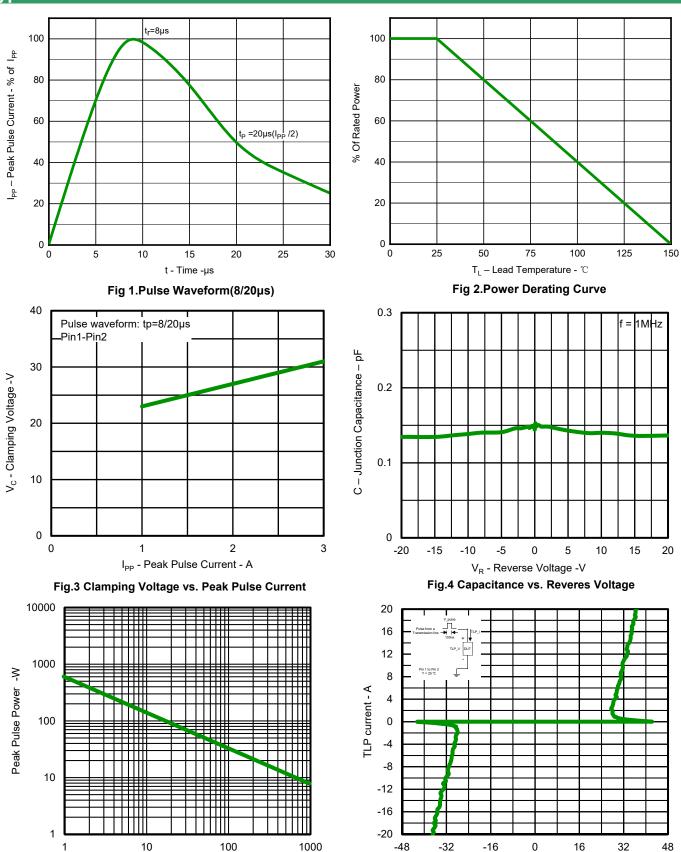
Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}	-	-	-	20	V
Breakdown Voltage	V_{BR}	I _t = 1mA	21	-	28	V
Reverse Leakage Current	I _R	V _{RWM} = 20V	-	-	1.0	μA
Clamping Voltage	V _C	$I_{PP} = 3A, t_P = 8/20 \mu s$	-	31	35	V
Junction Capacitance	CJ	$V_R = 0V, f = 1MHz$	-	0.15	0.22	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units	
Peak Pulse Power (t _P = 8/20µs)	P _{PP}	90	W	
Peak Pulse Current (t _P = 8/20μs)	I _{PP}	3.0	A	
Lead Soldering Temperature	T _L	260 (10 sec)	℃	
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	℃	
ESD Protection-Contact Discharge	V _{ESD}	±15	kV	
ESD Protection-Air Discharge	V _{ESD}	±15	kV	

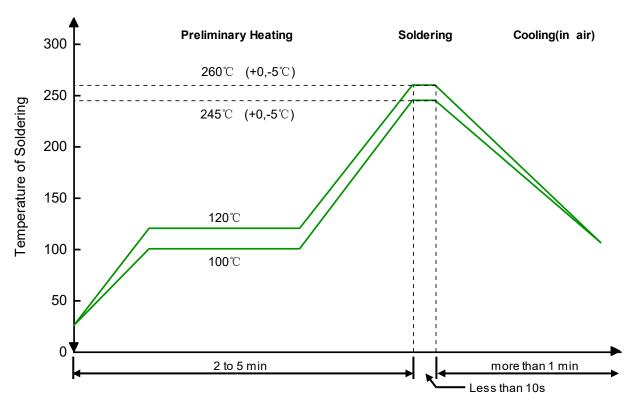
Typical Characteristics



 $\label{eq:PulseDuration} \mbox{-} \mu \mbox{s}$ Fig.5 Non-Repetitive Peak Pulse Power vs. Pulse Time

TLP Voltage - V

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

PCB Design

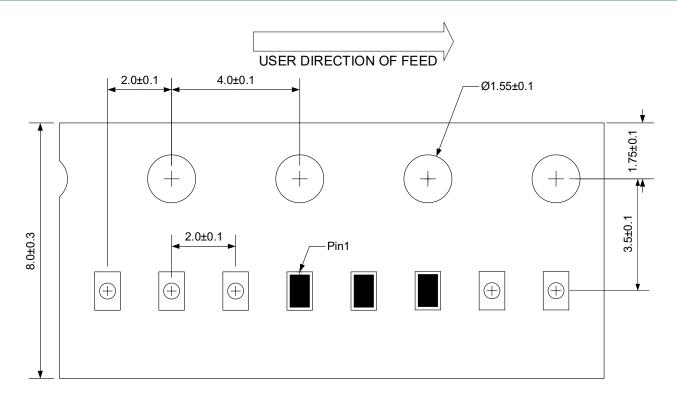
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

Ordering information

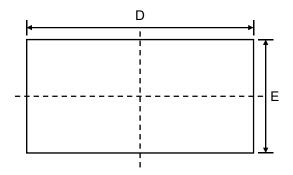
Package	Reel	Shipping
DFN0603-2L	7"	10000 / Tape & Reel

Load with information

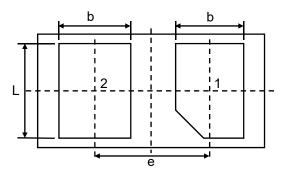


Unit:mm

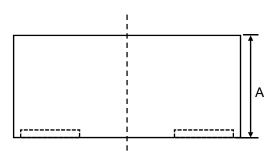
Product dimension (DFN0603-2L)



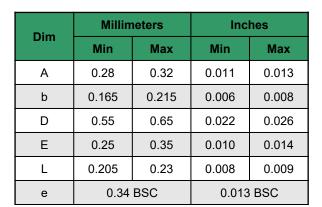
Top View

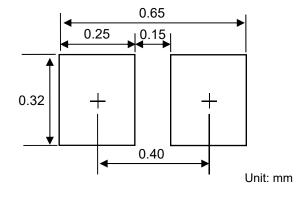


Bottom View



Side View





Suggested PCB Layout

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