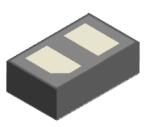


PESDNC2FD24VBN

Bi-directional 24V Normal Capacitance ESD Protector

Description

The PESDNC2FD24VBN protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L(Bottom View)

Feature

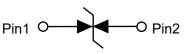
- 300W peak pulse power per line (t_P = 8/20µs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC61000-4-2(ESD) ±20KV(air), ±20KV(contact); IEC61000-4-4 (EFT) 40A (5/50ns)

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

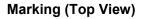
Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- DFN1006-2L without plating



Circuit Diagram

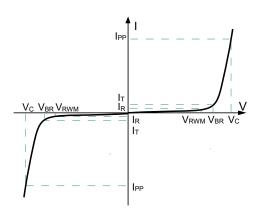




PESDNC2FD24VBN

Electronics Parameter

Symbol	Parameter		
VRWM	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
VBR	Breakdown Voltage @ I⊤		
Iτ	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
IF	Forward Current		
VF	Forward Voltage @ I _F		



Electrical characteristics per line@25°C (unless otherwise specified)

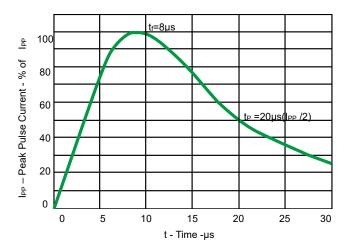
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	VRWM				24	V
Breakdown Voltage	V _{BR}	l _t = 1mA	25	27		V
Reverse Leakage Current	I _R	V _{RWM} = 24V T=25℃			1.0	μA
Clamping Voltage	Vc	I _{PP} =1A,t _P = 8/20µs		34	40	V
Clamping Voltage	Vc	I _{PP} =6A,t _P = 8/20µs		55	60	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		20	25	pF

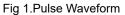
Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	300	W
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	Тѕтс	-55 to 150	°C

PESDNC2FD24VBN

Typical Characteristics





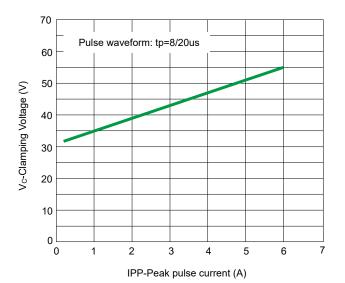


Fig 3. Clamping voltage vs. Peak pulse current

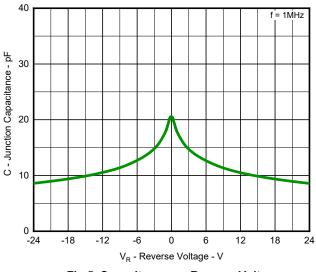


Fig 5. Capacitance vs. Reveres Voltage

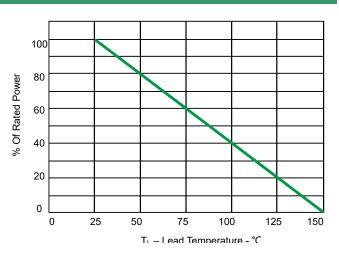


Fig 2.Power Derating Curve

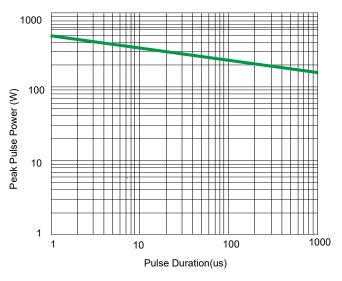
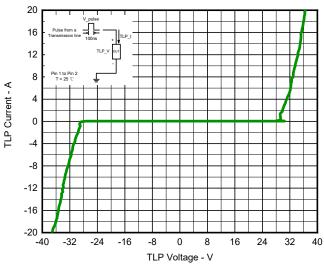
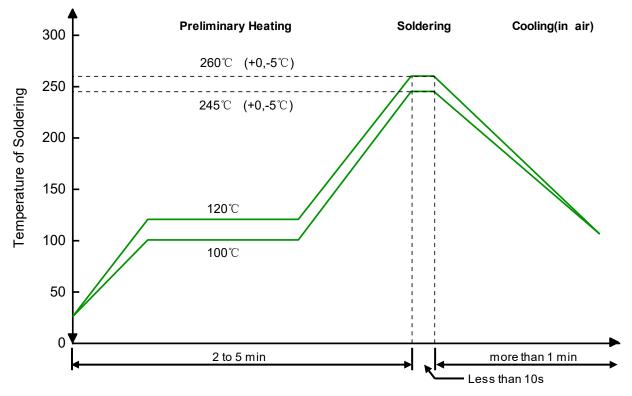


Fig 4. Non-Repetitive Peak Pulse Power vs. Pulse time



PESDNC2FD24VBN

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

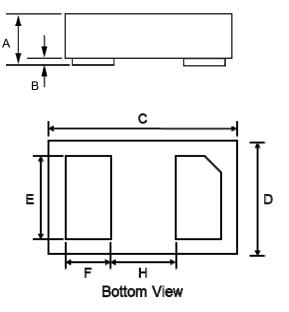
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

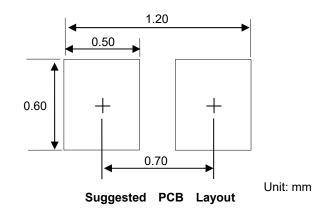
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

PESDNC2FD24VBN

Product dimension (DFN1006-2L)



Dim	Millim	neters	Inches		
	MIN	МАХ	MIN	МАХ	
А	0.40	0.55	0.016	0.022	
В	0.00	0.05	0.000	0.002	
С	0.90	1.10	0.035	0.043	
D	0.55	0.65	0.022	0.026	
E	0.35	0.55	0.014	0.022	
F	0.15	0.30	0.006	0.012	
Н	0.40 Тур.		0.015 Тур.		

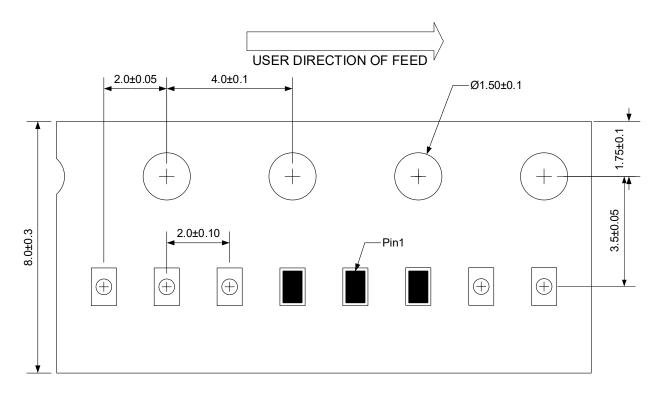


Ordering information

Device	Package	Reel	Shipping
PESDNC2FD24VBN	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

PESDNC2FD24VBN

Load with information



Unit:mm

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