



Low Capacitance TVS Array

Description

The PESDAUC563T5VU is low capacitance transient voltage suppressor array for high speed data interface that designed to protect sensitive electronics from damage or latch-up due to ESD lightning, and other voltage induced transient events.

All pins are rated to withstand 15kv ESD pulses using the IEC 61000-4-2 contact discharge method, which can meet the requirement of level 4.

Pin1

SOT-563 (Top View)

Feature

- ➤ 150W peak pulse power (tp=8/20µs) :
- ➤ SOT-563 Package
- Working voltage: 5V
- Low capacitance
- Low clamping voltage
- > RoHS Compliant
- ➤ Transient Protection for High Speed Data Lines to IEC61000-4-2(ESD)±15kV(air),±8kV(Contact)

Applications

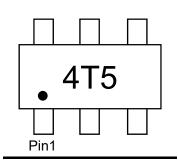
- > USB 2.0 Power & data line protection
- > DVI & HDMI port protection
- Serial ATA port protection
- ➤ Mobile handsets
- > Digital cameras and camcorders
- > PDA & MP3 players
- Digital TV and set-top boxes
- Other portable electronic components

Pin6 Pin5 Pin4 A A A A Pin1 Pin2 Pin3

Circuit Diagram

Mechanical Characteristics

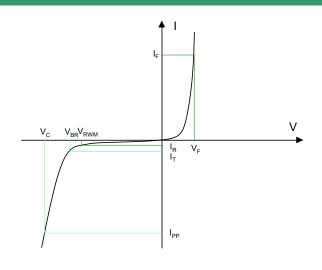
- > Lead finish:100% matte Sn(Tin)
- > Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- ➤ Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil



Marking (Top View)

Electronics Parameter

Symbol	Parameter		
V_{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I _T		
I _T	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
V _C	Clamping Voltage @ I _{PP}		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
I _F	Forward Current		
V_{F}	Forward Voltage @ I _F		



Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}	-	1	-	5.0	٧
Breakdown Voltage	V_{BR}	I _t = 1mA	6.0	-	9.0	V
Reverse Leakage Current	I _R	V _{RWM} = 5V	-	-	1.0	μA
Clamatin a Valta a	V _C	$I_{PP} = 1A, t_P = 8/20 \mu s$	-	-	12.5	\/
Clamping Voltage		$I_{PP} = 5A, t_P = 8/20 \mu s$	-	-	20) V
lunction Conscitance	Сл	$V_R = 0V, f = 1MHz, IO and GND$ -		1.5	-	, C
Junction Capacitance		V _R = 0V,f = 1MHz, IO and I/O	-	0.7	-	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power (t _P = 8/20μs)	P _{PP}	150	W
Peak Pulse Current (t _P = 8/20μs)	I _{PP}	5.0	А
Lead Soldering Temperature	T _L	260 (10 sec)	°C
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	°C
ESD Protection-Contact Discharge	V _{ESD}	±8	kV
ESD Protection-Air Discharge	V _{ESD}	±15	kV

Typical Characteristics

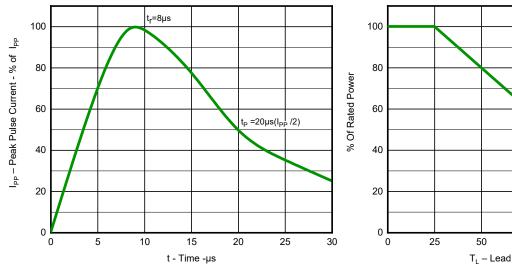


Fig 1.Pulse Waveform(8/20µs)

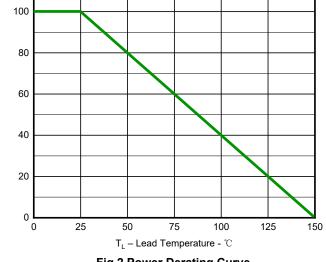


Fig 2.Power Derating Curve

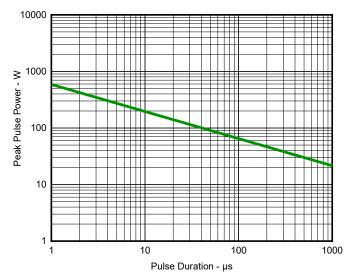
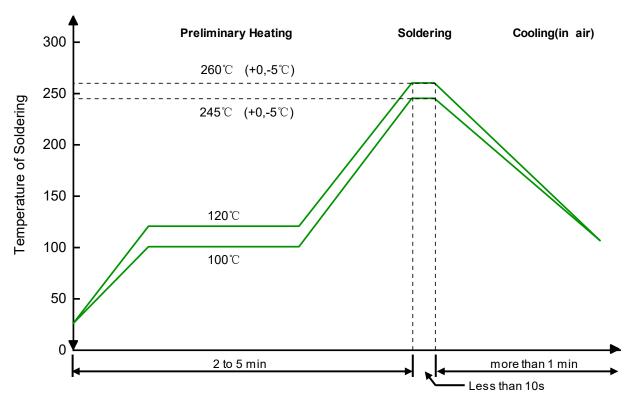


Fig 3. Non Repetitive Peak Pulse Power vs. Pulse Time

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

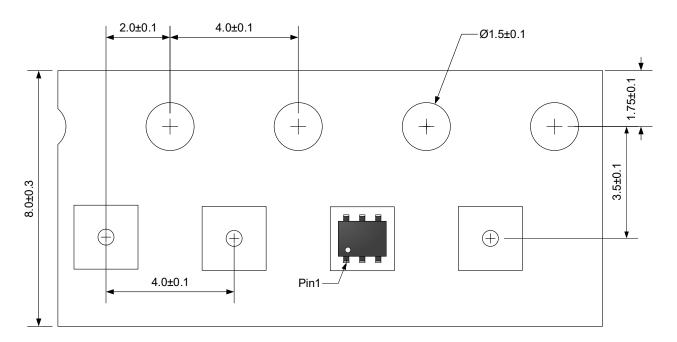
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- ➤ Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

Ordering information

Device	Package	Reel	Shipping
PESDAUC563T5VU	SOT-563	7"	3000 / Tape & Reel

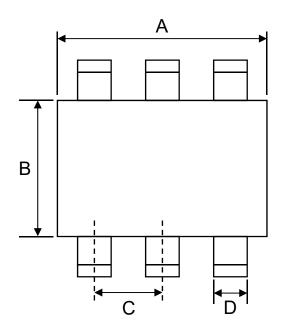
Load with information

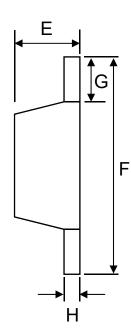




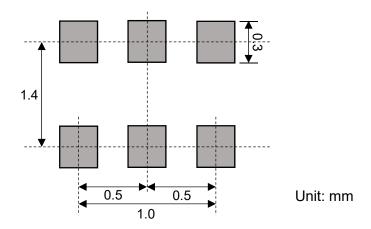
Unit:mm

Product Dimension (SOT-563)





Dim	Millim	neters	Inches		
	Min	Max	Min	Max	
Α	1.50	1.70	0.059	0.067	
В	1.10	1.30	0.043	0.051	
С	0.50	BSC	0.020 BSC		
D	0.17	0.27	0.007	0.011	
Е	0.50	0.60	0.020	0.024	
F	1.50	1.70	0.059	0.067	
G	0.10	0.30	0.004	0.012	
Н	0.08	0.16	0.003	0.006	



Suggested PCB Layout

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