

# **Digital Transistor(built-in resistors)**

### **Feature**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

### **Applications**

- Inverter
- Interface
- Driver



- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

# R1 OUT $R2 = 10K\Omega$ R3 - OUT $R4 = 10K\Omega$ $R5 = 10K\Omega$ $R6 = 10K\Omega$ $R7 = 10K\Omega$

### **Structure**

PNP epitaxial planar silicon transistor (Resistor built-in type)

### Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
location literature	$V_{I(off)}$	V <sub>CC</sub> =-5V,I <sub>O</sub> =-100μA	-	-	-0.5	V
Input voltage	$V_{I(on)}$	V <sub>O</sub> =-0.3V,I <sub>O</sub> =-10mA	-3	-	-	V
Output voltage	$V_{O(off)}$	I <sub>O</sub> /I <sub>I</sub> =-10mA/-0.5mA		-0.1	-0.3	V
Input current	I <sub>I</sub>	V <sub>I</sub> =-5V	-	-	-0.88	mA
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> =-50V, V <sub>I</sub> =0V	-	-	-0.5	μA
DC current gain	G₁	V <sub>O</sub> =-5V, I <sub>O</sub> =-5mA	30	-	-	
Input resistance	R <sub>1</sub>	-	7	10	13	ΚΩ
Resistance ration	R <sub>2</sub> /R <sub>1</sub>	-	0.88	1	1.2	-
Transition frequency	f⊤	V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz	-	250	-	MHz

# Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-40 to +10	V
Output ourront	lo	-50	mA
Output current	I <sub>C(MAX.)</sub>	-100	mA
Power dissipation	P <sub>d</sub>	150	mW
Junction temperature	Tj	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C

### **Typical Characteristics**

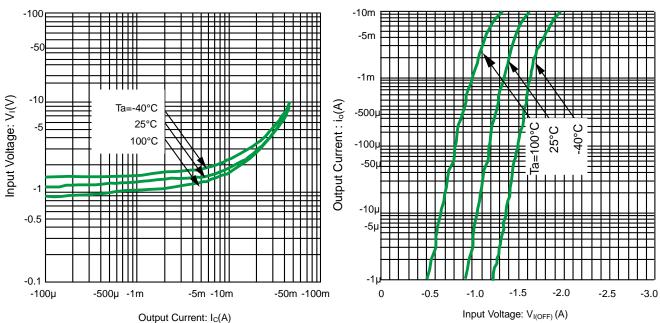
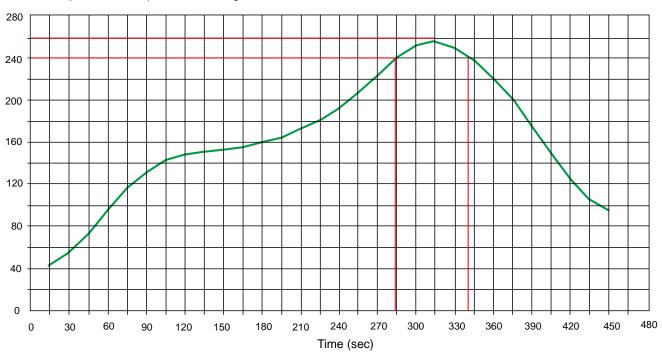


Fig 1.Input Voltage vs. output current @Vc=-0.3V (ON characteristics)

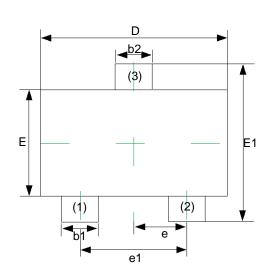
Fig 2.Output current vs. input voltage @Vcc=-5V(OFF characteristics)

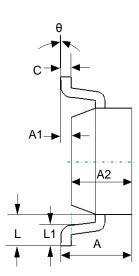
### **Solder Reflow Recommendation**

Peak Temp=257℃, Ramp Rate=0.802deg. ℃/sec

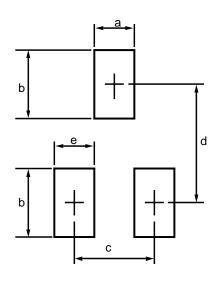


# **Product dimension (SOT-523)**





Dim	Millimeters		Inches		
	MIN	MAX	MIN	MAX	
А	0.700	0.900	0.028	0.035	
A1	0.000	0.100	0.000	0.004	
A2	0.700	0.800	0.028	0.031	
b1	0.150	0.250	0.006	0.010	
b2	0.250	0.350	0.010	0.014	
С	0.100	0.200	0.004	0.008	
D	1.500	1.700	0.059	0.067	
Е	0.700	0.900	0.028	0.035	
E1	1.450	1.750	0.057	0.069	
е	0.500TYP		0.020TYP		
e1	0.900	1.100	0.035	0.043	
L	0.400REF		0.016REF		
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	



Dim	Millimeters			
	MIN	MAX		
а		0.5		
b		0.6		
С		1.0		
d		1.24		
е		0.4		

# Ordering information

Device	Package	Shipping
PDTA114EE	SOT-523 (Pb-Free)	3000 / Tape & Reel

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