

**Feature**

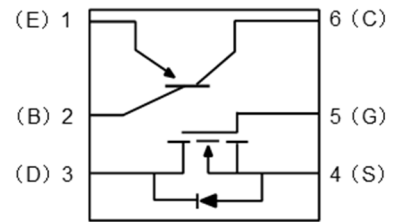
This device is Pb-Free, Halogen Free/BFR Free and RoHS compliant.  
PNMT6N1C is composed by a transistor and a MOSFET

Transistor:

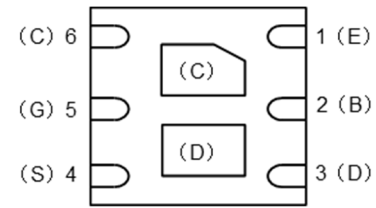
- Very low collector to emitter saturation voltage
- DC current gain >100
- 3A continuous collector current
- PNP epitaxial planar silicon transistor

MOSFET:

MOSFET Product Summary		
$V_{DS}(V)$	$R_{DS(on)}(\Omega)$	$I_D(A)$
20	0.2@ $V_{GS}=4.5V$	1



**Schematic diagram**



**Bottom View**

- Transistor

**Electrical characteristics per line@25°C ( unless otherwise specified)**

Parameter	Symbol	Conditions	Value	Units
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = -10mA$	-30	V
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = -0.1mA$	-40	V
Emitter -Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = -0.1mA$	-5	V
Collector Current	$I_C$		-3	A
Collector Peak Current	$I_{CM}$		-6	A
Base Current	$I_B$		-0.2	A
Base Peak Current	$I_{BM}$		-0.5	A
Total Dissipation @25°C	$P_{tot}$		1.2	W
Storage Temperature	$T_{STG}$		-65~150	°C
Max. Operating Junction Temperature	$T_J$		150	°C
Junction-to-Ambient Thermal Resistance <sup>(1)</sup>	$R_{\theta JA}$		104	°C/ W

Note 1: Surface mounted on FR-4 Board using 1 square inch pad size, 1oz copper

Absolute maximum rating@25°C

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
DC Current Gain	h <sub>FE</sub>	I <sub>C</sub> =-1mA, V <sub>CE</sub> =-5.0V	150	-	-	-
		I <sub>C</sub> =-1A, V <sub>CE</sub> =-5.0V	100	-	-	
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-0.1A, I <sub>B</sub> =-1mA	-	-	-0.14	V
		I <sub>C</sub> =-0.5A, I <sub>B</sub> =-50mA	-	-	-0.17	
		I <sub>C</sub> =-1A, I <sub>B</sub> =-100mA	-	-	-0.31	
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =-1A, I <sub>B</sub> =-0.05mA	-	-	-1.1	V
Collector Cut-off Current (I <sub>E</sub> =0)	I <sub>CBO</sub>	V <sub>CB</sub> =-40V	-	-	-0.1	μA
		V <sub>CB</sub> =-30V T <sub>C</sub> =125°C	-	-	-20	
Emitter Cut-off Current(I <sub>C</sub> =0)	I <sub>EBO</sub>	V <sub>EB</sub> =-5V	-	-	-0.1	μA

➤ MOSFET

Electrical characteristics per line@25°C ( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> = 1mA, V <sub>GS</sub> = 0V	20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±8V	-	-	±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 1mA	0.5	-	1.1	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 650mA	-	0.2	0.45	Ω
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 10V, f = 1MHz	-	30	-	pF
Output Capacitance	C <sub>DSS</sub>		-	13	-	pF
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	13	-	pF
<b>SWITCHING PARAMETERS</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.0V, R <sub>G</sub> = 10Ω, R <sub>L</sub> = 67Ω I <sub>D</sub> = 150mA	-	7	-	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		-	23	-	ns

Absolute maximum rating@25°C

Rating		Symbol	Value	Units
Drain-Source Voltage		$V_{DS}$	20	V
Gate-Source Voltage		$V_{GS}$	$\pm 8$	V
Drain Current	Continuous	$I_D$	1	A
	Pulsed	$I_D$	4	A
Total Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	140	mW

Typical Characteristics

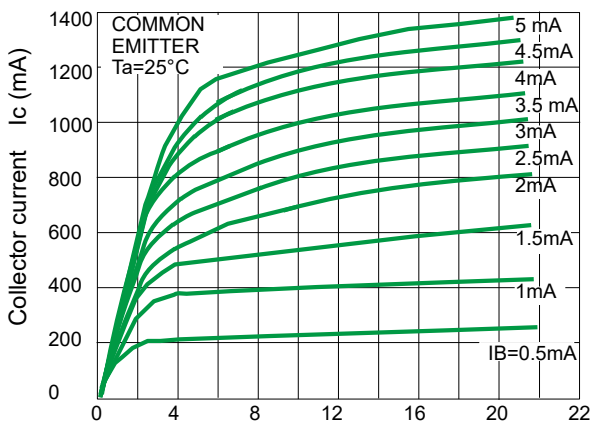


Fig1. Collector-emitter voltage  $V_{CE}$  (V)

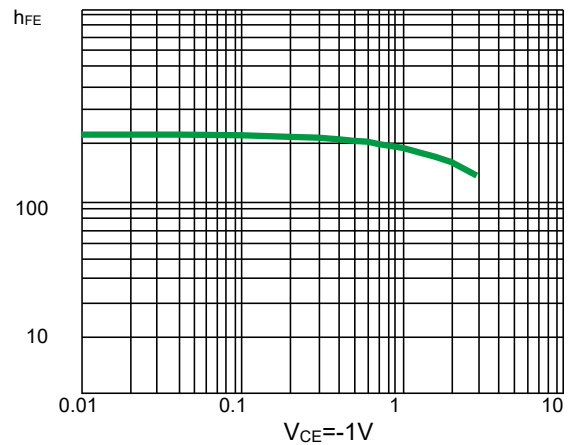


Fig2. DC Current Gain

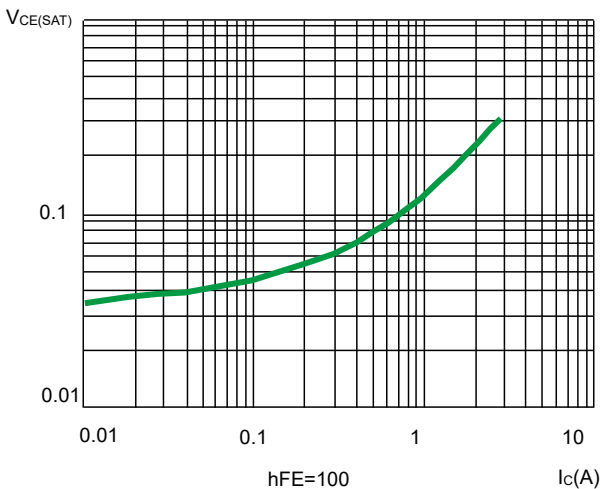


Fig 3. Collector-Emitter Saturation Voltage

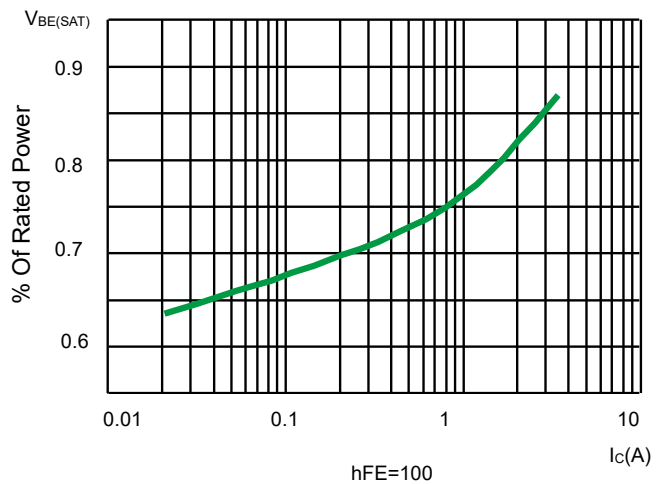


Fig4. Base-Emitter Saturation Voltage

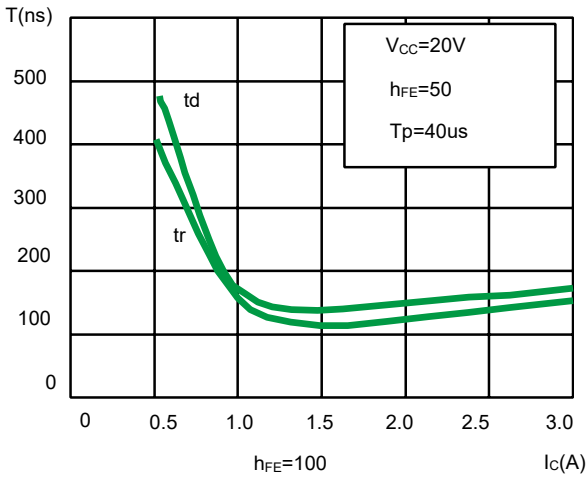


Fig 5. Switching Times Resistive Load

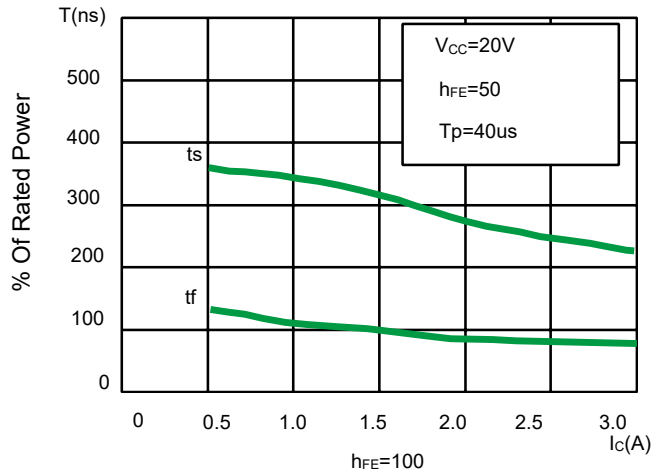


Fig 6. Switching Times Resistive Load

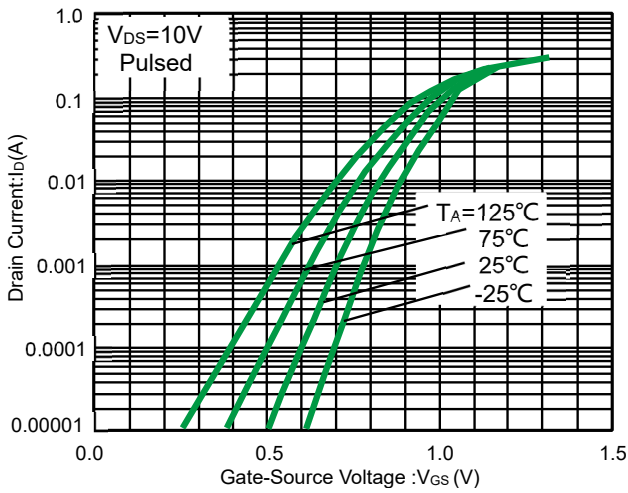


Fig 7. Typical transfer Characteristics

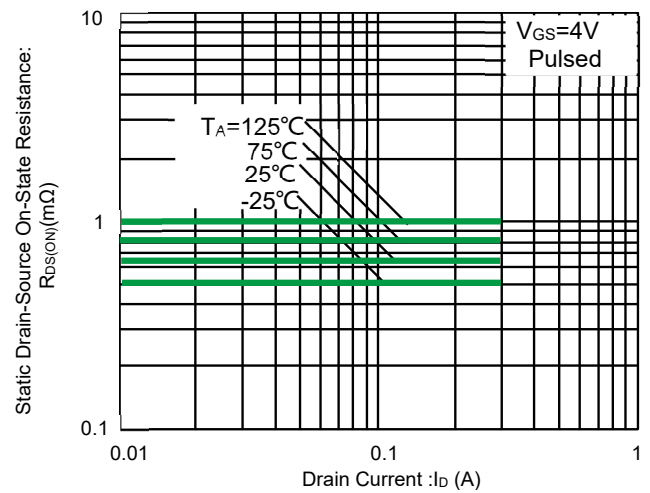


Fig 8. Static drain-source on-state resistance Vs. drain current(1)

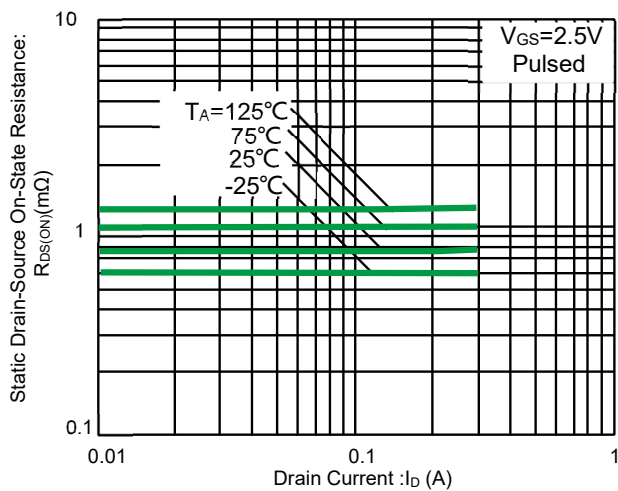


Fig 9. Static drain-source on-state resistance Vs. drain current(2)

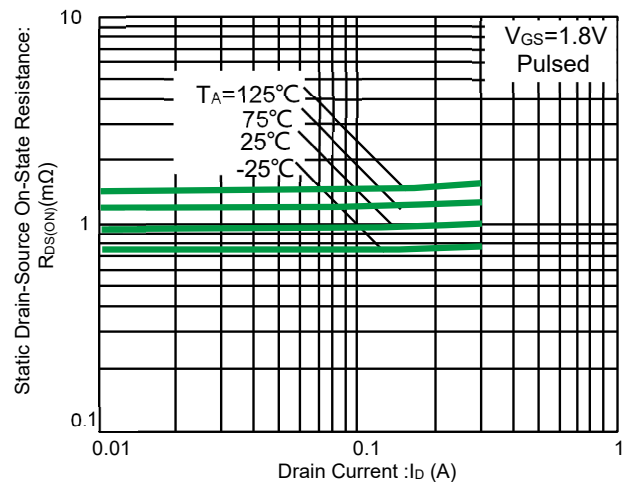


Fig 10. Static drain-source on-state resistance Vs. drain current(3)

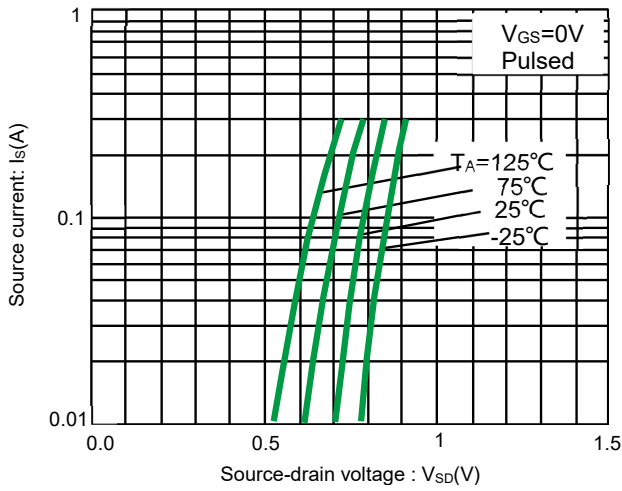


Fig 11. Source current vs. source-drain voltage

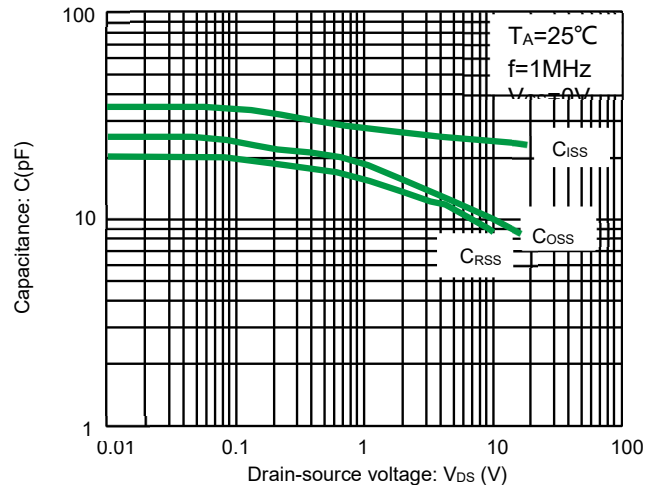


Fig 12. Typical capacitance vs. drain-source voltage

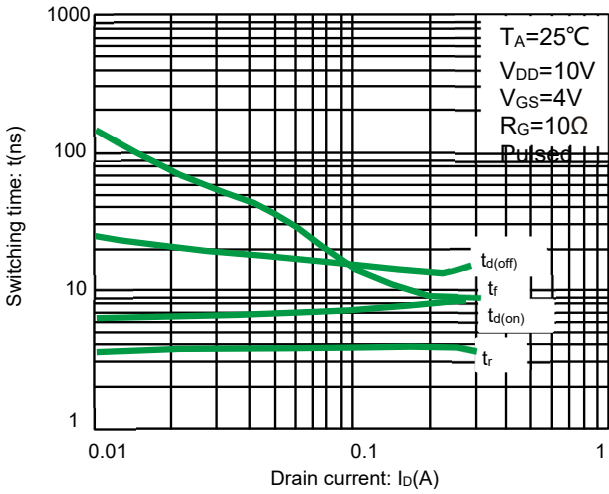
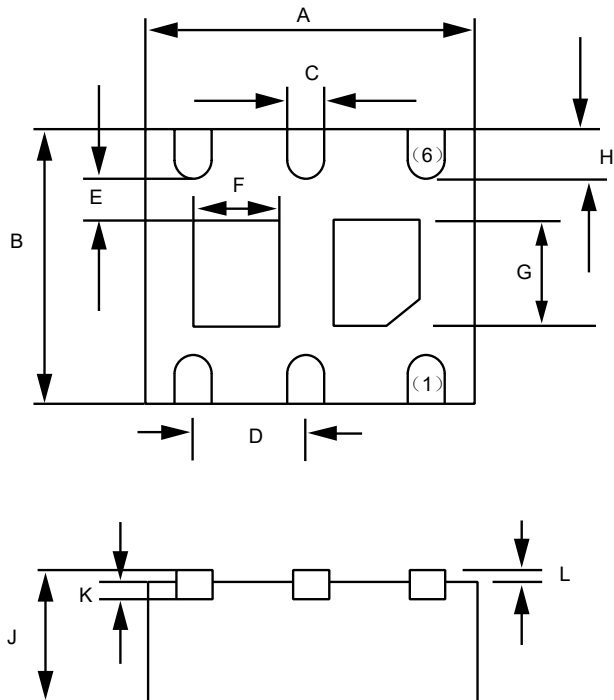
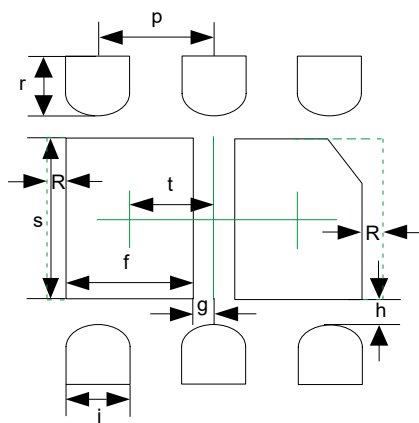


Fig 13. Switching characteristics

Product dimension DFN2020-6L



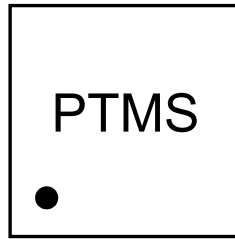
Dim	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	1.924	2.076	0.076	0.082
B	1.924	2.076	0.076	0.082
C	0.250	0.350	0.010	0.014
D	0.650 (typ.)		0.026 (typ.)	
E	0.200 MIN.		0.008 MIN.	
F	0.520	0.720	0.020	0.028
G	0.900	1.100	0.035	0.043
H	0.174	0.326	0.007	0.013
J	0.550	0.650	0.021	0.027
K	0.206 REF		0.206 REF	
L	0.203 REF		0.203 REF	



If there is enough place in PCB. It can be mounted with copper along the dotted line in order to optimize thermal design.

Dim	Millimeters	
	MIN	MAX
p	0.60	0.70
r	0.40	0.50
s	1.05	1.15
t	0.42	0.52
f	0.67	0.77
g	0.06	0.16
h	0.1	0.2
j	0.35	0.45
R	0.1	0.2

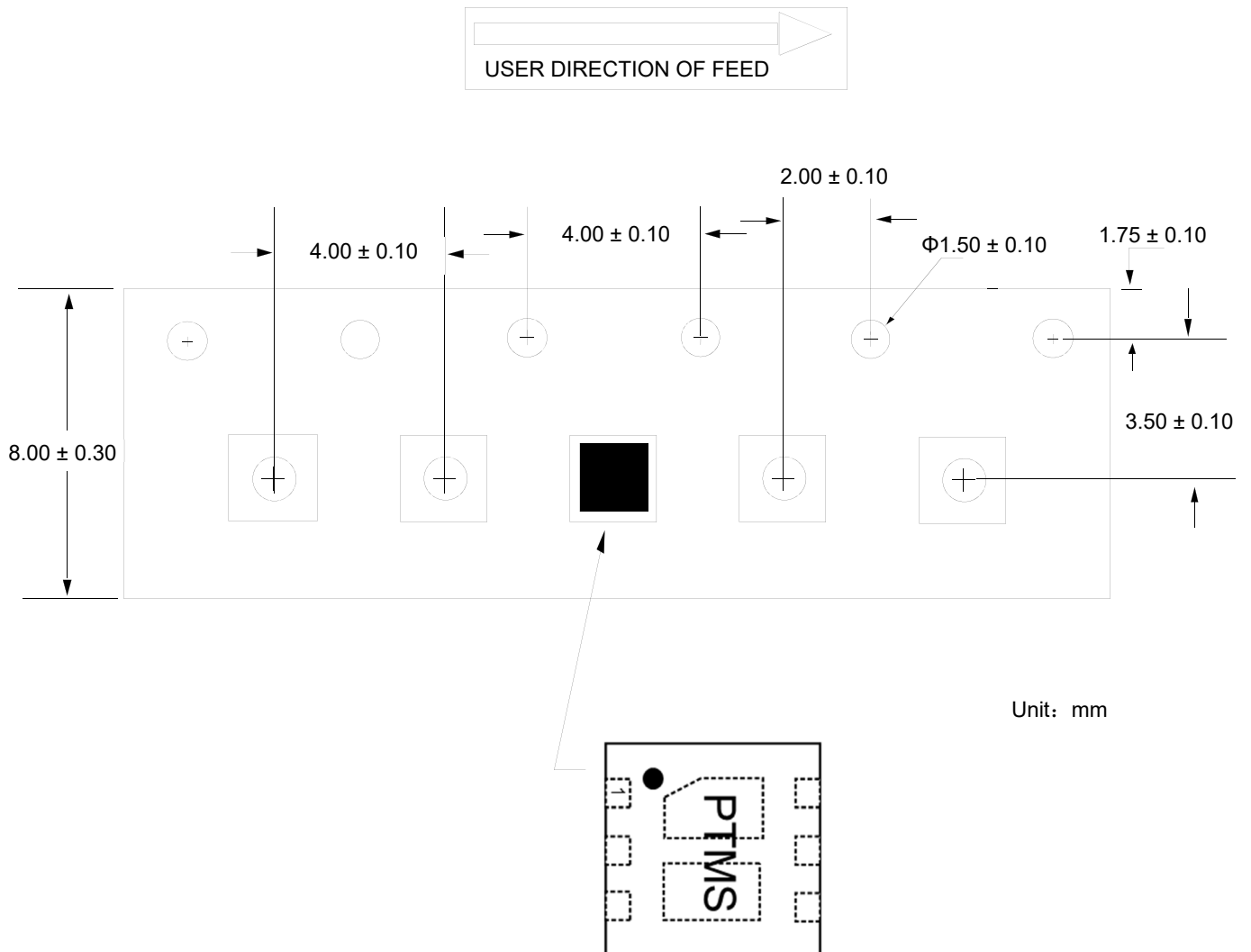
Marking information




Ordering information

Device	Package	Reel	Shipping
PNMT6N1C	DFN2020-6L	7"	3000 / Tape & Reel

Load with information




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