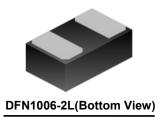


PSBD2FD40V1HN

Schottky Barrier Diode

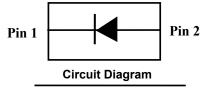
Feature

- ➤ Ultra Small mold type. (DFN1006-2L)
- Low I_R
- > High reliability.



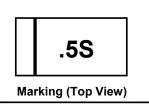
Applications

Low current rectification



Construction

Silicon epitaxial planar



Mechanical Characteristics

- Mounting position: Any
- Device meets MSL 1 requirements
- ➤ Qualified max reflow temperature:260°C
- DFN1006-2L without plating

Electrical characteristics per line@25°C

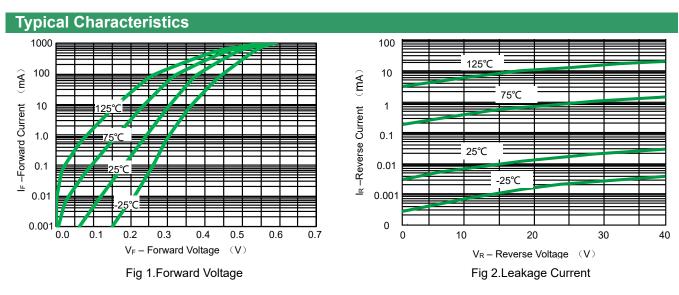
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	-	0.35	0.40	V	I _F =100mA
Forward voltage	VF	-	0.45	0.50	V	I _F =500mA
Forward voltage	V _F	-	0.55	0.60	V	I _F =1A
Reverse current	I _R	-	-	3	μΑ	V _R =10V
Reverse current	IR	-	-	0.1	mA	V _R =40V
Junction Capacitance	C _j	-	90	-	pF	V _R =0V f=1MHz

Absolute maximum rating@25℃

Parameter	Symbol	limits	Unit
Reverse voltage(repetitive peak)	V _{RM}	45	V
Reverse voltage (DC)	V _R	40	V
Average rectified forward current	lo	1	А
Non-Repetitive Peak Forward Surge Current (8.3ms single half sine-wave superimposed on rated load)	lfsm	5	А

Parameter	Symbol	limits	Unit
Repetitive peak forward current ($t_{p}\!\leq\!1ms;\delta\!\leq\!0.25)$	IFRM	5	А
Power Dissipation	PD	400	mW
Thermal resistance ¹⁾	Reja	310	°C/W
Operating Junction temperature Range	Tj	-55 to 125	°C
Storage temperature	T _{stg}	-55 to 125	℃

Note1:FR-4 PCB,minimum recommended pad layout.



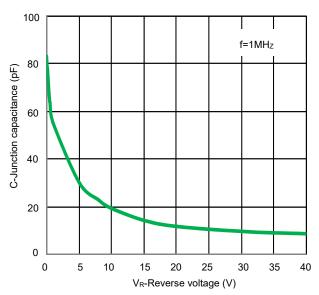
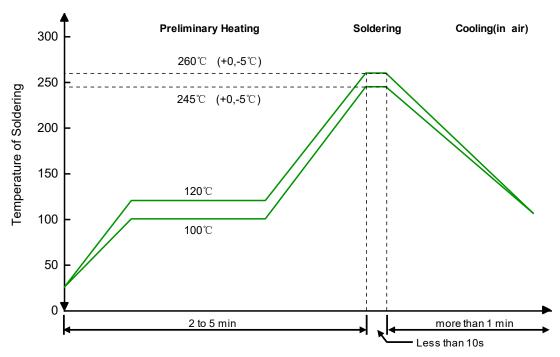


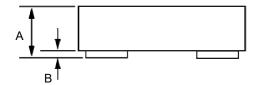
Fig 3. Capacitance vs. Reveres voltage

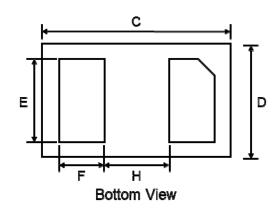
Solder Reflow Recommendation



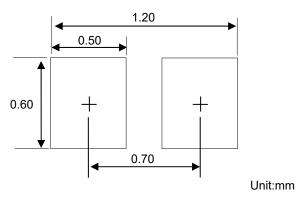
Remark: Pb free for 260°C; Pb for 245°C.

Product dimension (DFN1006-2L)





Dim	Millim	neters	Inches		
	MIN	MAX	MIN	MAX	
Α	0.40	0.55	0.016	0.022	
В	0.00	0.05	0.000	0.002	
С	0.90	1.10	0.035	0.043	
D	0.55	0.65	0.022	0.026	
E	0.35	0.55	0.014	0.022	
F	0.15	0.30	0.006	0.012	
Н	0.40 Typ.		0.015 Typ.		

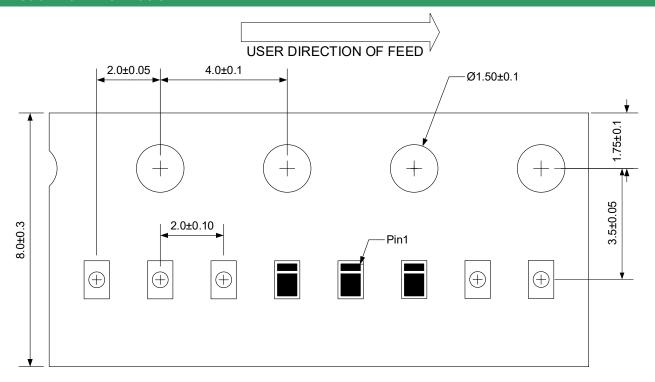


Suggested PCB Layout

Ordering information

Device	Package	Reel	Shipping
PSBD2FD40V1HN	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

Load with information



Unit:mm

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