

Description

The PSMTOF11R4H uses split gate trench technology to provide excellent $R_{DS(ON)}$ low gate charge. This device is suitable for power management and high efficiency applications at high switching frequencies applications.

MOSFET Product Summary		
$V_{DS}(V)$	$R_{DS(on)}(m\Omega)$	$I_D(A)$
100	4.1@ $V_{GS} = 10V$	165

Feature

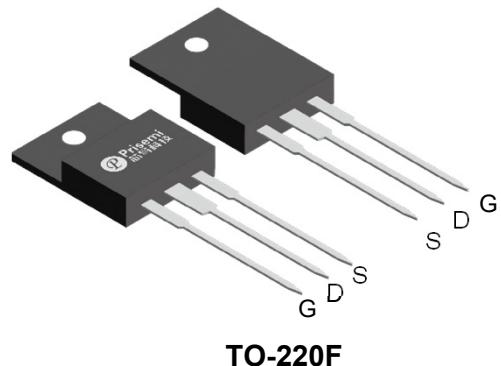
- Low $R_{DS(ON)}$ - Ensures On-State Losses are Minimized
- Excellent $Q_{gd} \times R_{DS(ON)}$ Product(FOM)
- Advanced Technology for DC-DC Converts
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% UIS (Avalanche) Rated
- Lead-Free Finish ; RoHS Compliant
- Halogen and Antimony Free. "Green" Device

Applications

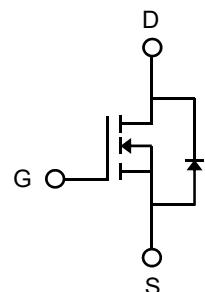
- PWM applications
- Load switch
- Power management
- DC-DC Converters
- Wireless Chargers

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ¹⁾	I_D	165	A
$T_C=100^\circ C$		105	
Pulsed Drain Current ²⁾	I_{DM}	660	A
Total Power Dissipation ³⁾	P_D	235.8	W
Avalanche Current ⁴⁾	I_{AS}	123	A
Avalanche Energy ⁴⁾	E_{AS}	766	mJ
Thermal Resistance , Junction-case ⁵⁾	$R_{\theta JC}$	0.53	°C/W
Thermal Resistance Junction-to-Ambient ⁶⁾	$R_{\theta JA}$	50.3	°C/W
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	°C



TO-220F



Circuit Diagram



Marking (Top View)

N-Channel MOSFET

PSMTOF11R4H

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100	113	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$	-	-	1.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	3.2	4.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 50A$	-	4.1	4.5	$m\Omega$
Dynamic Characteristics⁷⁾						
Input Capacitance	C_{iss}	$V_{DS} = 50V, V_{GS} = 0V, f = 1.0MHz$	-	5988	-	pF
Output Capacitance	C_{oss}		-	809	-	
Reverse Transfer Capacitance	C_{rss}		-	28	-	
Switching Characteristics⁷⁾						
Turn-on Delay Time	$t_{d(on)}$	$V_{DS} = 50V, V_{GS} = 10V, R_G = 10\Omega, I_D = 15A$	-	44	-	ns
Turn-on Rise Time	t_r		-	70	-	
Turn-Off Delay Time	$t_{d(off)}$		-	125	-	
Turn-Off Fall Time	t_f		-	70	-	
Total Gate Charge	Q_g	$V_{DS} = 50V, V_{GS} = 10V, I_D = 50A$	-	101	-	nC
Gate-Source Charge	Q_{gs}		-	30	-	
Gate-Drain Charge	Q_{gd}		-	27	-	
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	-	2.1	-	Ω
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = 1A$	-	0.7	1.4	V

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. Repetitive Rating: Pulse width limited by maximum junction temperature($T_{J_Max}=150^{\circ}C$).
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. This single-pulse measurement was taken under the following condition [$L=100\mu H, V_{GS}=10V, V_{DS}=100V$]while it's value is limited by $T_{J_Max}=150^{\circ}C$
5. Device mounted on infinite heatsink
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Guaranteed by design, not subject to production

Typical Characteristics

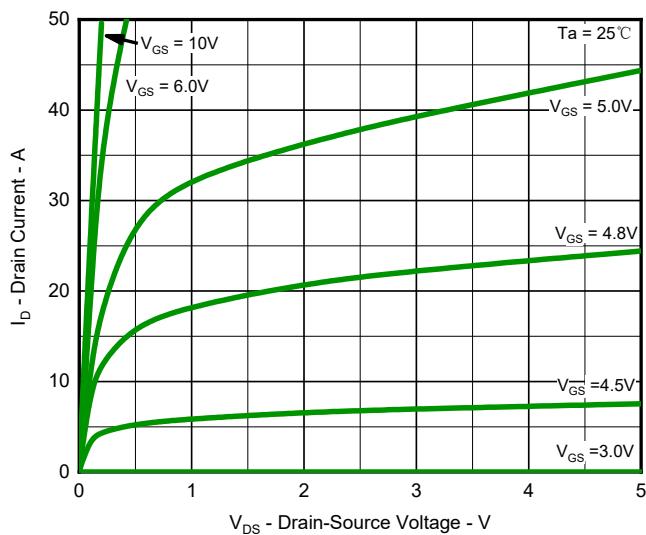


Fig.1 Output Characteristics

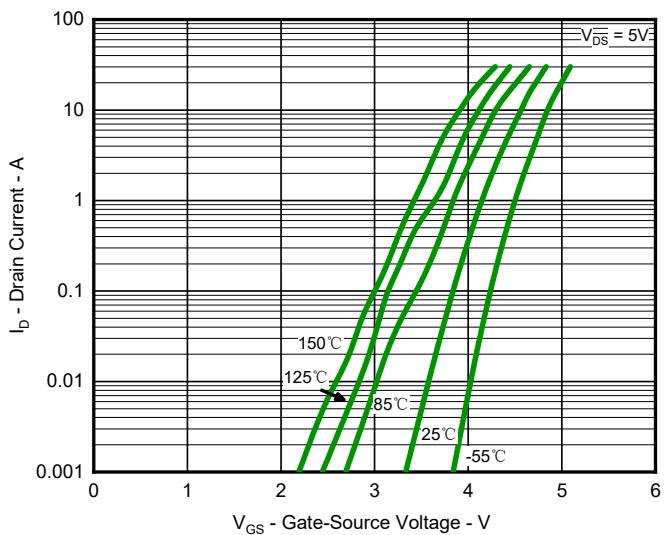


Fig.2 Typical Transfer Characteristic

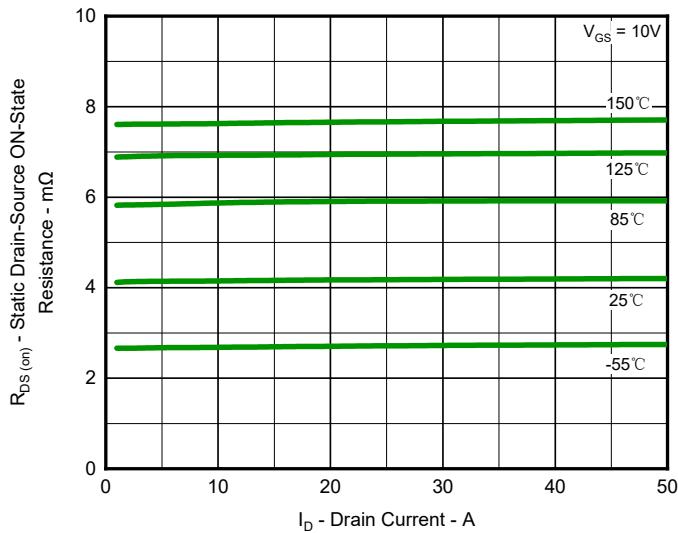


Fig.3 Typical On-Resistance vs. Drain Current and Temperature

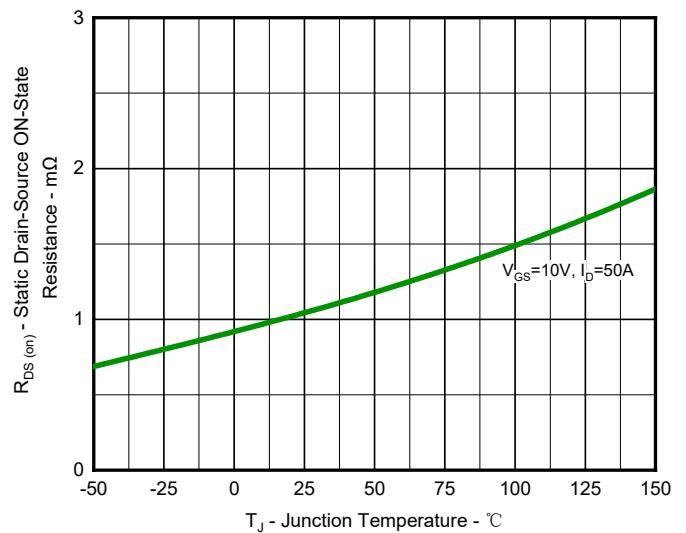


Fig.4 On-Resistance Variation with Temperature

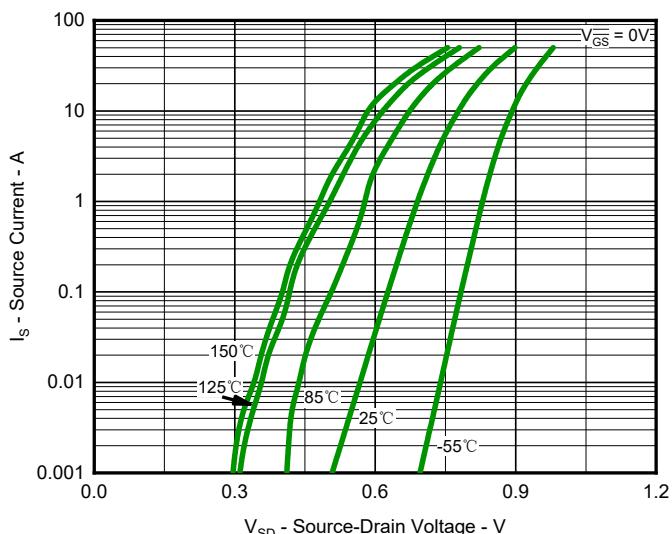


Fig.5 Diode Forward Voltage vs. Current

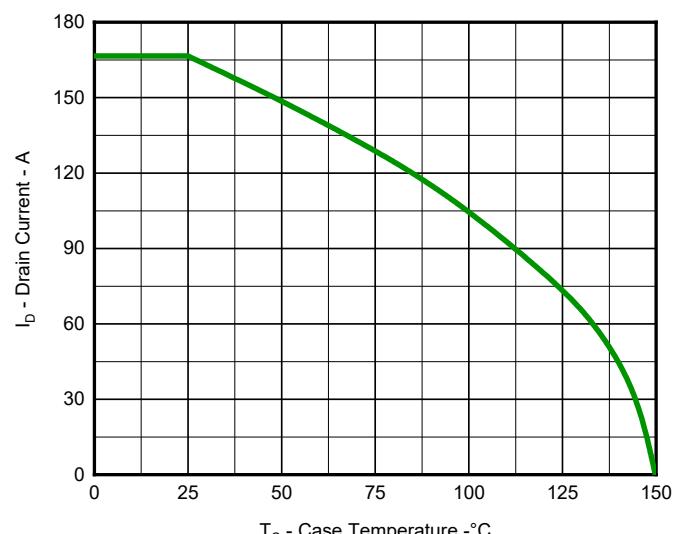


Fig.6 Maximum Drain Current vs. Case Temperature

N-Channel MOSFET

PSMTOF11R4H

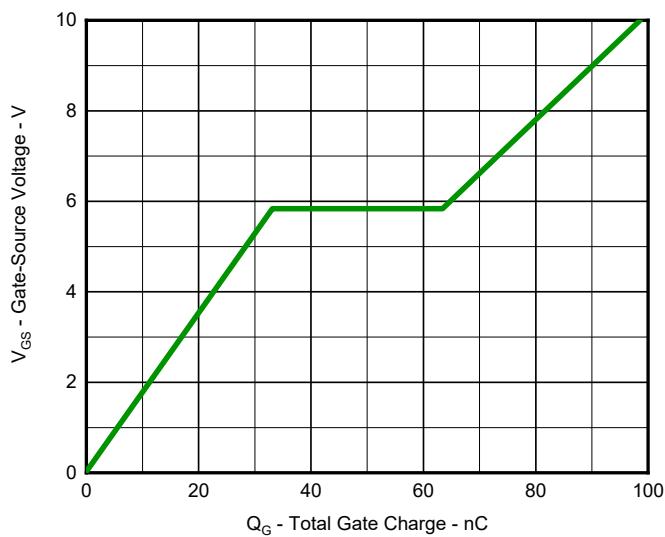


Fig.7 Gate Charge Characteristics

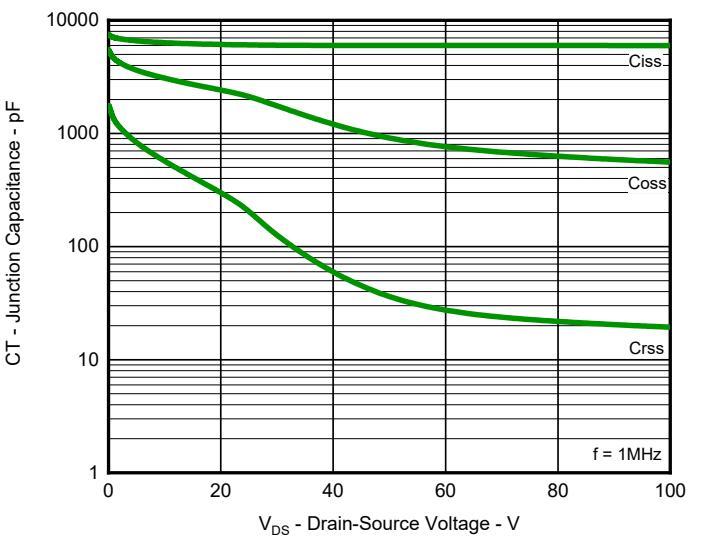


Fig.8 Typical Junction Capacitance

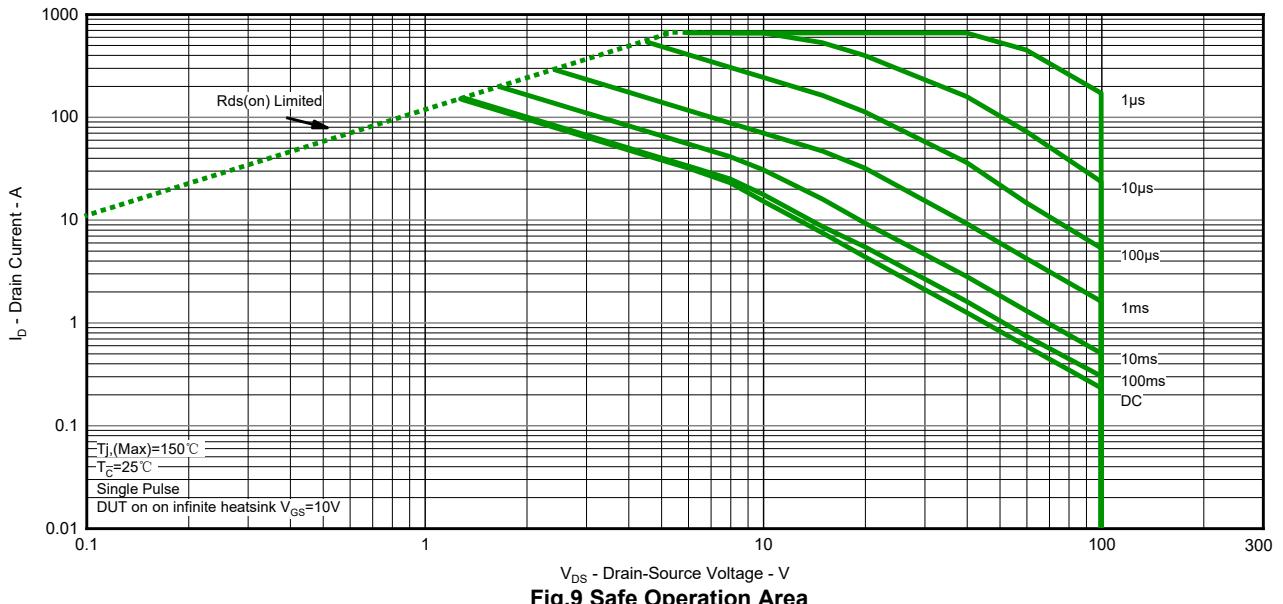


Fig.9 Safe Operation Area

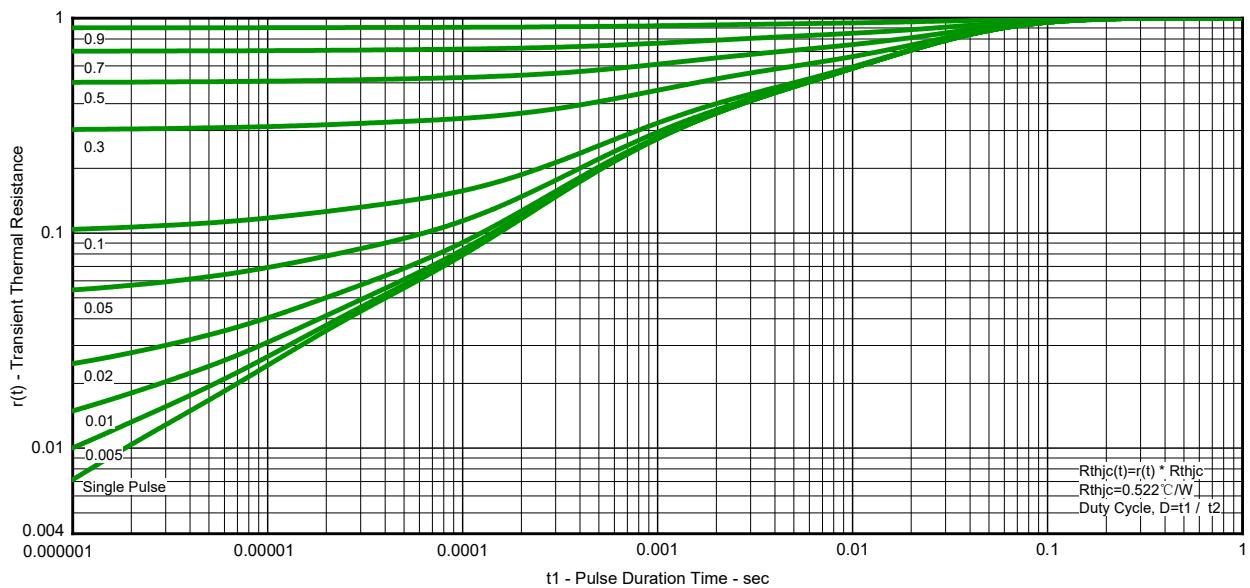
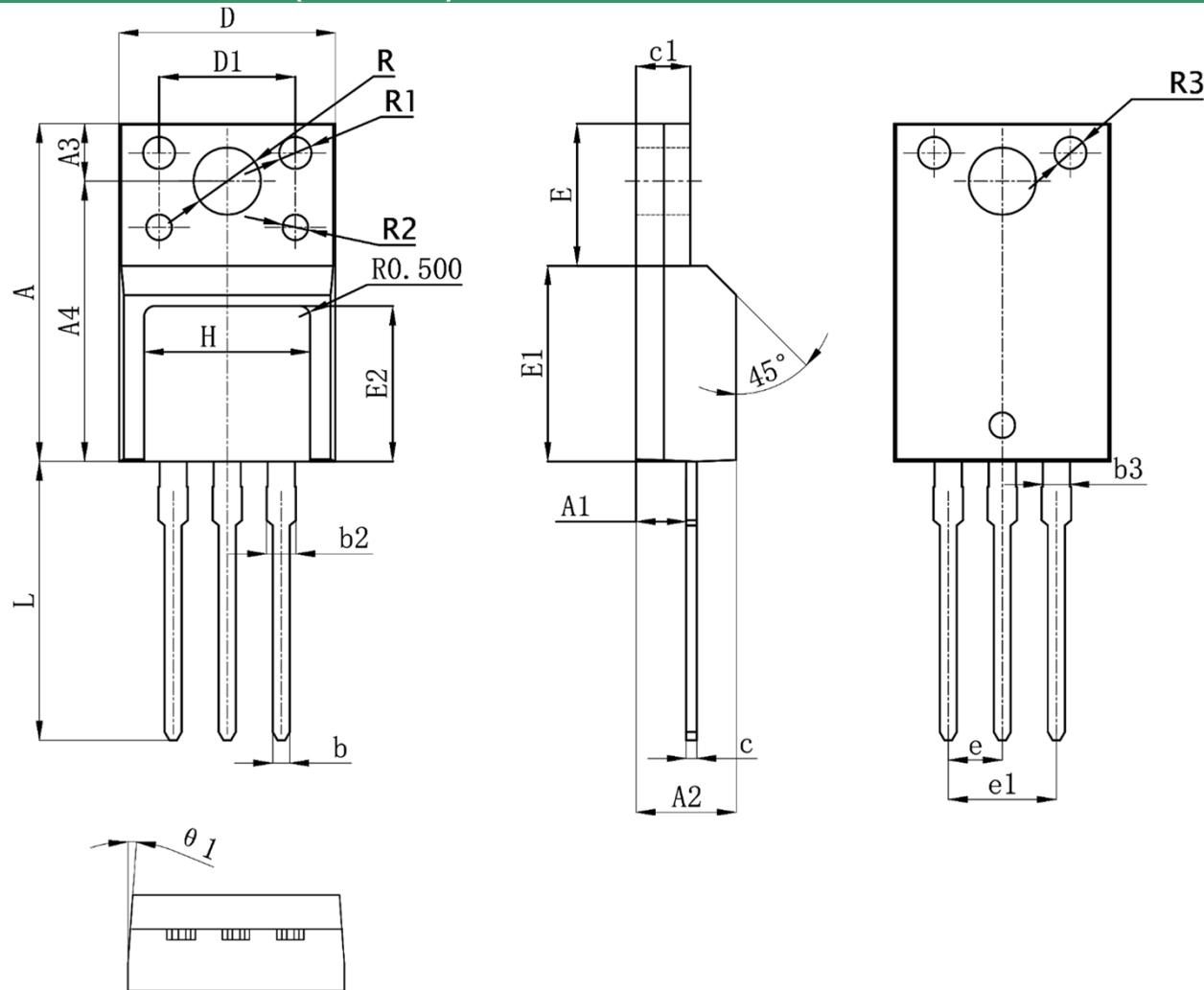


Fig.10 Transient Thermal Resistance

Product Dimension (TO-220F)



Dim	Millimeters		Inches		Dim	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	15.67	16.07	0.617	0.633	E	6.48	6.88	0.255	0.271
A1	2.15	2.55	0.085	0.100	E1	8.99	9.39	0.354	0.370
A2	4.50	4.90	0.177	0.193	E2	7.10	7.50	0.280	0.295
A3	3.10	3.50	0.122	0.138	e	2.54 BSC		0.100 BSC	
A4	12.27	12.87	0.483	0.507	e1	5.08 BSC		0.200 BSC	
b	0.77	0.83	0.030	0.033	L	13.14	13.54	0.517	0.533
b2	1.20	1.40	0.047	0.055	R	3.10	3.50	0.122	0.138
b3	1.20 BSC		0.047 BSC		R1	1.50 Ref.		0.059 Ref.	
c	0.40	0.60	0.016	0.024	R2	1.20 Ref.		0.047 Ref.	
c1	2.44	2.64	0.096	0.104	R3	1.50 Ref.		0.059 Ref.	
D	9.86	10.46	0.388	0.412	H	7.60	8.00	0.299	0.315
D1	6.90	7.10	0.272	0.280	θ1	4°	5°	4°	5°

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