

N-Channel MOSFET

Description

The PSMTO10R12D uses split gate trench technology to provide excellent $R_{DS(ON)}$ and low gate charge.

This device is suitable for power management and high efficiency applications at high switching frequencies applications.

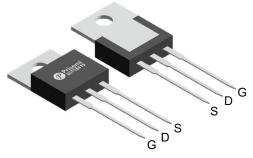
MOSFET Product Summary					
V _{DS} (V)	I _D (A)				
100	8.6@ V _{GS} = 10V	68			
	10.7@ V _{GS} = 4.5V	00			

Feature

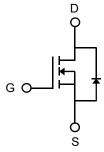
- > High Power and current handing capability
- > Lead free product is acquired
- > Surface Mount Package

Applications

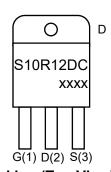
- > PWM applications
- ➤ Load Switch
- Power Management
- > DC-DC Converters
- Wireless Chargers



TO-220



Circuit Diagram



Marking (Top View)

Absolute maximum rating@25°C

Rating	Symbol	Value	Units		
Drain-Source Voltage	V _{DS}	100	V		
Gate-Source Voltage	V _{GS}	±20	V		
Drain Current-Continuous ¹⁾	T _C =25°C		68	۸	
Diam Current-Continuous 7	T _C =100°C	l _D	43	A	
Pulsed Drain Current ²⁾	I _{DM}	270	Α		
Total Power Dissipation ³⁾	P_{D}	92.6	W		
Avalanche Current ⁴⁾	I _{AS}	25	Α		
Avalanche Energy ⁴⁾	E _{AS}	158	mJ		
Thermal Resistance , Junction-to-Case ⁵⁾	$R_{\theta JC}$	1.4	°C/W		
Thermal Resistance Junction-to-Ambient ⁶	$R_{\theta JA}$	44.5	°C/W		
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	℃		

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units			
Off Characteristics									
Drain-Source Breakdown Voltage	BV _{DSS}	V_{DSS} $V_{GS} = 0V, I_D = 250 \mu A$		-	-	V			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V,V _{GS} = 0V	-	-	1.0	μA			
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 20 V, V_{DS} = 0 V$	-	-	±100	nA			
On Characteristics									
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.8	2.2	V			
Dunin Course On State Besisten	Б	V _{GS} = 10V,I _D = 20A	-	8.6	10.6	mΩ			
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 4.5V,I _D = 15A	-	- 10.7 13.5					
Dynamic Characteristics ⁷⁾									
Input Capacitance	C _{iss}		-	1244	-	pF			
Output Capacitance	C _{oss}	$V_{DS} = 50V, V_{GS} = 0V,$ f = 1.0MHz	-	395	-				
Reverse Transfer Capacitance	C _{rss}		-	14	-				
Switching Characteristics ⁷⁾									
Turn-on Delay Time	t _{d(on)}		-	5.7	-				
Turn-on Rise Time	t _r	V _{DS} = 50V, V _{GS} = 10V,	-	4.0	-	ns			
Turn-Off Delay Time	t _{d(off)}	$I_D = 20A, R_{GEN} = 3\Omega$	-	20.7	-				
Turn-Off Fall Time	t _f		-	10.5	-				
Total Gate Charge	Q _g		-	21.4	-	nC			
Gate-Source Charge	Q_{gs}	$V_{DS} = 50V, V_{GS} = 10V,$ $I_{D} = 20A$	-	3.2	-				
Gate-Drain Charge	$Q_{\rm gd}$		-	8.0	-				
Gate Resistance	R_g	V _{GS} =0V,V _{DS} =0V,f=1MHz	-	1.1	-	Ω			
Drain-Source Diode Characteristics									
Diode Forward Voltage	V _{SD}	V _{GS} = 0V,I _S = 20A	0.5	0.87	1.0	V			

Notes

Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.

^{2.} Repetitive Rating: Pulse width limited by maximum junction temperature(T_{J_Max}=150°C).

Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

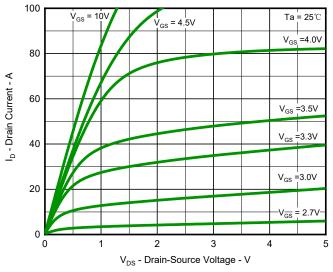
^{4.} This single-pulse measurement was taken under the following condition [L=0.5mH,V_{GS}=10V,V_{DS}=80V]while it's value is limited by T_{J_Max}=150°C.

Device mounted on infinite heatsink.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

^{7.} Guaranteed by design, not subject to production.

Typical Characteristics



100 10 I_D - Drain Current - A 0.1 -55°C V_{GS} - Gate-Source Voltage - V

Fig.1 Output Characteristics

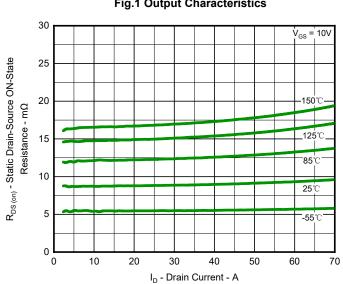


Fig.2 Typical Transfer Characteristic

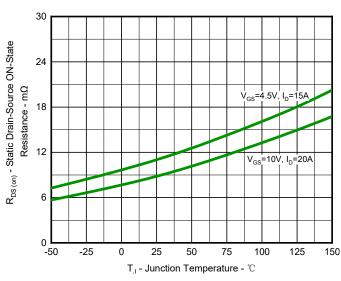


Fig.3 Typical On-Resistance vs Drain Current and Temperature

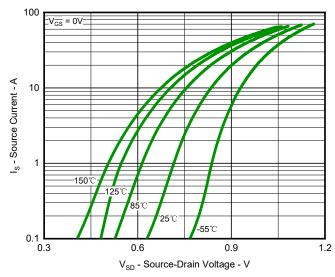


Fig.4 On-Resistance Variation with Temperature

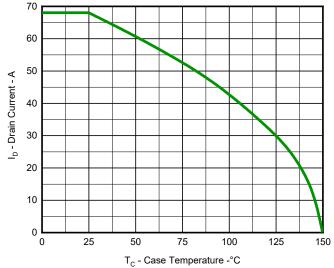


Fig.5 Diode Forward Voltage vs. Current

Fig.6 Maximum Drain Current vs. Case Temperature

N-Channel MOSFET

PSMTO10R12D

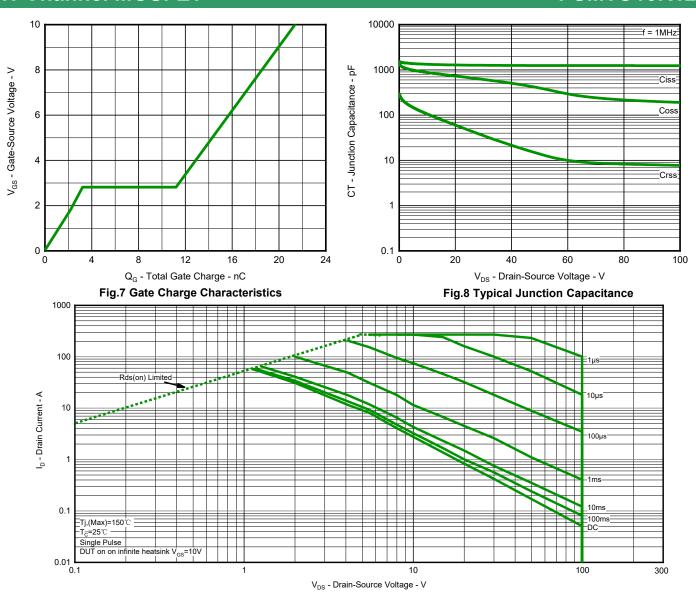


Fig.9 Safe Operation Area

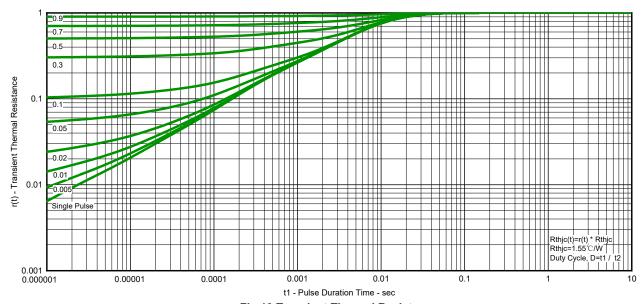
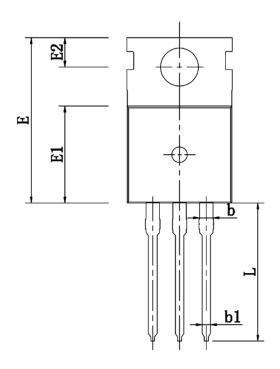
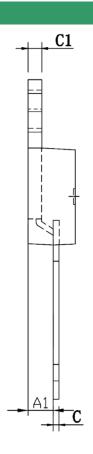
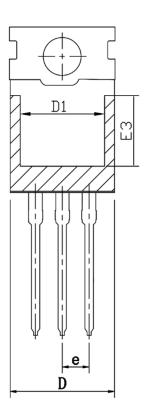


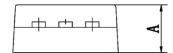
Fig.10 Transient Thermal Resistance

Product Dimension (TO-220)









Dim	Millimeters		Inches		Dim	Millimeters		Inches	
	Min	Max	Min	Max	Dim	Min	Max	Min	Max
Α	4.40	4.60	0.173	0.181	D1	7.942	7.97	0.313	0.314
A1	2.00	3.00	0.079	0.118	Е	15.20	15.75	0.598	0.620
b	1.20	1.36	0.047	0.054	E1	9.00	9.40	0.354	0.370
b1	0.70	0.90	0.028	0.035	E2	2.60	2.90	0.102	0.114
С	0.48	0.53	0.019	0.021	E3	7.247	7.587	0.285	0.299
C1	1.28	1.32	0.050	0.052	е	2.54	Ref.	0.354	0.362
D	9.80	10.20	0.386	0.402	L	13.00	13.40	0.512	0.528

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