

## Description

The PSM8PNS04R8L uses split gate trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. This device is suitable for power management and high efficiency applications at high switching frequencies applications.

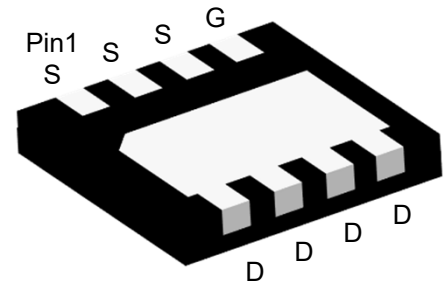
MOSFET Product Summary		
$V_{DS}(V)$	$R_{DS(on)}(m\Omega)(Typ)$	$I_D(A)$
40	5.3@ $V_{GS} = 10V$	60
	7.5@ $V_{GS} = 4.5V$	

## Feature

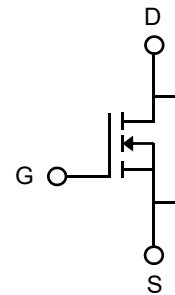
- Low  $R_{DS(ON)}$  - Ensures On-State Losses are Minimized
- Excellent  $Q_{gd} \times R_{DS(ON)}$  Product(FOM)
- Advanced Technology for DC-DC Converts
- Small Form Factor Thermally Efficient Package  
Enables Higher Density End Products
- 100% UIS (Avalanche) Rated
- Lead-Free Finish ; RoHS Compliant
- Halogen and Antimony Free. "Green" Device

## Applications

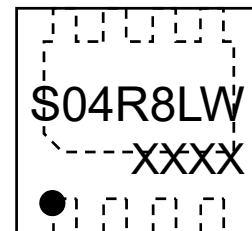
- PWM applications
- Load switch
- Power management
- DC-DC Converters
- Wireless Chargers



**DFN3030-8L  
(Bottom View)**



**Circuit Diagram**



Pin1

**Marking (Top View)**

## Absolute maximum rating@25°C

Rating		Symbol	Value	Units
Drain-Source Voltage		$V_{DS}$	40	V
Gate-Source Voltage		$V_{GS}$	±20	V
Drain Current-Continuous <sup>1)</sup>	$T_C=25^\circ C$	$I_D$	60	A
	$T_C=100^\circ C$		38	
Pulsed Drain Current <sup>2)</sup>		$I_{DM}$	240	A
Total Power Dissipation <sup>3)</sup>		$P_D$	37.9	W
Avalanche Current <sup>4)</sup>		$I_{AS}$	16	A
Avalanche Energy <sup>4)</sup>		$E_{AS}$	72	mJ
Thermal Resistance Junction-to-Ambient <sup>6)</sup>		$R_{\theta JA}$	3.3	°C/W
Thermal Resistance Junction-to-Case <sup>5)</sup>		$R_{\theta JC}$	46.6	°C/W
Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	°C

## Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	40	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 40V, V_{GS} = 0V$	-	-	1.0	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.6	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	5.3	7.0	m $\Omega$
		$V_{GS} = 4.5V, I_D = 15A$	-	7.5	10	
<b>Dynamic Characteristics<sup>7)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 20V, V_{GS} = 0V$ $f = 1MHz$	-	786	-	pF
Output Capacitance	$C_{oss}$		-	158	-	
Reverse Transfer Capacitance	$C_{rss}$		-	21	-	
<b>Switching Characteristics<sup>7)</sup></b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DS} = 20V, V_{GS} = 10V,$ $R_G = 6\Omega, I_D = 15A$	-	3.0	-	ns
Turn-on Rise Time	$t_r$		-	2.0	-	
Turn-Off Delay Time	$t_{d(off)}$		-	14	-	
Turn-Off Fall Time	$t_f$		-	3.5	-	
Total Gate Charge	$Q_g$	$V_{DS} = 20V, V_{GS} = 10V,$ $I_D = 15A$	-	10	-	nC
Gate-Source Charge	$Q_{gs}$		-	2.2	-	
Gate-Drain Charge	$Q_{gd}$		-	2.4	-	
Gate Resistance	$R_g$	$f = 1MHz, \text{Open Drain}$	-	2.3	-	$\Omega$
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = 1A$	-	0.7	1.2	V

## Notes:

1. Computed continuous current assumes the condition of  $T_{J\_Max}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. Repetitive Rating: Pulse width limited by maximum junction temperature ( $T_{J\_Max} = 150^\circ C$ ).
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. This single-pulse measurement was taken under the following condition [ $L = 0.5mH, V_{GS} = 10V, V_{DS} = 40V$ ] while its value is limited by  $T_{J\_Max} = 150^\circ C$ .
5. Device mounted on infinite heatsink.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1 inch square copper pad layout.
7. Guaranteed by design, not subject to production.

Typical Characteristics

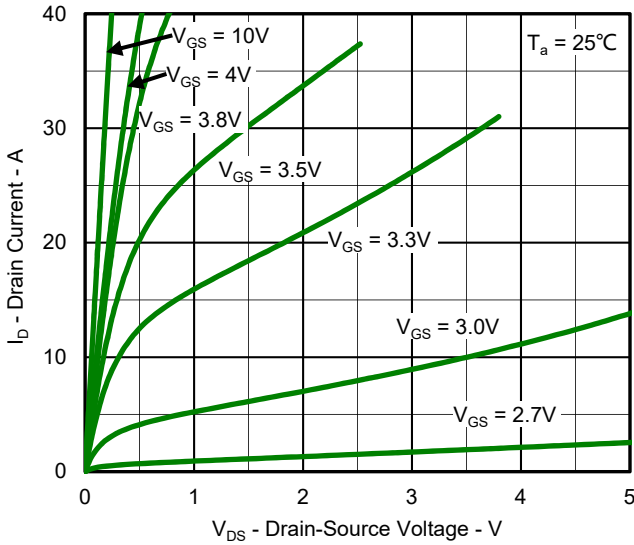


Fig.1 Output Characteristics

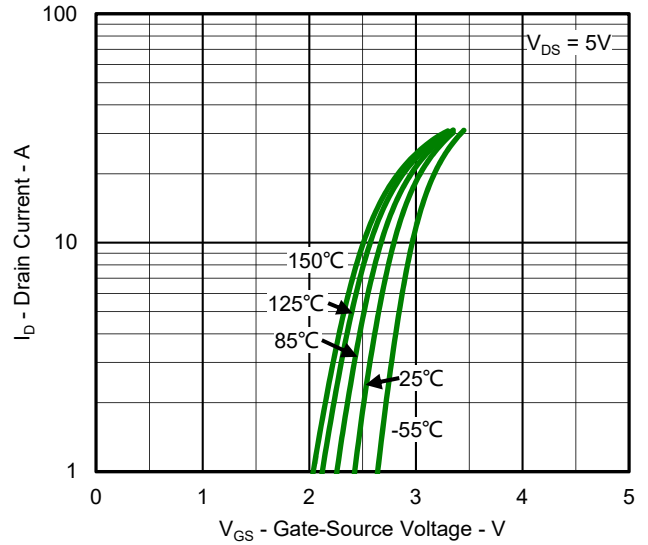


Fig.2 Typical Transfer Characteristic

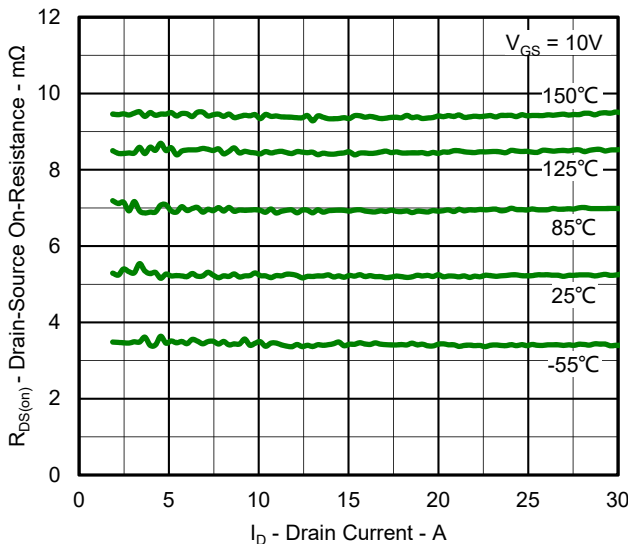


Fig.3 Typical On-Resistance vs Drain Current and Temperature

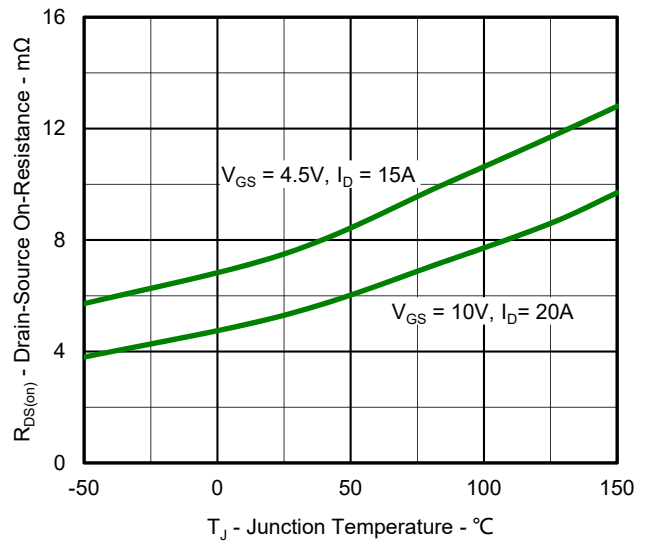


Fig.4 On-Resistance Variation with Temperature

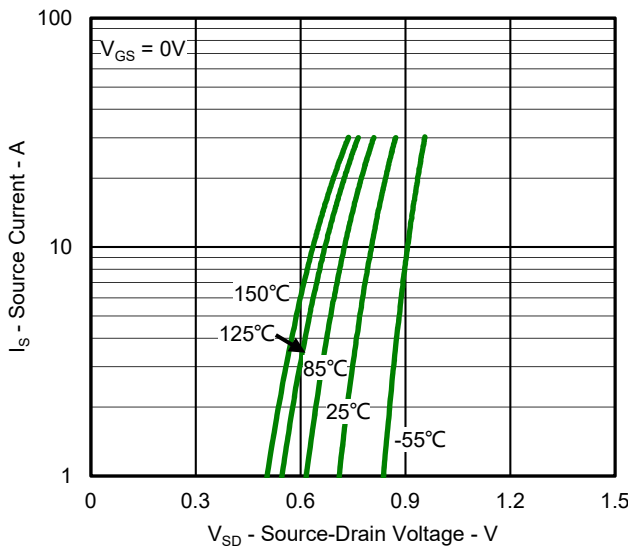


Fig.5 Diode Forward Voltage vs. Current

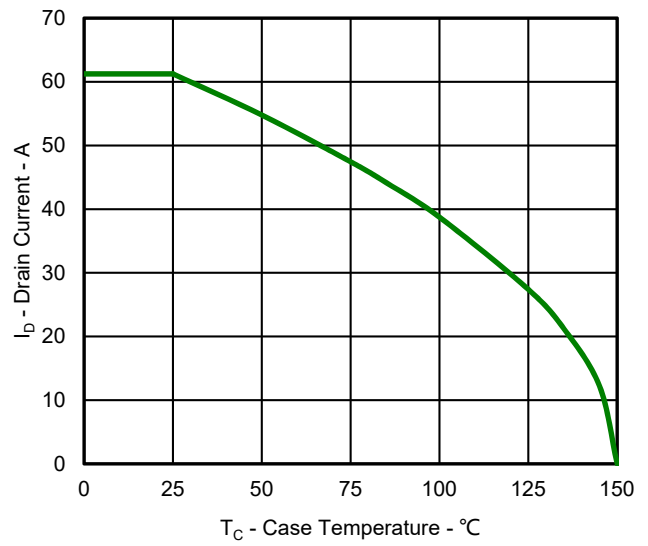
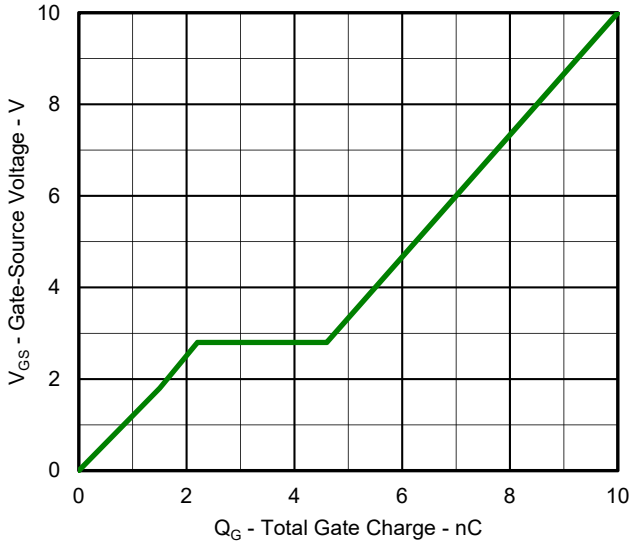
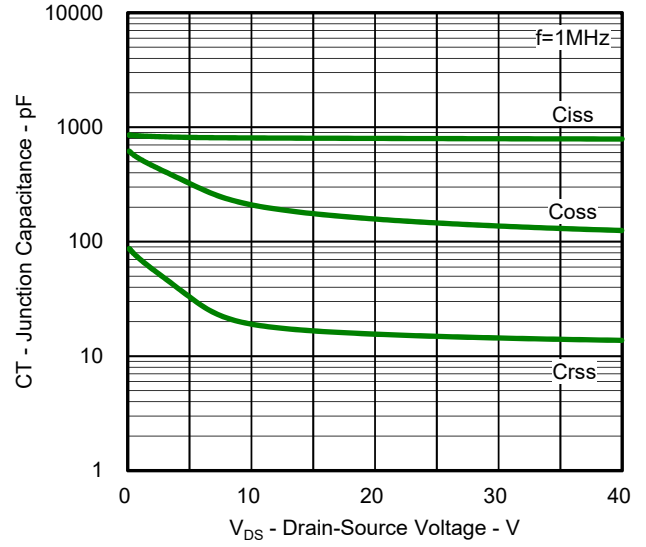


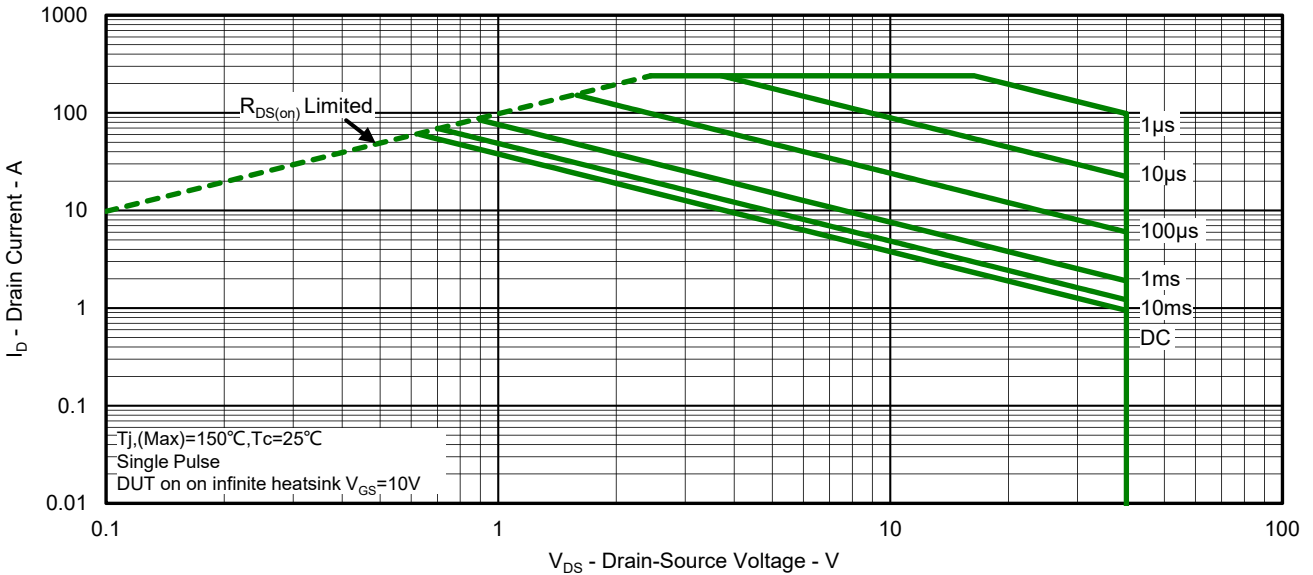
Fig.6 Maximum Drain Current vs. Case Temperature



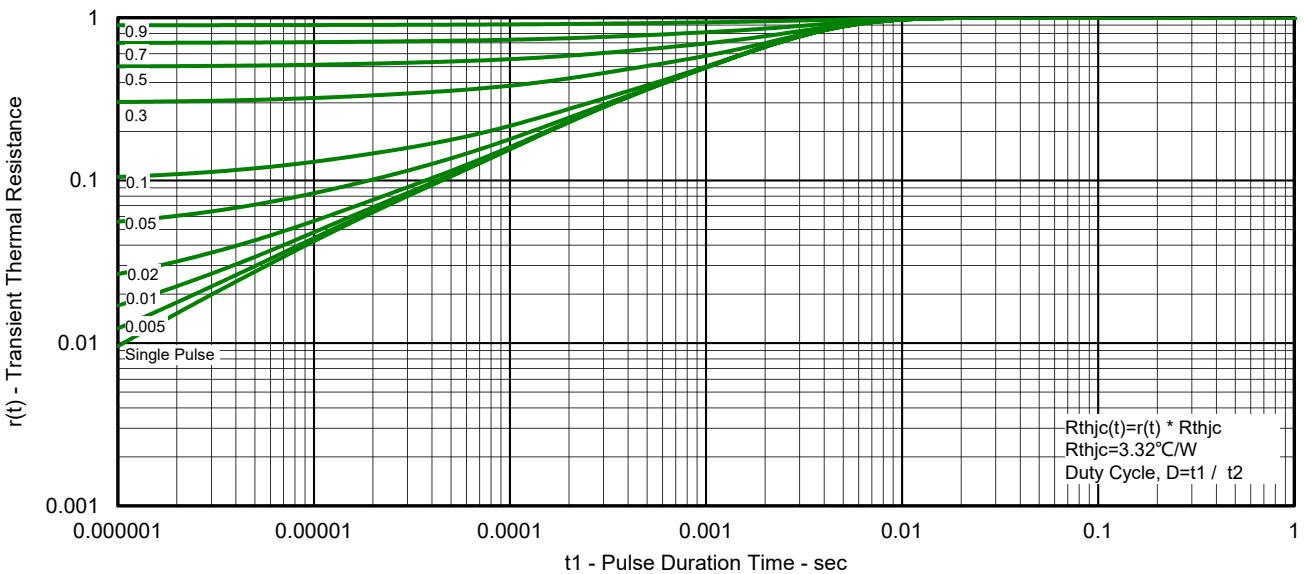
**Fig.7 Gate Charge Characteristics**



**Fig.8 Typical Junction Capacitance**

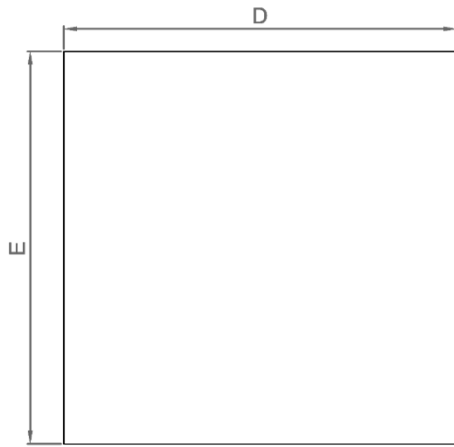


**Fig.9 Safe Operation Area**

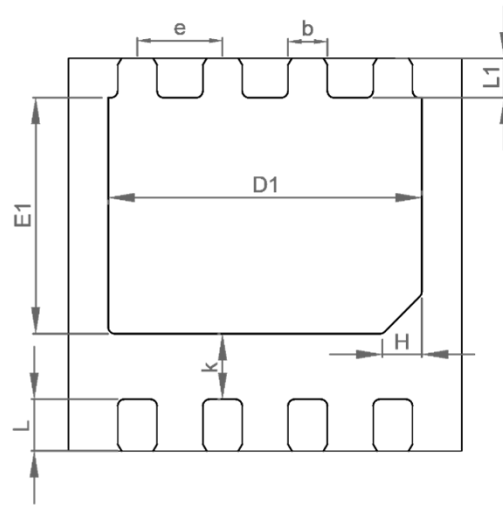


**Fig.10 Transient Thermal Resistance**

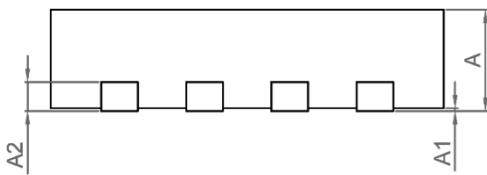
Product Dimension (DFN3030-8L)



TOP VIEW



BOTTOM VIEW



SIDE VIEW

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
A2	0.20 Ref.		0.008 Ref.	
D	2.90	3.10	0.114	0.122
E	2.90	3.10	0.114	0.122
E1	1.70	1.90	0.067	0.075
D1	2.35	2.55	0.093	0.100
e	0.65 BSC		0.026 BSC	
L	0.35	0.45	0.014	0.018
h	0.30 Ref.		0.012 Ref.	
k	0.50 Ref.		0.020 Ref.	
L1	0.25	0.35	0.010	0.014

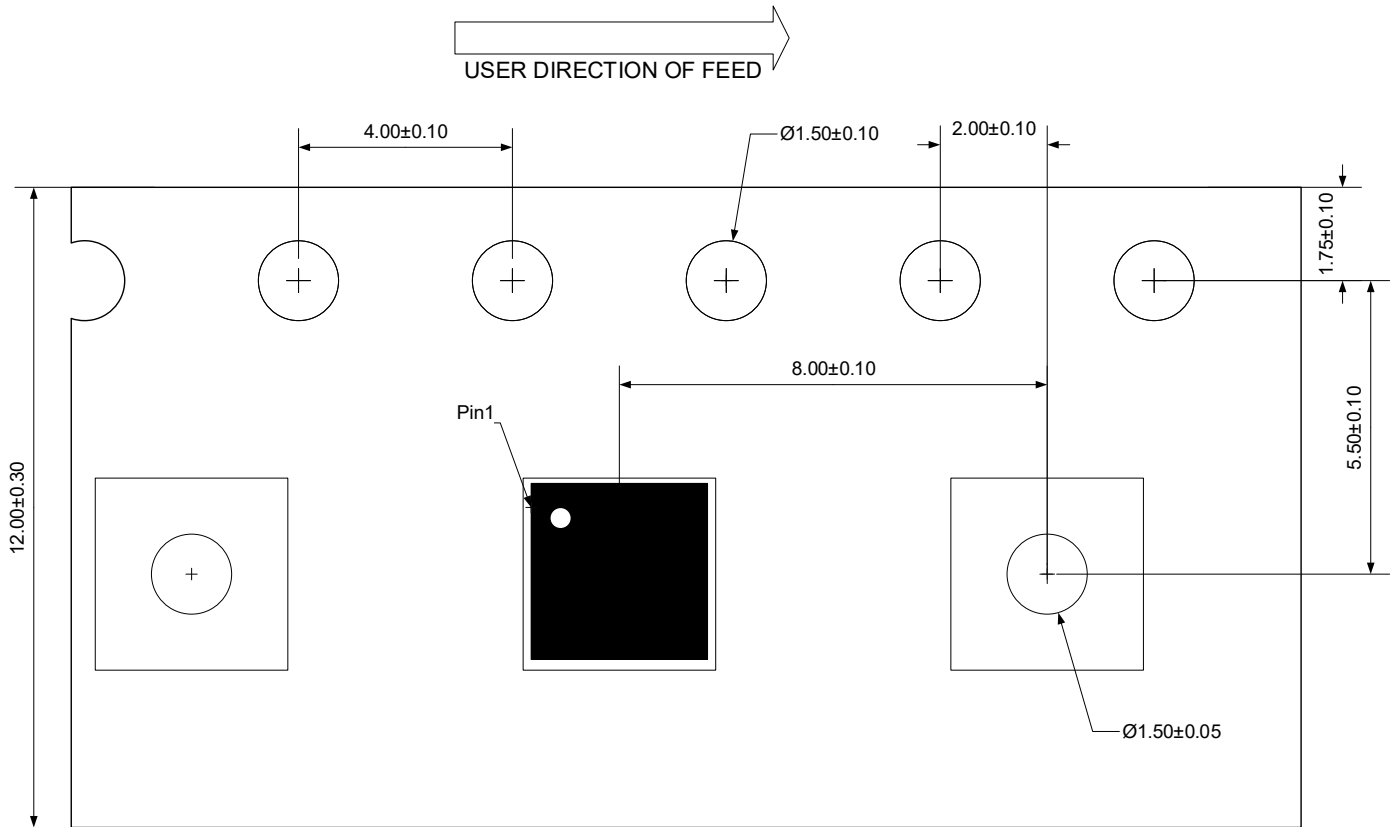
# N-Channel MOSFET

# PSM8PNS04R8L

## Ordering Information


Package	Reel	Shipping
DFN3030-8L	13"	5000 / Tape & Reel

## Load With Information



Unit:mm


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