

Description

The PPM8PN15V50 uses advanced trench technology to provide excellent $R_{DS(on)}$, low gate charge. This device is suitable for use as a load switch or in PWM applications.

MOSFET Product Summary

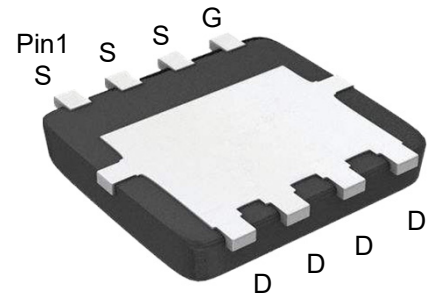
$V_{DS}(V)$	$R_{DS(on)}(m\Omega)(Typ)$	$I_D(A)$
-15	6.7@ $V_{GS} = -4.5V$	-54
	10@ $V_{GS} = -2.5V$	

Feature

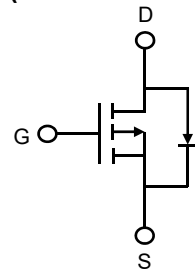
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Applications

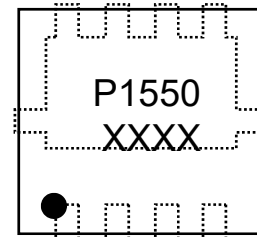
- PWM applications
- Load switch
- Power management
- DC-DC Converters
- Wireless Chargers



**PDFN3333-8L
(Bottom View)**



Circuit Diagram



Pin1

P1550 :Device Code
XXXX :Traceability Code

Marking (Top View)

Absolute maximum rating@25°C

Rating		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	-15	V
Gate-Source Voltage		V_{GS}	± 12	V
Drain Current-Continuous ¹⁾	$T_C = 25^\circ C$	I_D	-54	A
	$T_C = 100^\circ C$		-34	
Pulsed Drain Current ²⁾		I_{DM}	-216	A
Total Power Dissipation ³⁾		P_D	39	W
Avalanche Current ⁴⁾		I_{AS}	-29	A
Avalanche Energy ⁴⁾		E_{AS}	42	mJ
Thermal Resistance , Junction-to-Case ⁵⁾		$R_{\theta JC}$	3.2	$^\circ C/W$
Thermal Resistance , Junction-to-Ambient ⁵⁾		$R_{\theta JA}$	47.3	$^\circ C/W$
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = -250μA	-15	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -15V, V _{GS} = 0V	-	-	-1.0	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ± 12V, V _{DS} = 0V	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-0.45	-0.65	-1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-5A	-	6.7	9	mΩ
		V _{GS} =-2.5V, I _D =-4A	-	10	13	
Dynamic Characteristics ⁶⁾						
Input Capacitance	C _{iss}	V _{DS} = -8V, V _{GS} = 0V f= 1MHz	-	1940	-	pF
Output Capacitance	C _{oss}		-	336	-	
Reverse Transfer Capacitance	C _{rss}		-	307	-	
Switching Characteristics ⁶⁾						
Turn-on Delay Time	t _{d(on)}	V _{DS} =- 8V, V _{GS} = -4.5V, R _G = 10Ω, I _D = -5A	-	8	-	ns
Turn-on Rise Time	t _r		-	29	-	
Turn-Off Delay Time	t _{d(off)}		-	136	-	
Turn-Off Fall Time	t _f		-	85	-	
Total Gate Charge	Q _g	V _{DS} = -12V, V _{GS} = -4.5V, I _D = -5A	-	23.7	-	nC
Gate-Source Charge	Q _{gs}		-	2.2	-	
Gate-Drain Charge	Q _{gd}		-	6.4	-	
Gate Resistance	R _g	f=1MHz, Open Drain	-	8.2	-	Ω
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -4A	-	-0.7	-1.2	V
Reverse Recovery Time	t _{rr}	I _F =-5A, di/dt=100A/us, V _R =-10V	-	39	-	ns
Reverse Recovery Charge	Q _{rr}		-	20	-	nC

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. Repetitive Rating: Pulse width limited by maximum junction temperature ($T_{J_Max} = 150^\circ C$).
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. This single-pulse measurement was taken under the following condition ($L = 0.1mH, V_{GS} = -4.5V, V_{DS} = -12V$) while its value is limited by $T_{J_Max} = 150^\circ C$.
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
6. Guaranteed by design, not subject to production.

Typical Characteristics

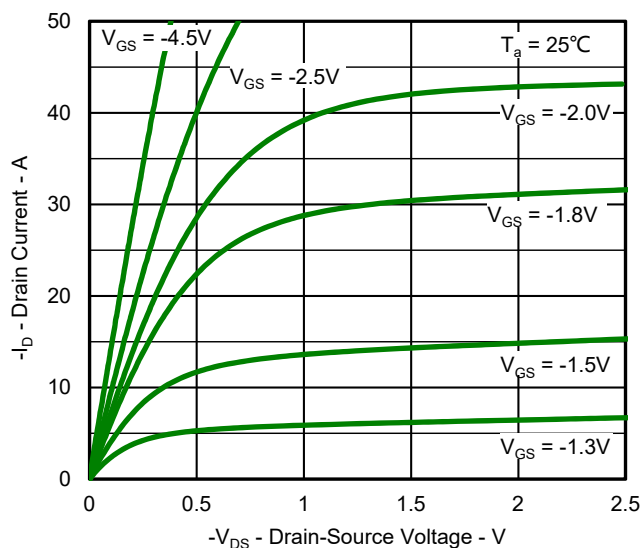


Fig.1 Output Characteristics

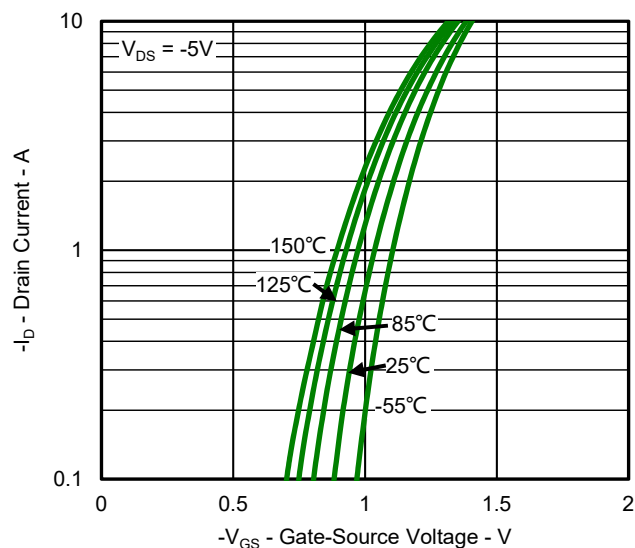


Fig.2 Typical Transfer Characteristic

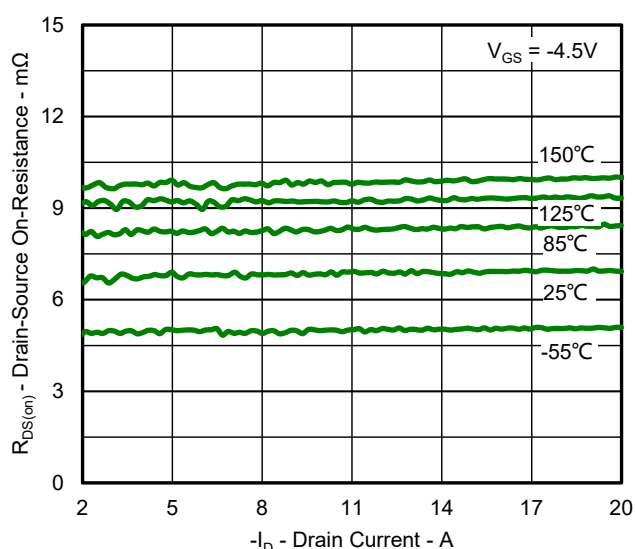


Fig.3 Typical On-Resistance vs Drain Current and Temperature

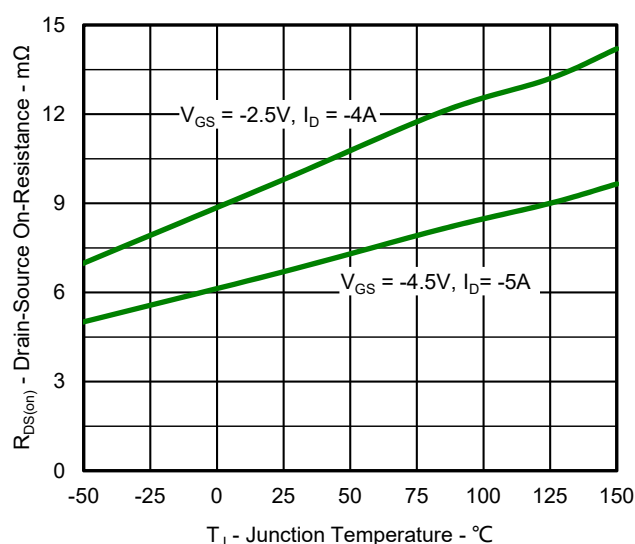


Fig.4 On-Resistance Variation with Temperature

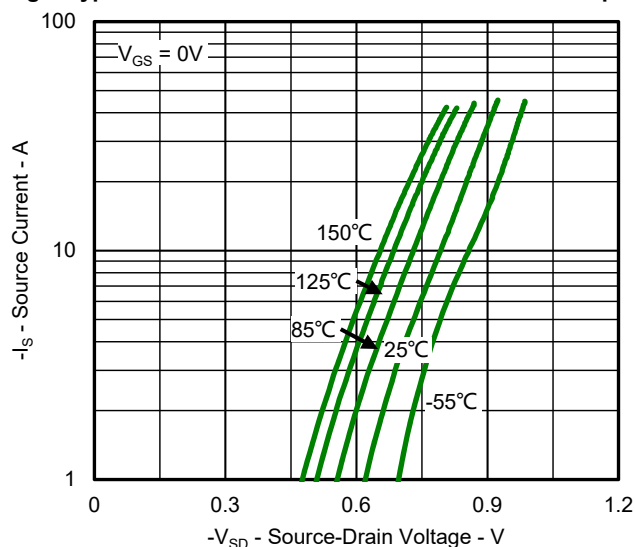


Fig.5 Diode Forward Voltage vs. Current

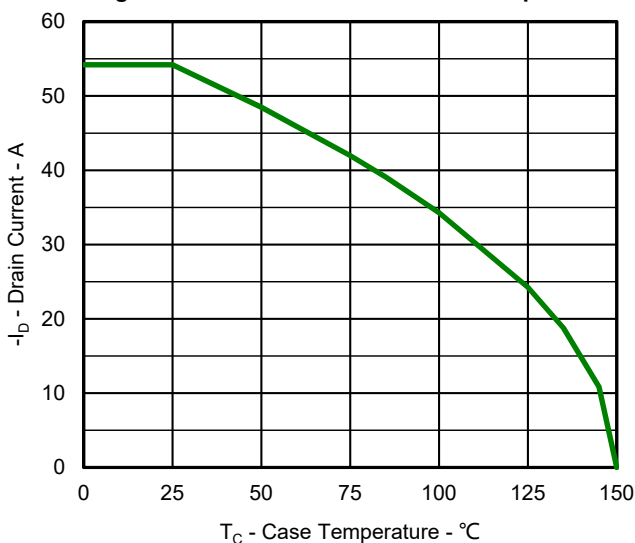


Fig.6 Maximum Drain Current vs. Case Temperature

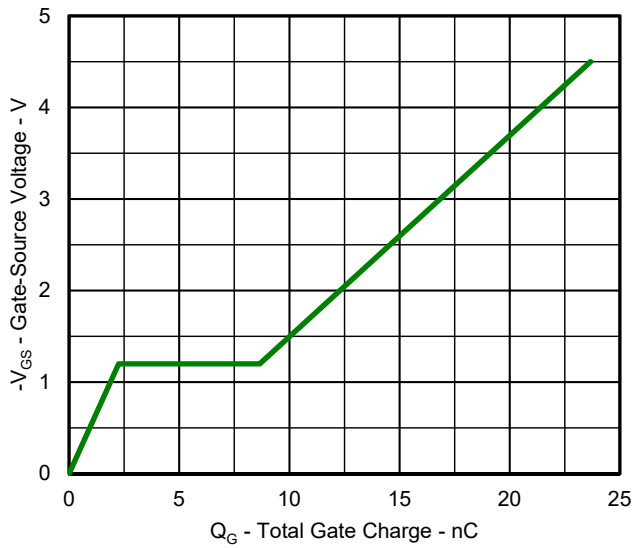


Fig.7 Gate Charge Characteristics

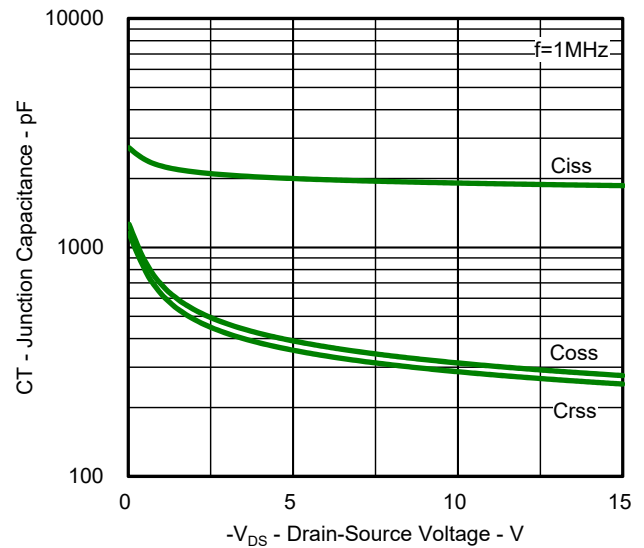


Fig.8 Typical Junction Capacitance

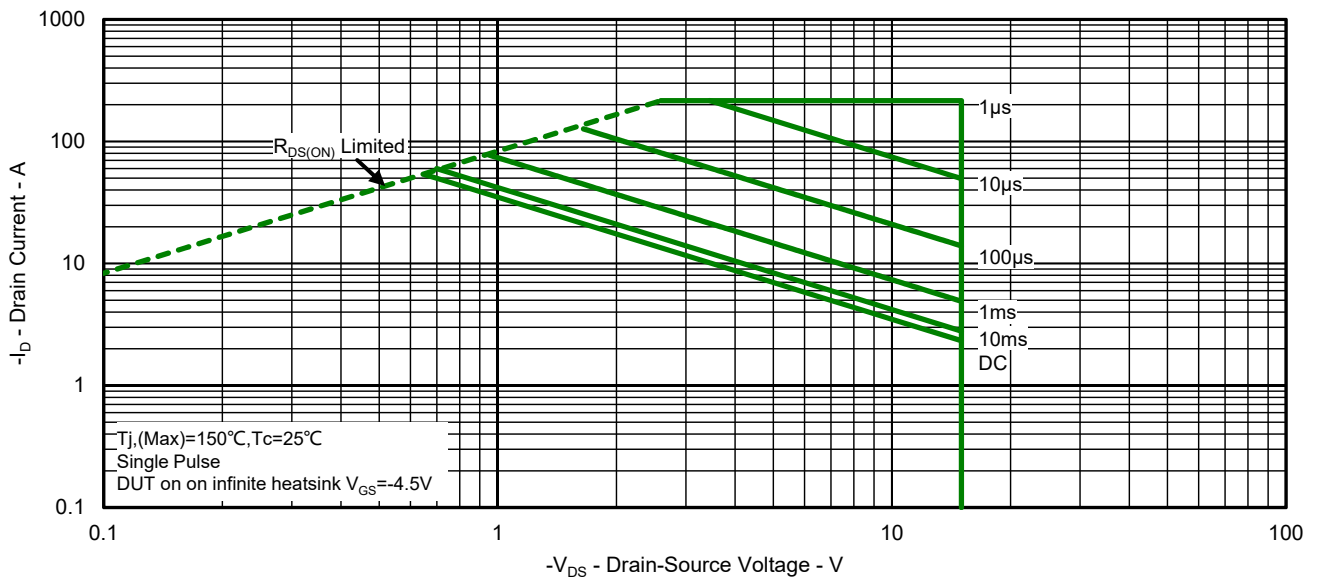


Fig.9 Safe Operation Area

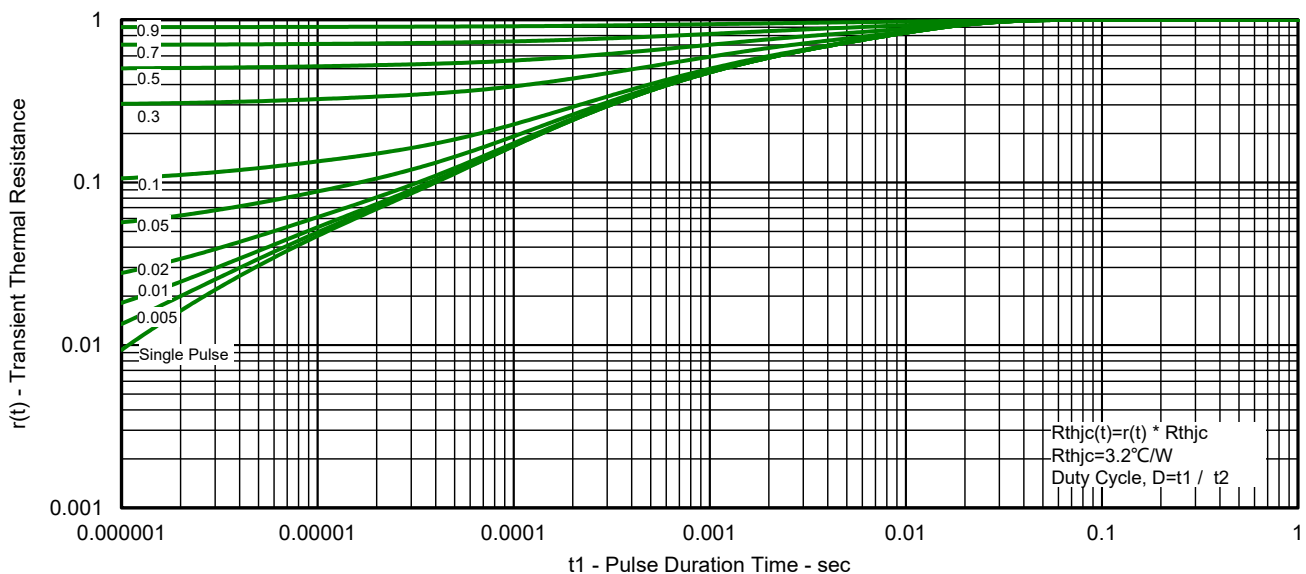
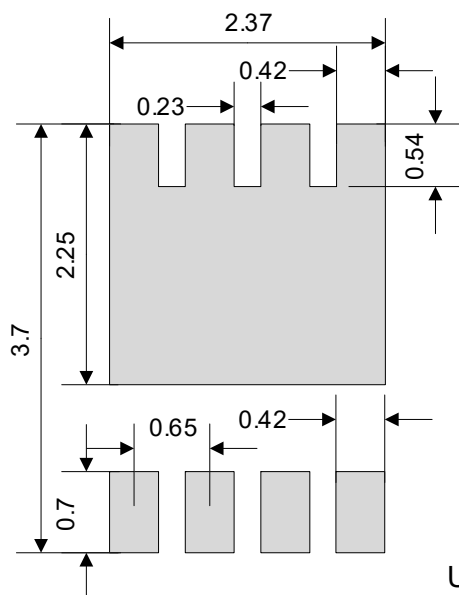
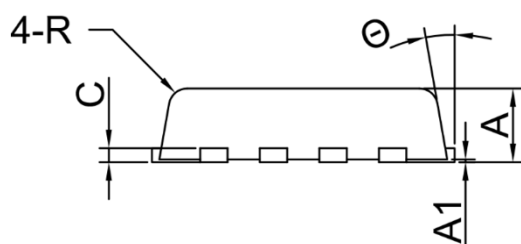
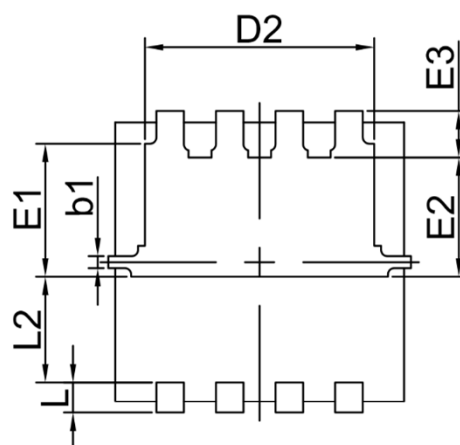
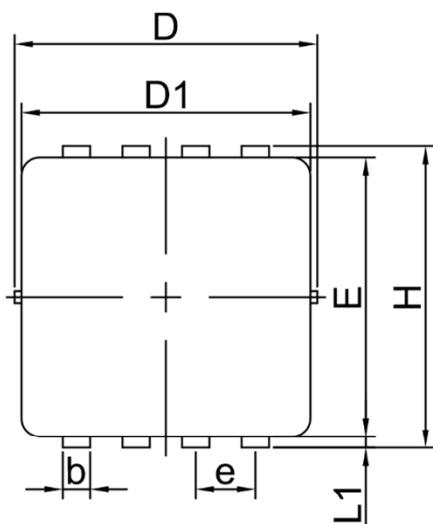


Fig.10 Transient Thermal Resistance

Product Dimension (PDFN3333-8L)



Unit: mm

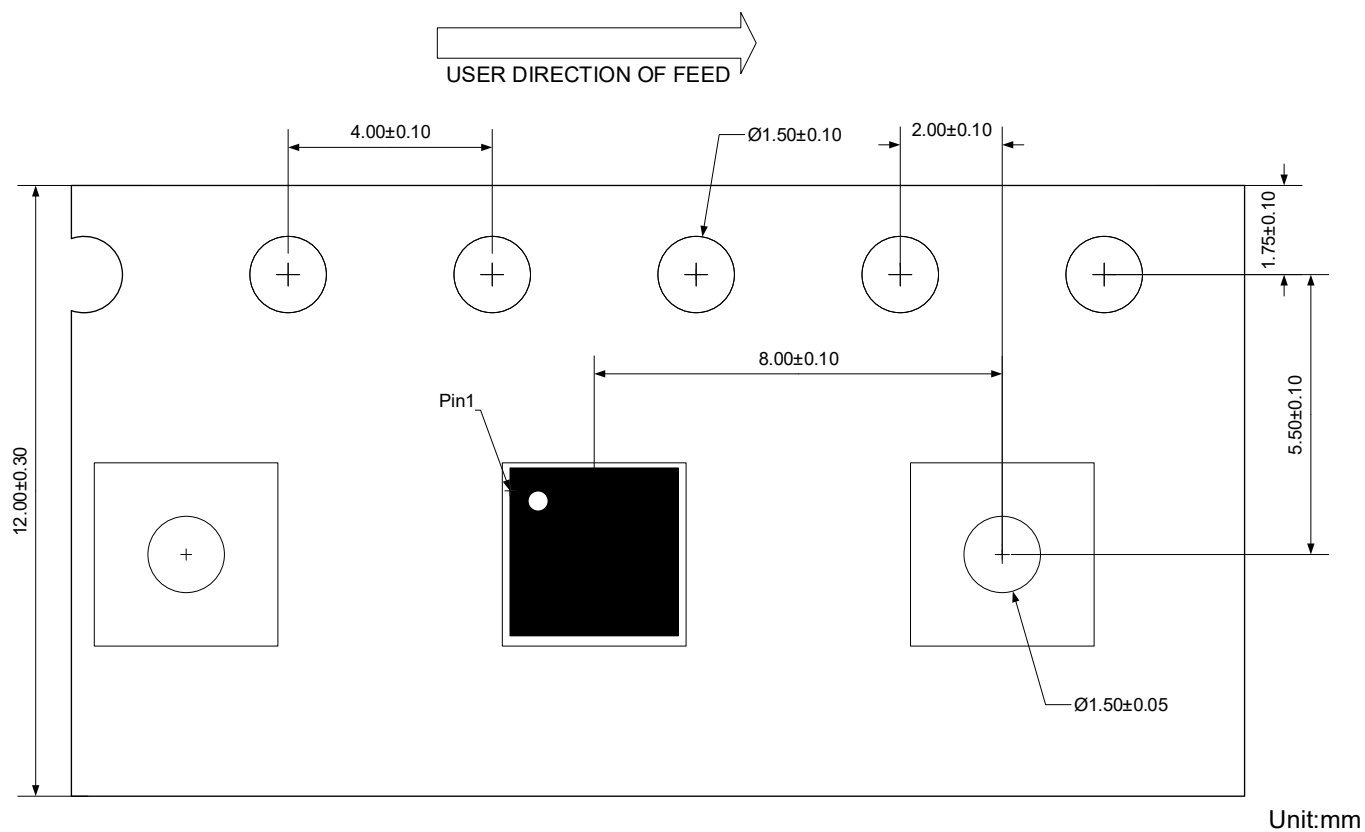
Suggested PCB Layout

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.60	0.90	0.024	0.035
A1	0.00	0.05	0.000	0.002
b	0.24	0.35	0.009	0.014
b1	0.08	0.30	0.003	0.012
c	0.152 Ref.		0.006 Ref.	
D	2.95	3.40	0.116	0.134
D1	2.95	3.25	0.116	0.128
D2	2.40	2.60	0.094	0.102
E	2.95	3.20	0.116	0.126
E1	1.35	1.57	0.053	0.062
E2	1.20	1.40	0.047	0.055
E3	0.40	0.60	0.016	0.024
e	0.65 BSC		0.026 BSC	
H	3.20	3.40	0.126	0.134
L	0.30	0.50	0.012	0.020
L1	0.10	0.20	0.004	0.008
L2	1.13 Ref.		0.044 Ref.	
R	0.20 Ref.		0.008 Ref.	
θ	5°	14°	5°	14°

Ordering Information

Package	Reel	Shipping
PDFN3333-8L	13"	5000 / Tape & Reel

Load With Information



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