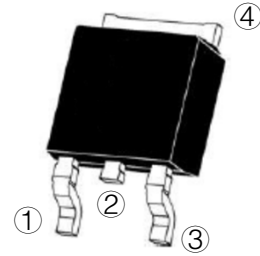


Description

The PNMDP500V5 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.


TO-252 (Top View)

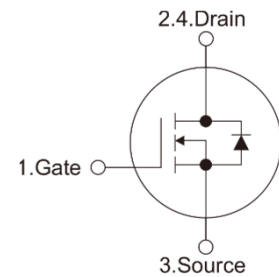
MOSFET Product Summary		
$V_{DS}(V)$	$R_{DS(on)}(\Omega)(Typ)$	$I_D(A)$
500	1.3 @ $V_{GS} = 10V$	5.0

Feature

- Fast switching capability
- Avalanche energy tested
- Improved dv/dt capability, high ruggedness

Mechanical data

- Case: TO-252
- Approx. Weight: 0.33g (0.012oz)
- Lead free finish, RoHS compliant
- Case Material: "Green" molding compound, UL flammability classification 94V-0, "Halogen-free".


Schematic diagram

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	± 30	V
Drain Current-Continuous	I_D	$T_C=25^\circ C$	5.0
		$T_C=100^\circ C$	3.2
Pulsed Drain Current ²⁾	I_{DM}	20	A
Avalanche Energy, Single Pulsed ³⁾	E_{AS}	112	mJ
Peak Diode Recovery dv/dt	dv/dt	2.1	V/ns
Maximum Power Dissipation	P_D	65	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 ~ +150	°C
Junction-to-Ambient	$R_{\theta JA}$	63	°C/W
Junction-to-Case	$R_{\theta JC}$	1.9	°C/W

Notes:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L=10mH, $V_{DD}=50V$, $R_G=25\Omega$, Starting $T_J = 25^\circ C$

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	500	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 500V, V_{GS} = 0V$	-	-	1.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 2.5A$	-	1.3	1.6	Ω
Forward Transconductance	g_{FS}	$V_{DS} = 15V, I_D = 2.5A$	-	3.3	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 25V, V_{GS} = 0V,$ $f = 1.0MHz$	-	564	-	pF
Output Capacitance	C_{oss}		-	66	-	
Reverse Transfer Capacitance	C_{rss}		-	12	-	
Gate Resistance	R_G	$V_{DS}=0V, FREQ=1.0MHz$	-	2.1	-	Ω
Switching Characteristics						
Turn-on Delay Time ⁴⁾	$t_{d(on)}$	$V_{DS} = 250V, V_{GS} = 10V,$ $I_D = 5A, R_G = 25\Omega^{4,5)}$	-	11	-	ns
Turn-on Rise Time	t_r		-	28	-	
Turn-Off Delay Time	$t_{d(off)}$		-	36	-	
Turn-Off Fall Time	t_f		-	26	-	
Total Gate Charge ⁴⁾	Q_g	$V_{DS} = 400V, V_{GS} = 10V,$ $I_D = 5A, I_G = 1mA^{4,5)}$	-	17	-	nC
Gate-Source Charge	Q_{gs}		-	4.0	-	
Gate-Drain Charge	Q_{gd}		-	8.0	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage ⁴⁾	V_{SD}	$V_{GS} = 0V, I_S = 5.0A$	-	-	1.4	V
Diode Continuous Current	I_S		-	-	5.0	A
Diode Pulsed Current	I_{SM}		-	-	20	A
Reverse Recovery Time ⁴⁾	t_{rr}	$I_F = 5.0A,$ $di/dt = 100A/\mu s$	-	319	-	nS
Reverse Recovery Charge	Q_{rr}		-	3.9	-	μC

Notes:

4. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

5. Essentially independent of operating temperature.

Typical Characteristics

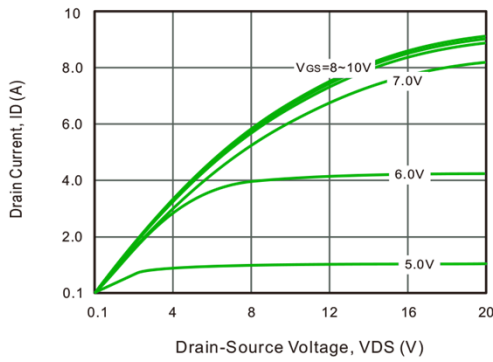


Fig.1 Output characteristics

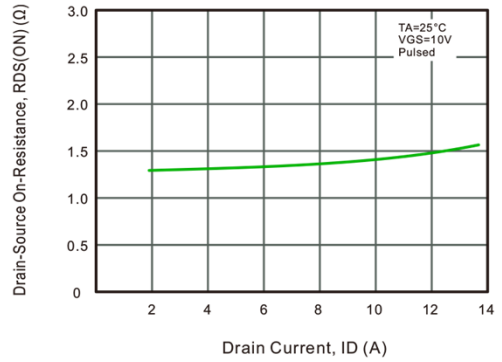


Fig.2 Drain-Source On-Resistance vs. Drain Current

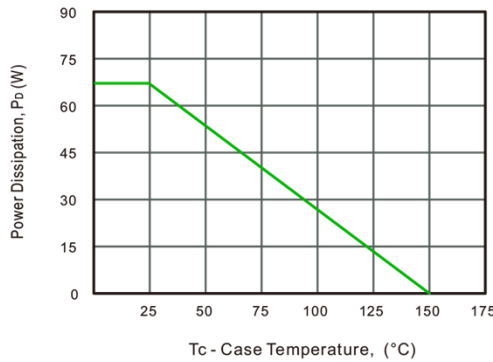


Fig.3 Power Dissipation

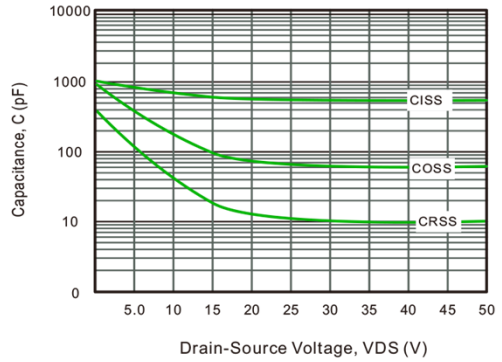


Fig.4 Capacitance Characteristics

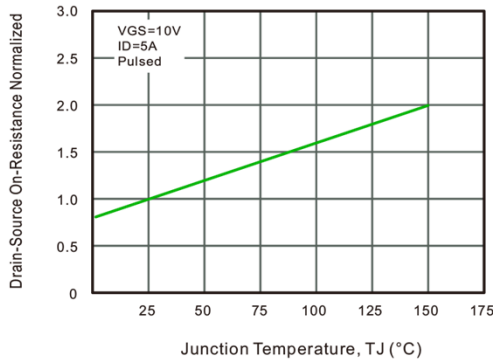


Fig.5 Drain-Source On-Resistance vs. Junction Temperature

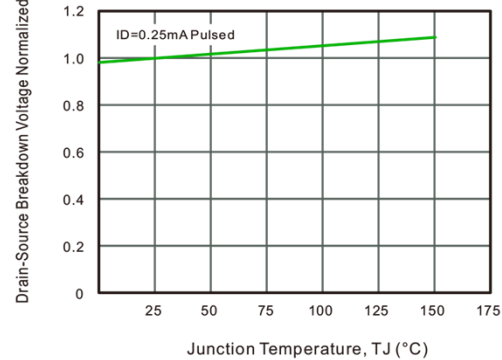


Fig.6 Breakdown Voltage vs. Junction Temperature

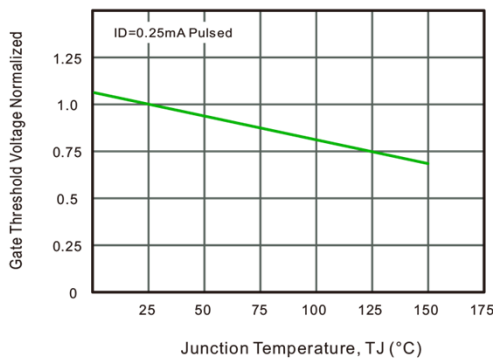


Fig.7 Gate Threshold Voltage vs. Junction Temperature

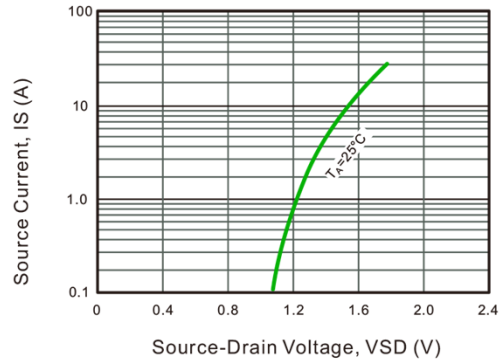


Fig.8 Source Current vs. Source-Drain Voltage

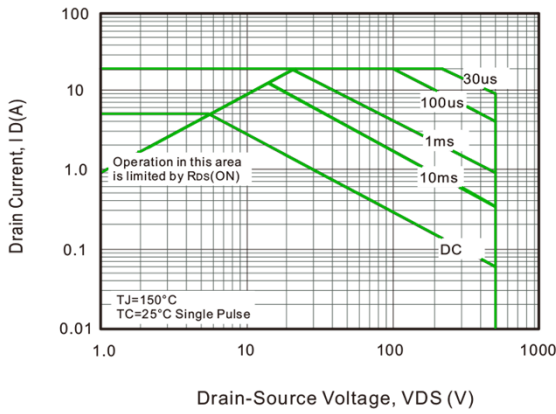


Fig.9 Safe Operating Area

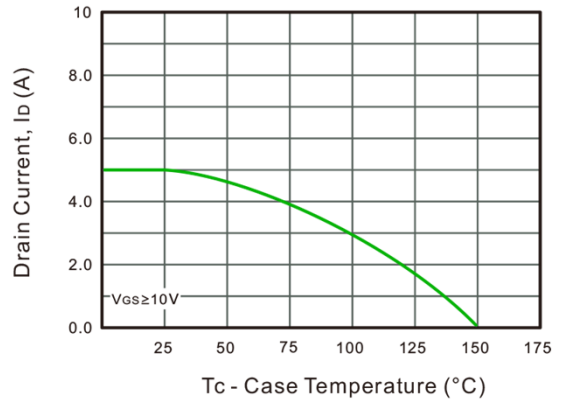


Fig.10 Drain Current Derating

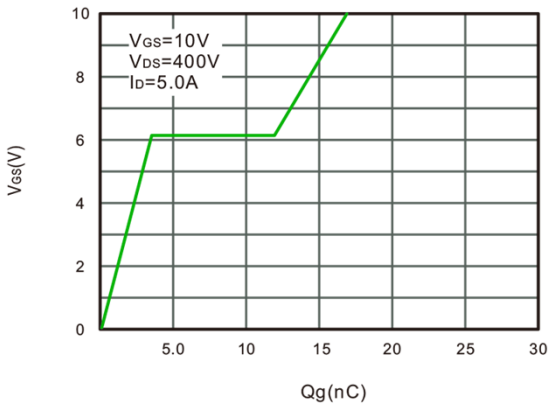


Fig.11 Gate Charge Characteristics

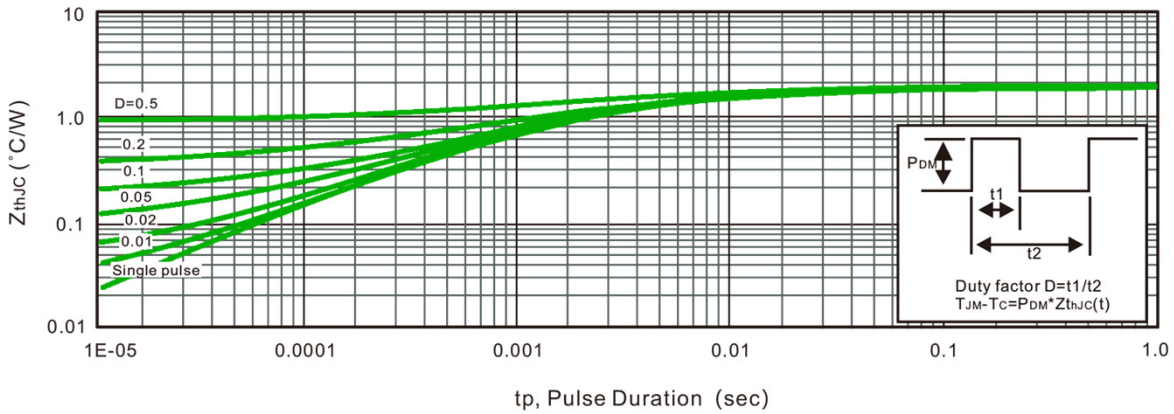
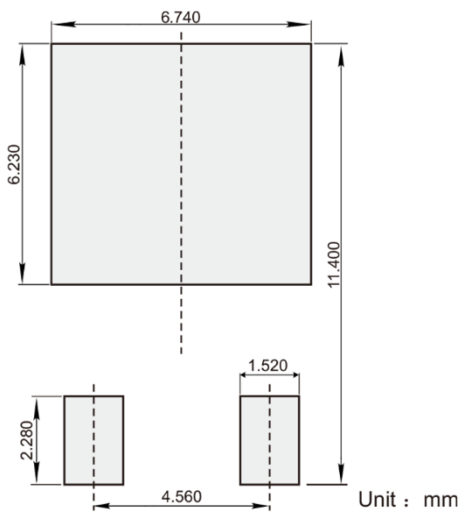
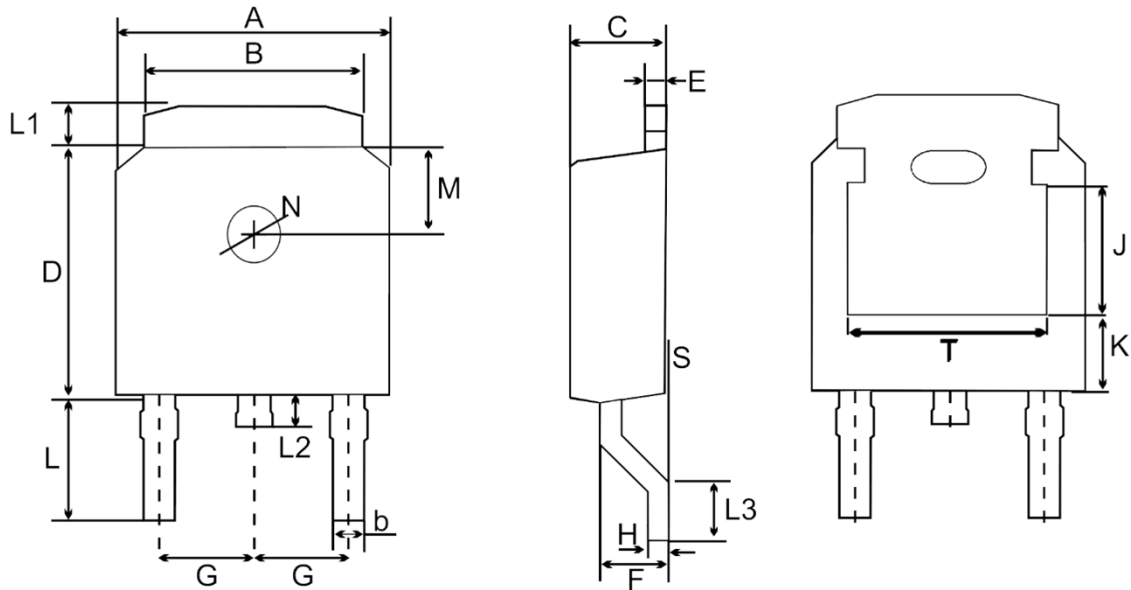


Fig.12 Max. Transient Thermal Impedance

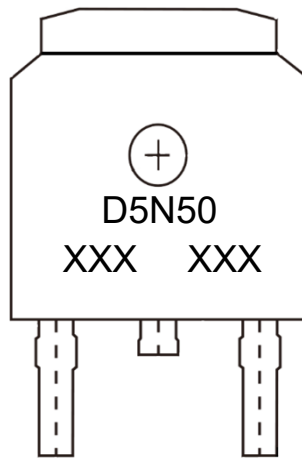
Product Dimension (TO-252)



Suggested PCB Layout

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	6.30	6.70	0.248	0.264
B	5.13	5.53	0.202	0.218
b	0.66	0.86	0.026	0.034
C	2.10	2.50	0.083	0.098
D	5.90	6.30	0.232	0.248
E	0.41	0.61	0.016	0.024
F	1.27	1.87	0.050	0.074
G	2.30 Typ.		0.091 Typ.	
H	0.45	0.55	0.018	0.022
L	2.70	3.10	0.106	0.122
L1	0.80	1.20	0.031	0.047
L2	0.60	1.00	0.024	0.039
L3	1.00	1.75	0.039	0.069
S	0.00	0.10	0.000	0.004
M	1.80 Typ.		0.071 Typ.	
N	1.30 Typ.		0.051 Typ.	
J	3.20 Ref.		0.126 Ref.	
K	1.80 Ref.		0.071 Ref.	
T	4.83 Ref.		0.190 Ref.	

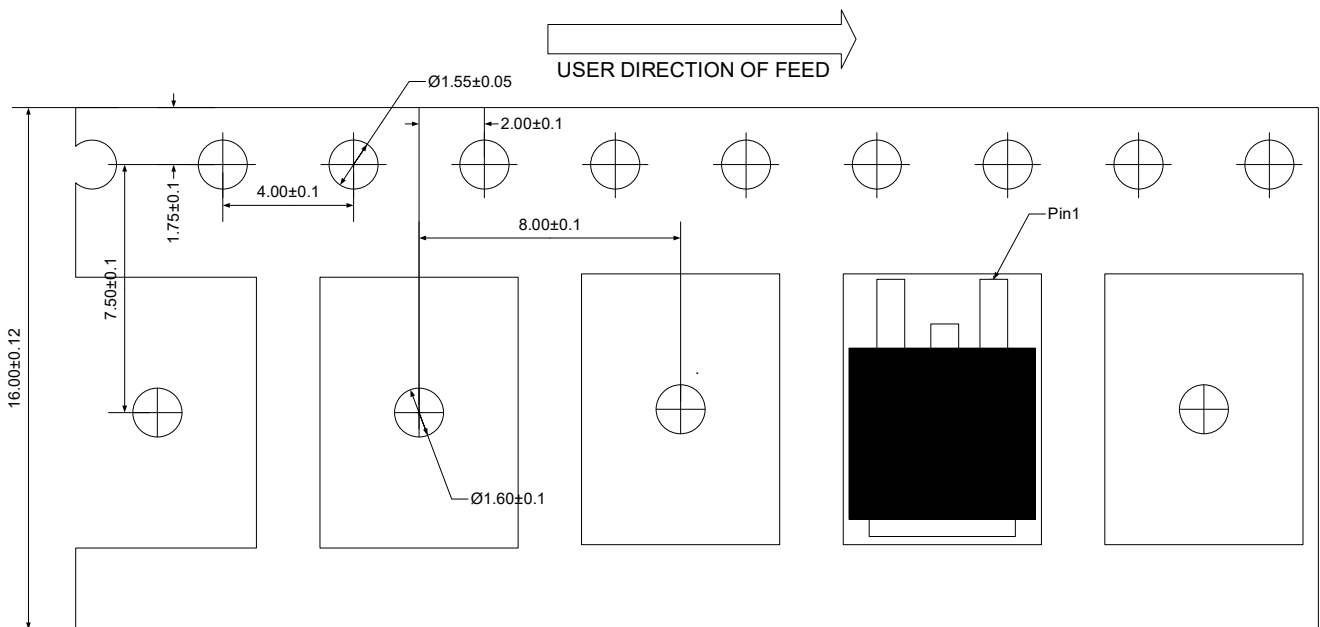
Marking Information



Ordering Information


Device	Package	Reel	Shipping
PNMDP500V5	TO-252	13"	2500 / Tape & Reel

Load With Information



Unit:mm


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