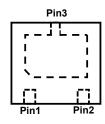


# **Uni-directional 10V High Capacitance TVS**

## **Description**

The PTVSHC3N10VU Transient Voltage Suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC3N10VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC3N10VU is available in a DFN2×2-3L package with working voltages of 10 volt.



Pin configuration

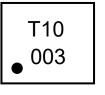
#### **Feature**

- > 4000W Peak pulse power per line (tp = 8/20µs)
- DFN2×2-3L package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30kV(air), ±30kV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

# Pin 1, 20 Pin 3

## **Applications**

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players



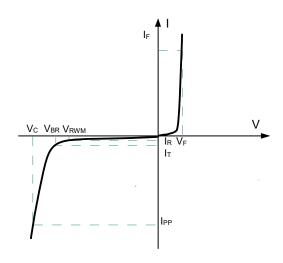
Marking (Top View)

### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C

## **Electronics Parameter**

Symbol	Parameter	
V <sub>RWM</sub>	Peak Reverse Working Voltage	
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>	
V <sub>BR</sub>	Breakdown Voltage @ I⊤	
Ι <sub>Τ</sub>	Test Current	
I <sub>PP</sub>	Maximum Reverse Peak Pulse Curren	
Vc	Clamping Voltage @ IPP	
P <sub>PP</sub>	Peak Pulse Power	
CJ	Junction Capacitance	
lF	Forward Current	
VF	Forward Voltage @ I <sub>F</sub>	



## Electrical characteristics per line@25℃( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Peak Reverse Working Voltage	V <sub>RWM</sub>				10	V
Breakdown Voltage	V <sub>BR</sub>	I <sub>T</sub> =1mA	11	12	13.5	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =10V			1	μA
Clamping Voltage	Vc	$I_{PP}$ =20A $t_P$ = 8/20 $\mu$ s		12.5	14	V
Clamping Voltage	Vc	I <sub>PP</sub> =140A t <sub>P</sub> = 8/20μs		17.5	19.5	٧
Clamping Voltage	Vc	I <sub>PP</sub> =200A t <sub>P</sub> = 8/20μs		20	22	V
Junction Capacitance	Сл	V <sub>R</sub> =0V f = 1MHz		1300	1500	pF

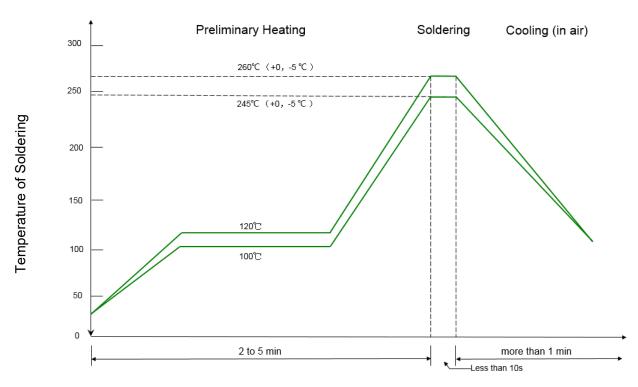
Notes: Measured from pin 3 to pin 1 and pin 2.

## Absolute maximum rating@25℃

Rating	Symbol	Value	Unit
Peak Pulse Power ( t⊳ = 8/20µs )	P <sub>pp</sub>	4000	W
Peak Pulse Current ( t <sub>P</sub> = 8/20μs )	Ірр	200	Α
Lead Soldering Temperature	T∟	260 (10 sec)	°C
Operating Temperature	re T <sub>J</sub> -55 to 150		°C
Storage Temperature	T <sub>STG</sub>	-55 to 150 °C	

#### **Typical Characteristics** <u>a</u> IPP - Peak Pulse Current - % of % Of Rated Power t - Time -µs T₁ – I ead Temperature - °C Fig 1.Pulse Waveform Fig 2.Power Derating Curve Pulse waveform: tp=8/20µs f=1MHz Vc-Clamping Voltage (V) C-Junction capacitance (pF) V<sub>R</sub>-Reverse voltage (V) IPP-Peak pulse current (A) Fig 3. Clamping voltage vs. Peak pulse current Fig 4. Capacitance vs. Reverse voltage Peak Pulse Power (W) 00 00 01 TLP Current - A -4 -8 -12 -16 -20 Pulse Duration(µs) TLP Voltage - V Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time Fig 6. TLP Measurement

### **Solder Reflow Recommendation**



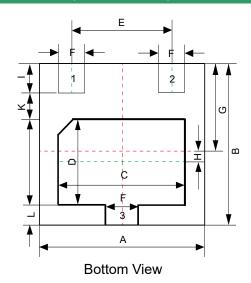
Remark: Pb free for 260°C; Pb for 245°C.

## **PCB Design**

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

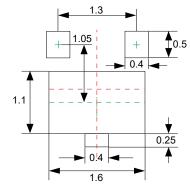
- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

# Product dimension (DFN2×2-3L)





Dim	Millimeters		
DIIII	MIN	MAX	
Α	1.90	2.10	
В	1.90	2.10	
С	1.40	1.60	
D	0.90 1.15		
E	1.30BSC		
F	0.25	0.40	
G	0.90	1.10	
Н	0.20	0.30	
I	0.32 0.48		
J	0.50 0.65		
K	0.20	0.45	
L	0.15 0.30		



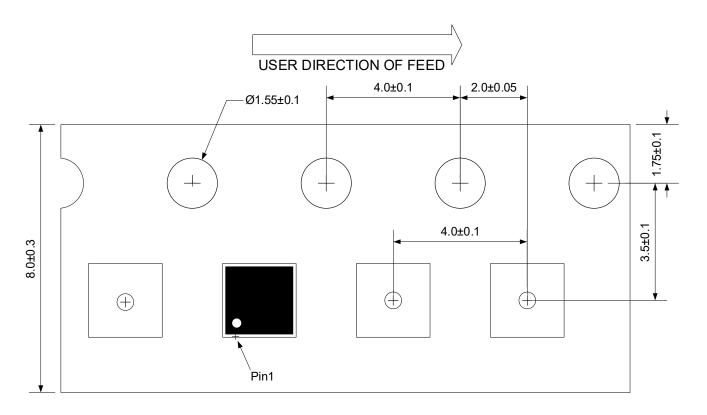
Recommended Soldering Pad

Unit:mm

## Ordering information

Device	Package	Reel	MPQ
PTVSHC3N10VU	DFN2×2-3L (Pb-Free)	7"	3000 / Tape & Reel

# Load with information



Unit:mm

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