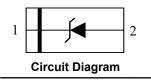
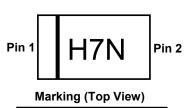


PTVSHC2EN7VU Transient Voltage Suppressor

Description

The PTVSHC2EN7VU ESD protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. PTVSHC2EN7VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC2EN7VU is available in a DFN1610-2L package with working voltages of 7 volt. It is used to meet the ESD immunity requirements of IEC 61000-4-2, (±30kV air, ±30kV contact discharge)





Feature

- 1400W Peak pulse power per line (t_P = 8/20µs)
- DFN1610-2L package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
 ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

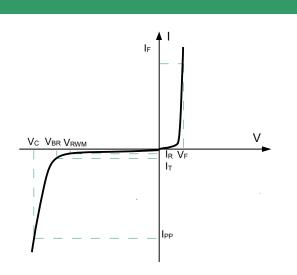
- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness∶≤3mil
- Device meets MSL3 requirements

Electronics Parameter

Symbol	Parameter	
VRWM	Peak Reverse Working Voltage	
IR	Reverse Leakage Current @ VRWM	
V _{BR}	Breakdown Voltage @ I⊤	
Ι _Τ	Test Current	
IPP	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	
lf	Forward Current	
VF	Forward Voltage @ I _F	



Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				7	V
Breakdown Voltage	V _{BR}	It=1mA		8	9.5	V
Reverse Leakage Current	I _R	V _{RWM} =7V			1	μA
Clamping Voltage	Vc	I _{PP} =70A t _P = 8/20μs		20	24	V
Junction Capacitance	Cj	V _R =0V f = 1MHz	650	700	750	pF

Absolute maximum rating@25°C

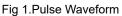
Rating	Symbol	Value	Units
Peak Pulse Power(t _P = 8/20µS)	P _{pp}	1400	W
Lead Soldering Temperature	ΤL	260 (10 sec)	°C
Operating Temperature	TJ	-55 to +125	°C
Storage Temperature	Tstg	-55 to +150	°C

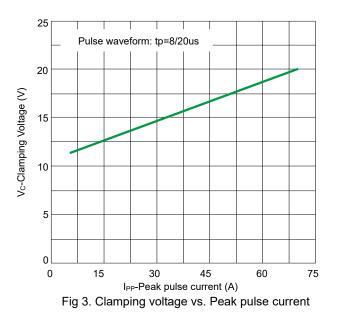
PTVSHC2EN7VU

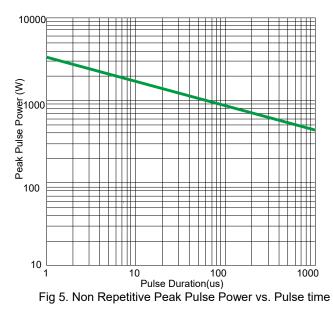
PTVSHC2EN7VU

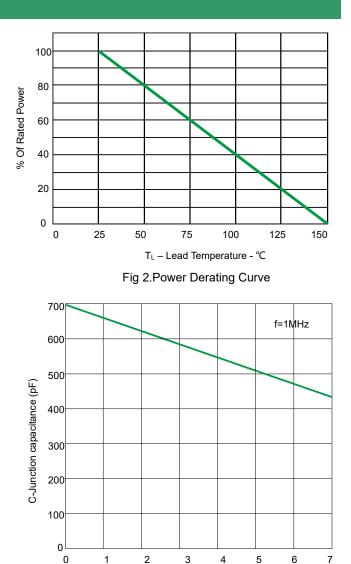
Typical Characteristics



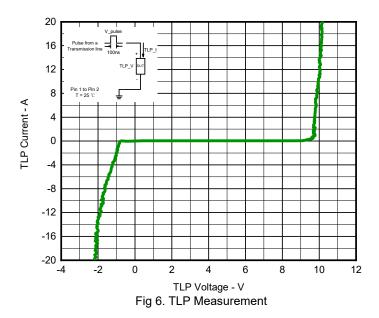








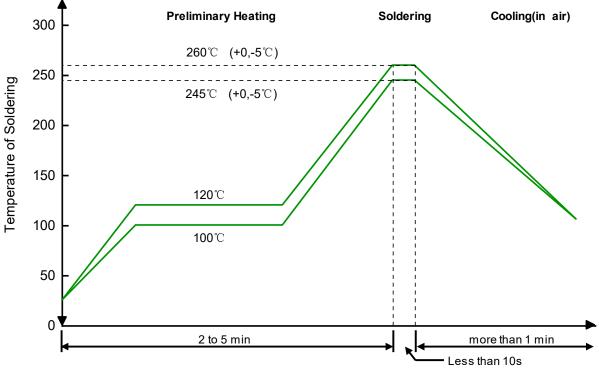
V_R-Reverse voltage (V) Fig 4. Capacitance vs. Reveres voltage



PTVSHC2EN7VU

Transient Voltage Suppressor

Solder Reflow Recommendation



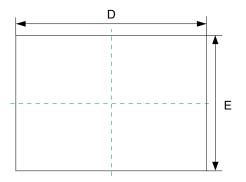
Remark: Pb free for 260°C; Pb for 245°C.

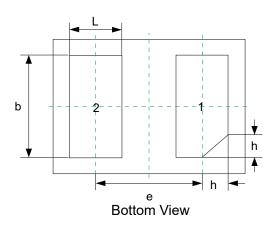
PCB Design

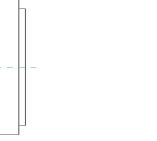
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

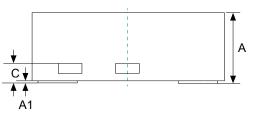
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN1610-2L)

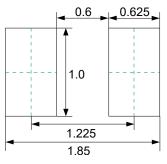








Dim	Millimeters		
	MIN	МАХ	
А	0.40	0.60	
A1		0.05	
b	0.75	0.85	
С	0.10	0.20	
D	1.55	1.65	
e	1.10BSC		
E	0.95	1.05	
L	0.35	0.45	
h	0.15	0.25	



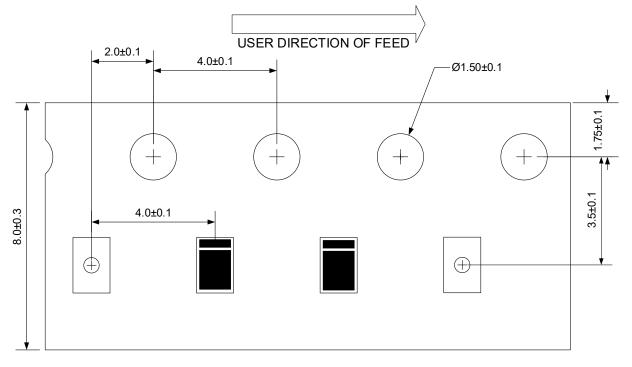
Recommended Soldering Pad

Ordering information

Device	Package	Shipping
PTVSHC2EN7VU	DFN1610-2L (Pb-Free)	3000 / Tape & Reel

Unit:mm

Load with information



Unit:mm

PTVSHC2EN7VU

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