

Uni-directional 4.8V High Capacitance TVS

Description

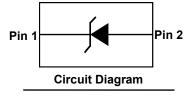
The PTVSHC2EN4V8UF Transient Voltage Suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC2EN4V8UF protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC2EN4V8UF is available in a DFN1610-2L package with working voltages of 4.8 volt.



DFN1610-2L(Bottom View)

Feature

- \triangleright 2000W Peak pulse power per line (t_P = 8/20µs)
- DFN1610-2L package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
 ±30kV(air), ±30kV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)
 IEC 61000-4-5 (Lightning) 180A (8/20us)



Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP4 players

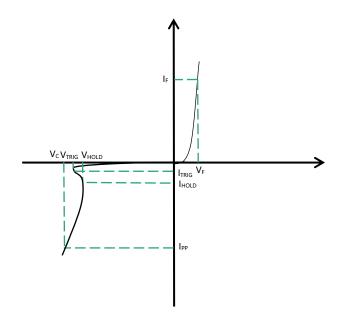
Pin 1 H4T Pin 2 Marking (Top View)

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V _{TRIG}	Reverse trigger Current		
V _{HOLD}	Reverse holding voltage		
Ι _Τ	Test Current		
lpp	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
Сл	Junction Capacitance		
I _F	Forward Current		
V _F	Forward Voltage @ I _F		

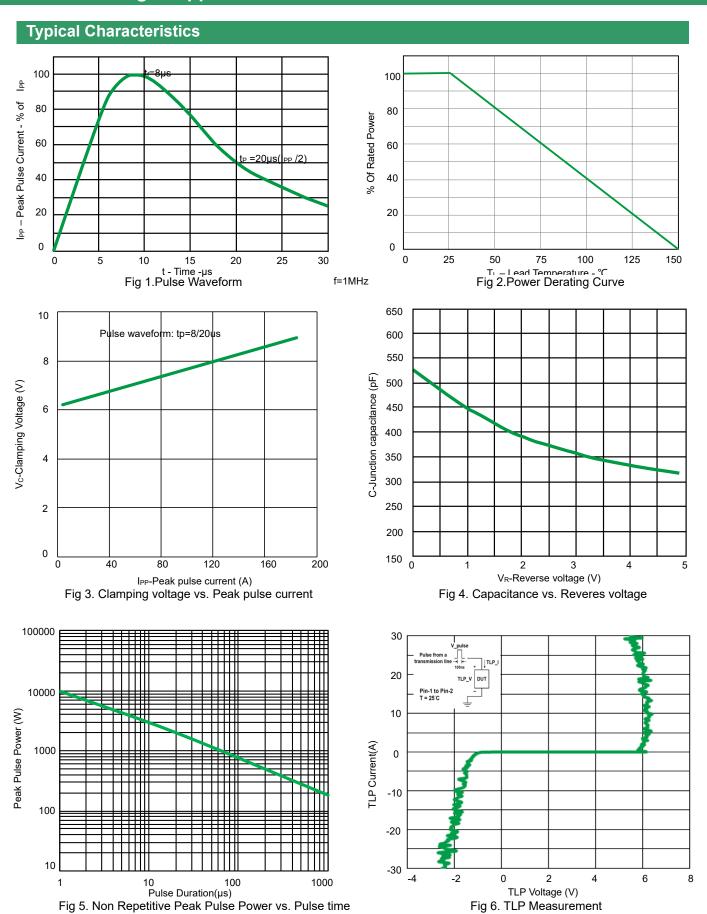


Electrical characteristics per line@25℃(unless otherwise specified)

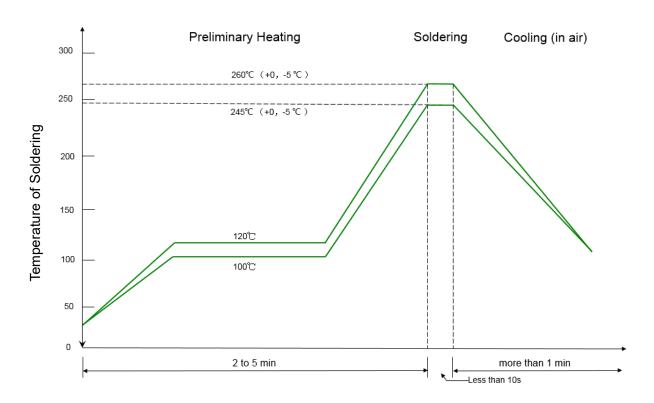
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Peak Reverse Working Voltage	V _{RWM}				4.8	V
Reverse trigger voltage	V _{TRIG}	I _{TRIG} =2µA	5.0	5.9	6.5	V
Reverse holding voltage	V _{HOLD}	I _{HOLD} =1.0mA		5.7		V
Reverse Leakage Current	I _R	V _{RWM} =4.8V			5	μA
Clamping Voltage	Vc	I _{PP} =100A t _P = 8/20μs		7.3	8.8	V
Clamping Voltage	Vc	I _{PP} =180A t _P = 8/20μs		8.8	10.8	V
Junction Capacitance	CJ	V _R =0V f = 1MHz		530	650	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Unit
Peak Pulse Power (t₂ = 8/20μs)	P_pp	2000	W
Peak Pulse Current (t _P = 8/20µs)	I _{pp}	180	А
Lead Soldering Temperature	T∟	260 (10 sec)	°C
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	Тѕтс	-55 to 150	${\mathbb C}$



Solder Reflow Recommendation



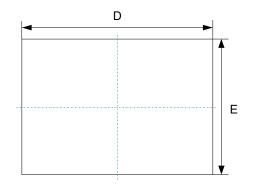
Remark: Pb free for 260°C; Pb for 245°C.

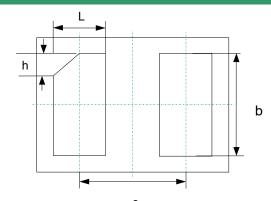
PCB Design

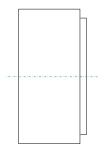
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

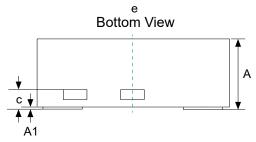
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN1610-2L)

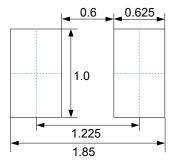








Dim	Millimeters			
	MIN	MAX		
Α	0.45	0.60		
A1		0.05		
b	0.75	0.85		
С	0.10	0.20		
D	1.55	1.65		
е	1.10BSC			
E	0.95	1.05		
L	0.35	0.45		
h	0.15	0.25		



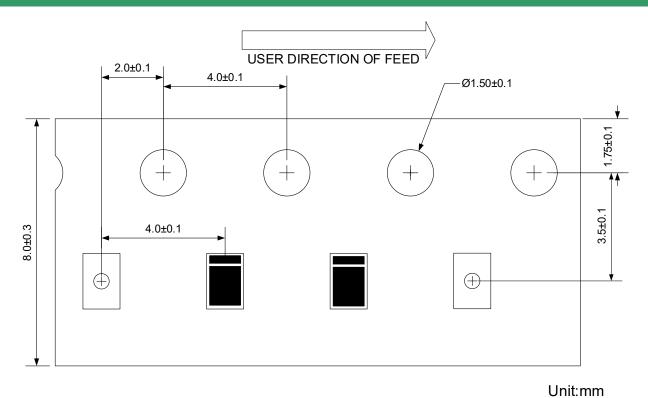
Recommended Soldering Pad

Unit: mm

Ordering information

Device	Package	Reel	MPQ
PTVSHC2EN4V8UF	DFN1610-2L (Pb-Free)	7"	3000 / Tape & Reel

Load with information



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