

PTVSHC1TF24VUH

Uni-directional 24V High Capacitance TVS Protector

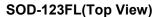
Description

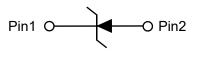
The PTVSHC1TF24VUH transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC1TF24VUH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC1TF24VUH is available in a SOD-123FL package with working voltages of 24 volt.

Feature

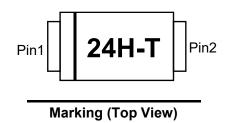
- 7200W peak pulse power per line (t_P = 8/20µs)
- SOD-123FL package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30kV(air), ± 30kV(contact); IEC 61000-4-5 (Lightning) 225A (8/20us)







Circuit Diagram



Applications

- > Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

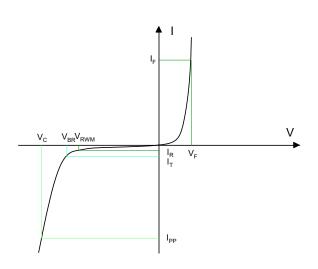
Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

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Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V _{BR}	Breakdown Voltage @ I _T		
Ι _Τ	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
V _c	Clamping Voltage @ I _{PP}		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
I _F	Forward Current		
V _F	Forward Voltage @ I _F		



Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}	-	-	-	24	V
Breakdown Voltage	V _{BR}	I _t = 1mA	25	-	28.5	V
Reverse Leakage Current	I _R	V _{RWM} = 24V	-	-	1.0	μA
Clamping Voltage	V _c	I _{PP} = 225A, t _p = 8/20μs	-	32	35	V
Junction Capacitance	CJ	V _R = 0V,f = 1MHz	-	-	900	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power (t _P = 8/20µs)	P _{PP}	7200	W
Peak Pulse Current (t _P = 8/20µs)	I _{PP}	225	А
Lead Soldering Temperature	TL	260 (10 sec)	°C
Junction and Storage Temperature Range	T _{J,} T _{STG}	-55~+150	°C
ESD Protection-Contact Discharge	V _{ESD}	±30	kV
ESD Protection-Air Discharge	V _{ESD}	±30	kV

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125

150

1000

Typical Characteristics

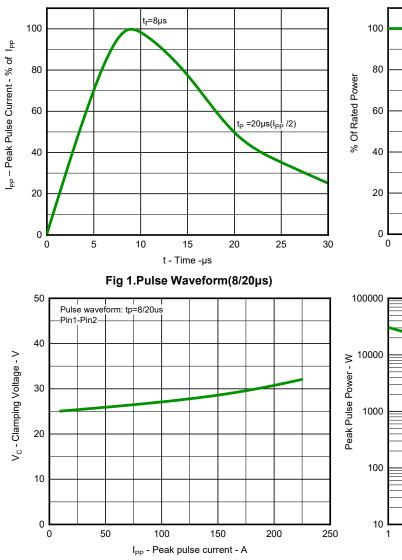


Fig 3. Clamping voltage vs. Peak pulse current

75

 T_L – Lead Temperature - °C

Fig 2.Power Derating Curve

100

25

50

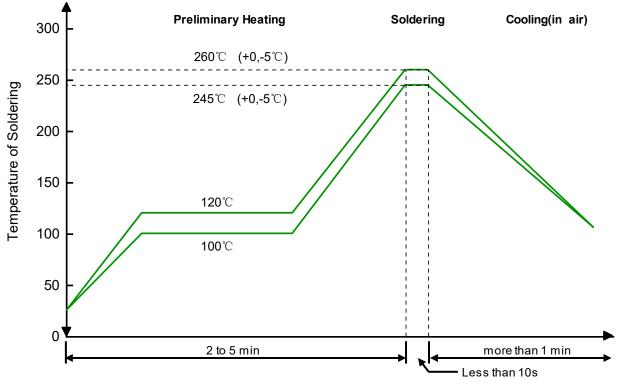
Pulse Duration - µs Fig 4. Non Repetitive Peak Pulse Power vs. Pulse time

100

10

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Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

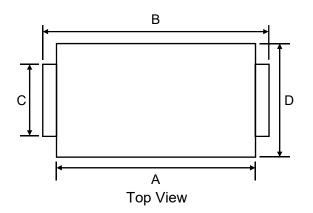
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- ➤ Keep the length of via holes in mind! The longer the more inductance they will have.

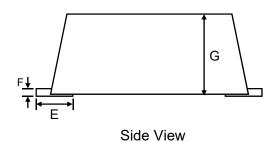
Ordering information

Device	Package	Reel	Shipping
PTVSHC1TF24VUH	SOD-123FL	7"	3000 / Tape & Reel

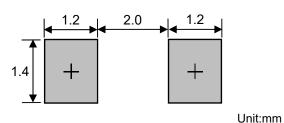
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Product dimension (SOD-123FL)



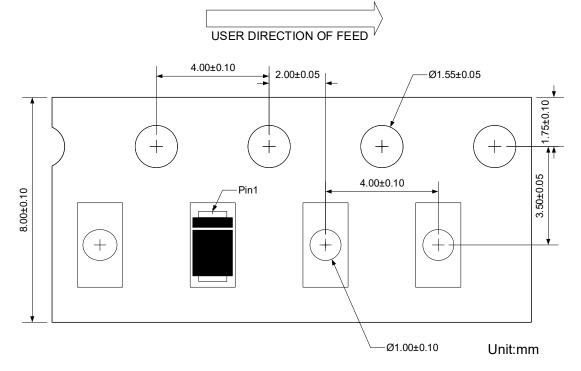


Millimeters Inches Dim Min Min Max Max А 2.50 3.00 0.098 0.118 В 3.40 4.05 0.134 0.159 С 0.80 1.20 0.031 0.047 D 1.50 1.90 0.059 0.075 Е 0.45 0.95 0.018 0.037 F 0.05 0.26 0.002 0.010 G 0.90 1.00 0.035 0.039



Suggested PCB Layout

Load with information



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