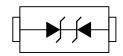


PTVSHC1DF7VBH TVS Protector

Description

The PTVSHC1DF7VBH TVS protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC1DF7VBH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC1DF7VBH is available in a SOD-123FL package with working voltages of 7volt. It is used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge)



Feature

- 3700W Peak pulse power per line (t_P = 8/20µs)
- SOD-123FL package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
 ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
 - MP3 players

Mechanical Characteristics

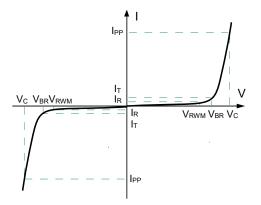
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

PTVSHC1DF7VBH

TVS Protector

Electronics Parameter

Symbol	Parameter		
VRWM	Peak Reverse Working Voltage		
IR	Reverse Leakage Current @ VRWM		
V _{BR}	Breakdown Voltage @ I⊤		
Ι _Τ	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		



Electrical characteristics per line@25 $^{\circ}$ C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				7	V
Breakdown Voltage	V _{BR}	It=1mA		7.8		V
Reverse Leakage Current	IR	V _{RWM} =7V			200	μA
Clamping Voltage	Vc	I _{PP} =185Α t _P = 8/20μs		19	22	V
Junction Capacitance	Cj	V _R =0V f = 1MHz			1200	pF

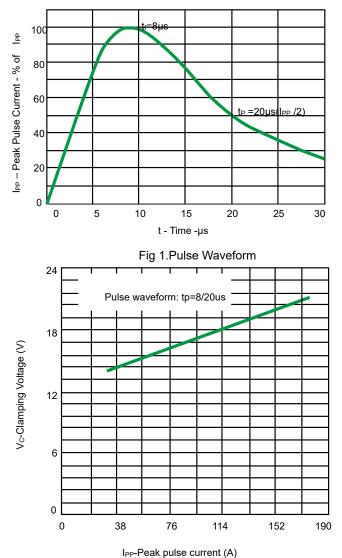
Absolute maximum rating@25°C

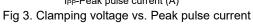
Rating	Symbol	Value	Units
Peak Pulse Power(t _P = 8/20µS)	P _{pp}	3700	w
Lead Soldering Temperature	ΤL	260 (10 sec)	°C
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	Тѕтс	-55 to +150	°C

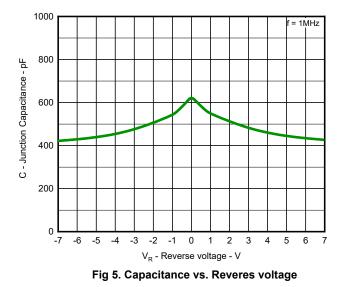
ESD Protector

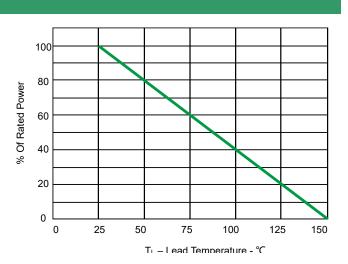
PTVSHC1DF7VBH

Typical Characteristics









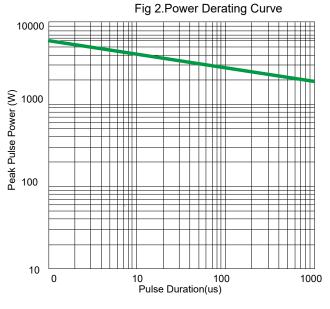
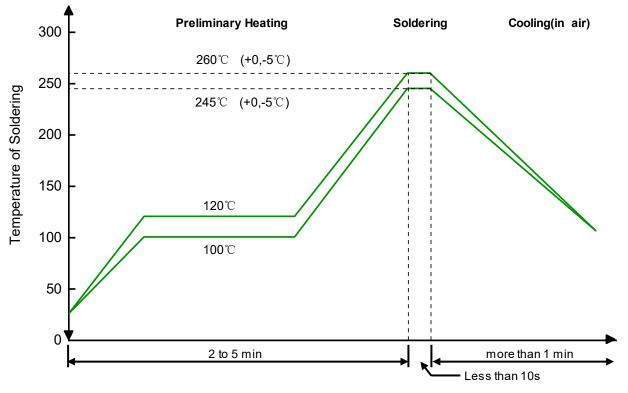


Fig 4. Non Repetitive Peak Pulse Power vs. Pulse time

ESD Protector

PTVSHC1DF7VBH

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

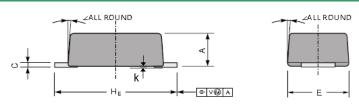
PCB Design

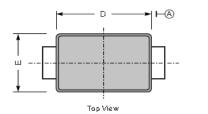
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

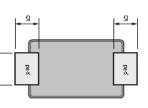
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

ESD Protector

Product dimension (SOD-123FL)





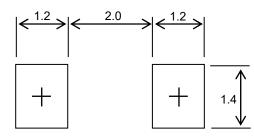


Bottom View

Unit:mm

Dim	Inches		Millimeters		
	MIN	MAX	MIN	MAX	
А	0.031	0.047	0.80	1.20	
С	0.002	0.010	0.05	0.25	
HE	0.138	0.154	3.50	3.90	
E	0.061	0.077	1.55	1.95	
D	0.098	0.114	2.50	2.90	
g	0.020	0.043	0.50	1.10	
е	0.024	0.039	0.60	1.00	
k	0.004		0.10		
2	7°				

φ



Suggested PCB Layout

Unit:mm

Marking information



0		:	
Ura	erino	ΙΠΤΟΓΙ	mation

Device	Package	Reel	Shipping
PTVSHC1DF7VBH	SOD-123FL (Pb-Free)	7"	3000 / Tape & Reel

PTVSHC1DF7VBH

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