

Bi-directional 3.3V Ultra Small Capacitance ESD Protector

Description

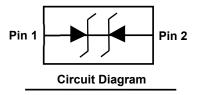
The PESDUC2XD3V3BF protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



DFN0603-2L(Bottom View)

Feature

- DFN0603-2L package
- Replacement for MLV(0201)
- Bidirectional configurations
- Response time is typically < 1 ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±25kV(air), ±25kV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)



Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies



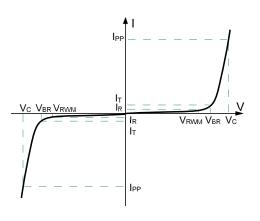
Marking (Top View)

Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- DFN0603-2L without plating

Electronics Parameter

Symbol	Parameter	
V _{RWM}	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V_{BR}	Breakdown Voltage @ I⊤	
lτ	Test Current	
lpp	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
Сл	Junction Capacitance	
IF	Forward Current	
V _F	Forward Voltage @ I _F	



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				3.3	V
Breakdown Voltage	V _{BR}	I _t = 1mA	4.8			V
Reverse Leakage Current	I _R V _{RWM} = 3.3V T=25°C				1	μΑ
Clamping Voltage ¹⁾	Vc	TLP = 16A t _P = 100ns		24		V
Dynamic Resistance ¹⁾	R _{DYN}			0.9		Ω
Clamping Voltage ²⁾	Vc	I _{PP} = 1A t _P = 8/20μs		9.5	11	V
Clamping Voltage ²⁾	lamping Voltage ²⁾ V _C I _{PP}			19	21	V
Junction Capacitance		V _R =0V f = 1MHz		0.25	0.45	pF

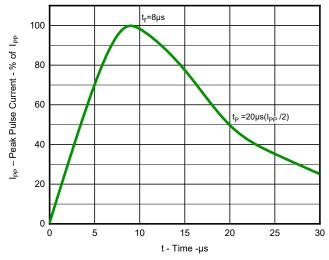
Notes:

- 1) TLP parameter: Z_0 =50 Ω , t_p =100ns, t_r =2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
- 2) Non-repetitive current pulse, according to IEC61000-4-5.

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (tp=8/20 μ s)	P _{PP}	90	W
Peak Pulse Current (tp=8/20 μ s)	I PP	5	Α
Operating Temperature	TJ	-55 to 150	$^{\circ}$ C
Storage Temperature	T _{STG}	-55 to 150	$^{\circ}$ C
ESD Protection-Contact Discharge	V _{ESD}	±25	kV
ESD Protection-Air Discharge	V _{ESD}	±25	kV

Typical Characteristics



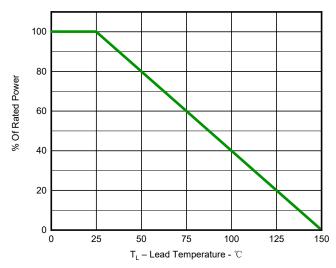


Fig 1.Pulse Waveform(8/20µs)

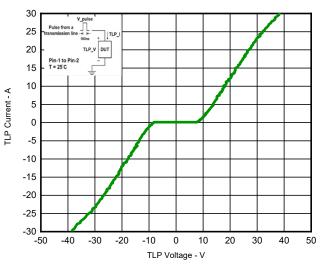


Fig 2.Power Derating Curve

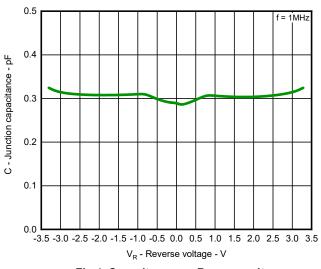


Fig 3. TLP Measurement

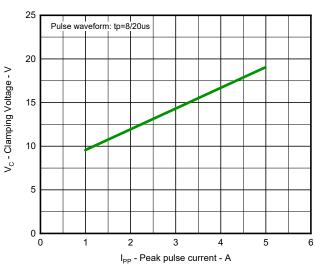


Fig 4. Capacitance vs. Reveres voltage

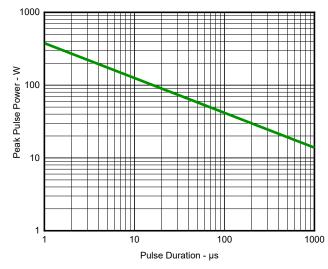
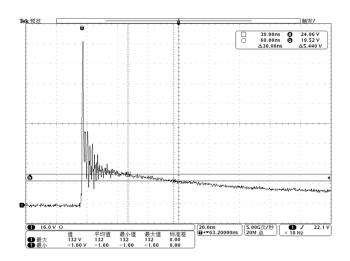


Fig 5. Clamping voltage vs. Peak pulse current

Fig 6. Non Repetitive Peak Pulse Power vs. Pulse time



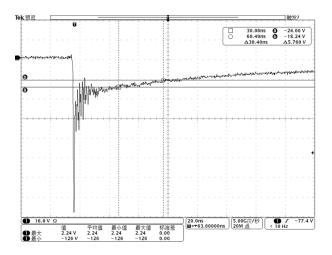
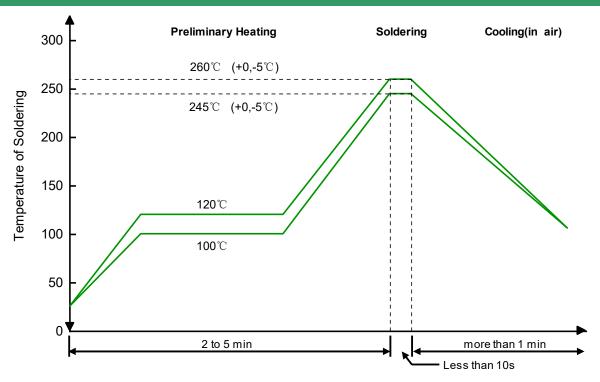


Fig 7.ESD Clamping voltage (IEC61000-4-2 +8kV contact)

Fig 8.ESD Clamping voltage (IEC61000-4-2 -8kV contact)

Solder Reflow Recommendation



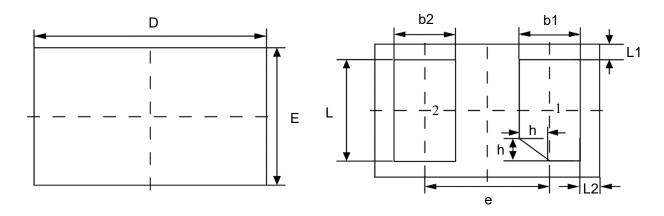
Remark: Pb free for 260°C; Pb for 245°C.

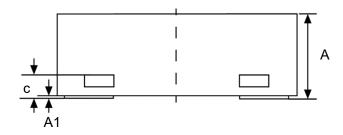
PCB Design

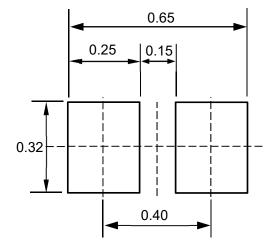
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN0603-2L)







Dim	Millimeters				
ווווט	MIN	Тур.	MAX		
Α	0.28	0.30	0.35		
A1	0	0.02	0.05		
b1	0.13	0.18	0.23		
b2	0.14	0.19	0.24		
С	0.05	0.1	0.15		
D	0.55	0.60	0.65		
е	0.35BSC				
L1	0.025BSC				
L2	0.035BSC				
Е	0.25	0.30	0.35		
L	0.20	0.25	0.30		
h	0	0.05	0.10		

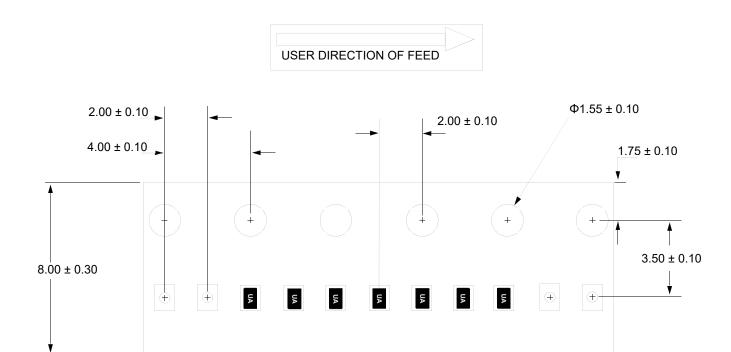
Unit:mm

Suggested PCB Layout

Ordering information

Device	Package	Reel	Shipping	
PESDUC2XD3V3BF	DFN0603-2L (Pb-Free)	7"	12000 / Tape & Reel	

Load with information



Unit: mm

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