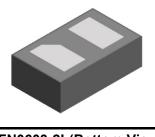


Bi-directional 20V Ultra Low Capacitance ESD Protector

Description

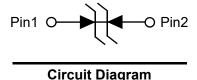
The PESDREC2XD20VBF is an ultra low capacitance ESD protection device specifically designed to protect high-speed lines. It protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.

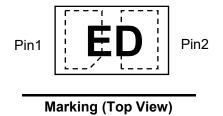


DFN0603-2L(Bottom View)

Feature

- ➤ Ultra-Low capacitance: 0.15pF
- \gt 24W peak pulse power per line (t_P = 8/20µs)
- ➤ DFN0603-2L package
- > Response time is typically < 1 ns
- > Bidirectional configurations
- Low clamping voltage
- > RoHS compliant
- ➤ Transient protection for data lines to IEC 61000-4-2(ESD) ±10kV(air), ± 9kV(contact); IEC 61000-4-5 (Lightning) 3.5A (8/20us)





Applications

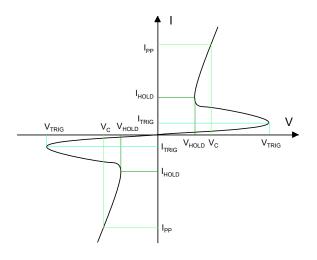
- > Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- > Portable instrumentation
- > Cordless phones
- Digital cameras
- > Peripherals

Mechanical Characteristics

- > Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- > DFN0603-2L without plating

Electronics Parameter

Symbol	Parameter			
V_{RWM}	Peak Reverse Working Voltage			
I _R	Reverse Leakage Current @ V _{RWM}			
V _{BR}	Breakdown Voltage @ I _T			
I _T	Test Current			
I _{PP}	Maximum Reverse Peak Pulse Current			
V _C	Clamping Voltage @ I _{PP}			
P _{PP}	Peak Pulse Power			
CJ	Junction Capacitance			
I _F	Forward Current			
V _F	Forward Voltage @ I _F			



Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}	-	-	-	20	V
Breakdown Voltage	V_{BR}	I _t = 1mA	28	-	36	V
Reverse Leakage Current	I _R	V _{RWM} = 20V	-	1	1.0	μΑ
Clamping Voltage ²⁾	V _C	$I_{PP} = 3.5A, t_{P} = 8/20 \mu s$	-	5.8	7.0	٧
Junction Capacitance	CJ	$V_R = 0V, f = 1MHz$	-	0.15	0.19	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power (t _P = 8/20μs)	P _{PP}	24	W
Peak Pulse Current (t _P = 8/20μs)	I _{PP}	3.5	A
Lead Soldering Temperature	T _L	260 (10 sec)	°C
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	°C
ESD Protection-Contact Discharge	V _{ESD}	±9	kV
ESD Protection-Air Discharge	V _{ESD}	±10	kV

Typical Characteristics

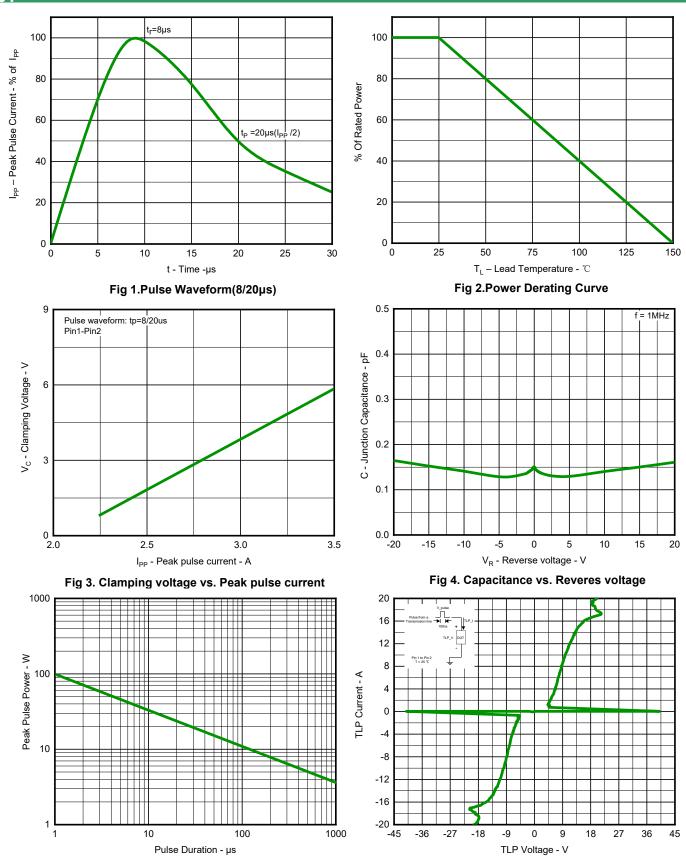
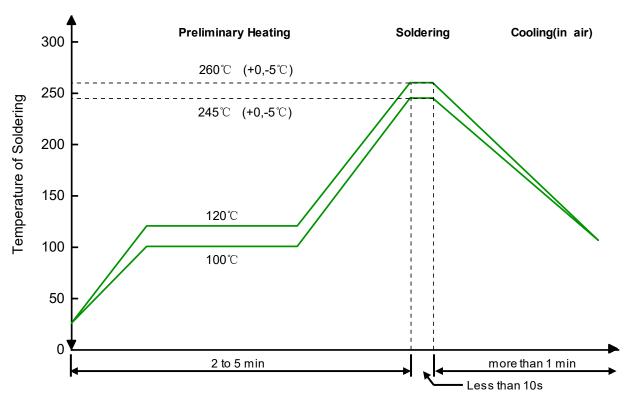


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time Fig 6. TLP Measurement

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

PCB Design

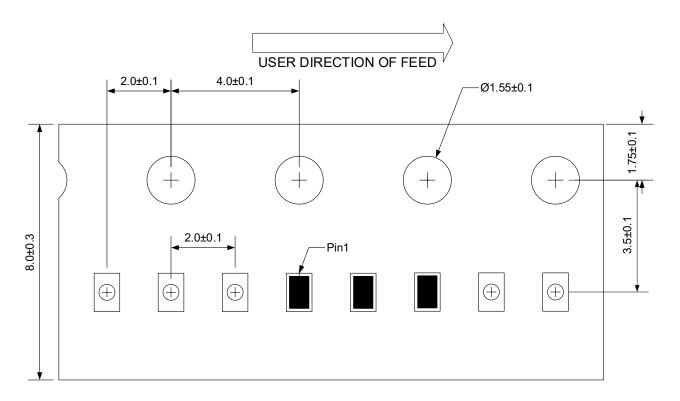
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

Ordering information

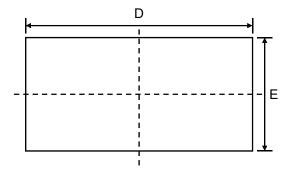
Device	Package	Reel	Shipping
PESDREC2XD20VBF	DFN0603-2L (Pb-Free)	7"	12000 / Tape & Reel

Load with information

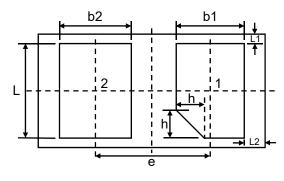


Unit:mm

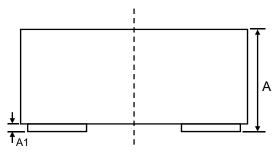
Product dimension (DFN0603-2L)



Top View

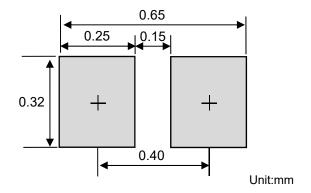


Bottom View



Side View

Dim	Millimeters		Inches		
Dilli	Min	Max	Min	Max	
Α	0.28	0.35	0.011	0.014	
A1	0.00	0.05	0.000	0.002	
b1	0.12	0.24	0.005	0.009	
b2	0.13	0.24	0.005	0.009	
D	0.55	0.65	0.022	0.026	
Е	0.25	0.35	0.010	0.014	
е	0.35 BSC		0.014 BSC		
L	0.18	0.30	0.007	0.012	
L1	0.025 BSC		0.001 BSC		
L2	0.035 BSC		0.001 BSC		
h	0.00	0.10	0.000	0.004	



Suggested PCB Layout

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