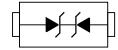


PESDNC9D5VB ESD Protector

Description

The PESDNC9D5VB protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



Feature

- 80W peak pulse power per line (t_P = 8/20µs)
- SOD-923 package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC61000-4-2(ESD) ±30KV(air), ±30KV(contact);
 IEC61000-4-4 (EFT) 40A (5/50ns)

Mechanical Characteristics

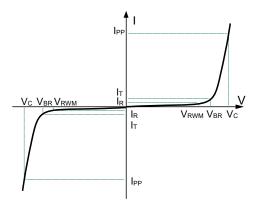
- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter	
VRWM	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V _{BR}	Breakdown Voltage @ I⊤	
IT	Test Current	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	
IF	Forward Current	
V _F	Forward Voltage @ I _F	

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies



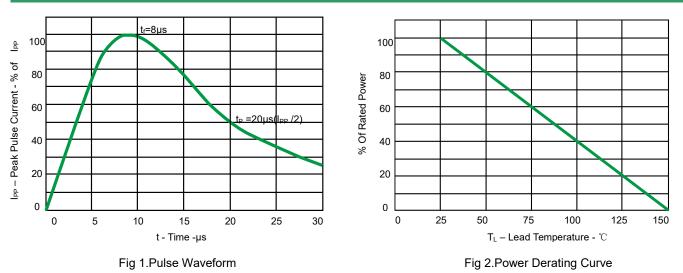
PESDNC9D5VB

Electrical characteristics per line@25 $^\circ C$ (unless otherwise specified)						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				5	V
Breakdown Voltage	V _{BR}	It = 1mA	5.6	6.7	7.8	V
Reverse Leakage Current	I _R	V _{RWM} = 5V Т=25℃			1.0	μA
Maximum Reverse Peak Pulse Current	I _{PP}			5.5		А
Clamping Voltage	Vc	I _{PP} =1A			10	V
Clamping Voltage	Vc	I _{PP} =3A			13	V
Clamping Voltage	Vc	I _{PP} =5A			15	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		12	15	pF

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	80	W
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Typical Characteristics



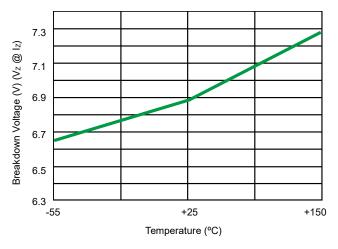


Fig 3.Typical Breakdown Voltage vs. Temperature

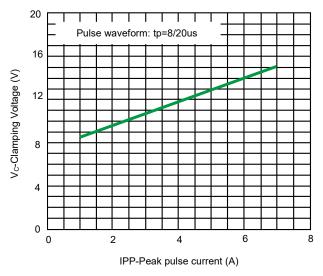
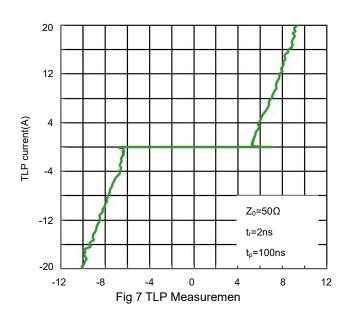


Fig 5. Clamping voltage vs. Peak pulse current



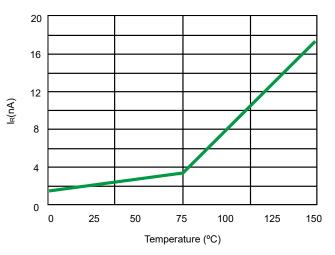


Fig 4. Typical Leakage Current vs. Temperature

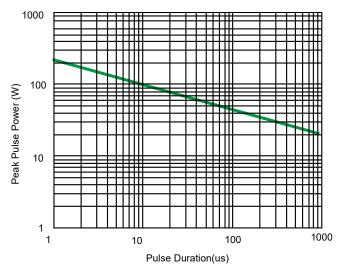


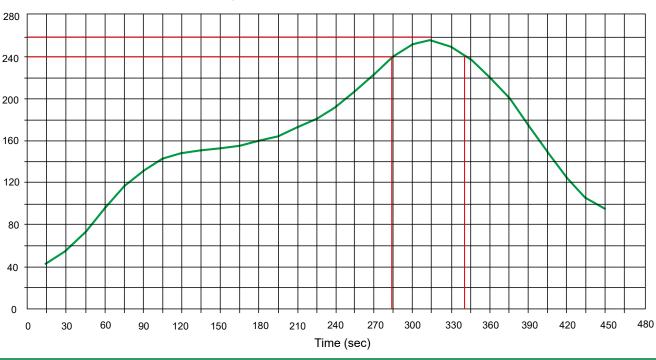
Fig 6. Non-Repetitive Peak Pulse Power vs. Pulse time

or _____

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Solder Reflow Recommendation



Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

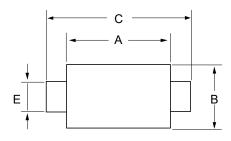
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

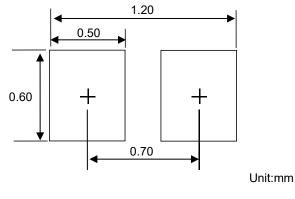
PESDNC9D5VB

Product dimension (SOD-923)



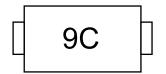


Dim	Inches		Millimeters		
Dim	MIN	MAX	MIN	МАХ	
А	0.030	0.033	0.75	0.85	
В	0.022	0.026	0.55	0.65	
С	0.037	0.041	0.95	1.05	
D	0.014	0.017	0.36	0.43	
E	0.006	0.010	0.15	0.25	
F	0.002	0.006	0.05	0.15	
н	0.003	0.007	0.07	0.17	



Suggested PCB Layout

Marking information



Ordering information

Device Package		Reel	Shipping		
PESDNC9D5VB	SOD-923 (Pb-Free)	7"	8000 / Tape & Reel		

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