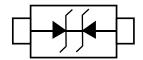


Description

The PESDNC3D8VBL1 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



Feature

- 30W peak pulse power per line (t_P = 8/20µs)
- SOD-323 package
- Replacement for MLV(0805)
- Bidirectional configurations
- Protects one power or I/O port
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±8KV(air), ±8KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- Laptop computers
- Cellular phones
- Digital cameras
- PDAs

Mechanical Characteristics

Lead finish:100% matte Sn(Tin)

Mounting position: Any

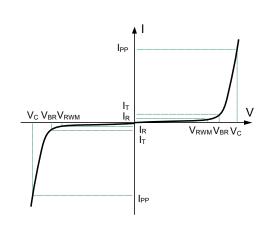
➤ Qualified max reflow temperature:260°C

➤ Pure tin plating: 7 ~ 17 um

Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter
V _{RWM}	Peak Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V_{BR}	Breakdown Voltage @ I _T
lτ	Test Current
I _{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ I _{PP}
P _{PP}	Peak Pulse Power
CJ	Junction Capacitance
l _F	Forward Current
V _F	Forward Voltage @ I _F



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				8	V
Breakdown Voltage	V _{BR}	I _t = 1mA	9	11	-	V
Reverse Leakage Current	I _R	V _{RWM} = 8V T=25°C			1.0	μΑ
Clamping Voltage	Vc	$I_{PP} = 1A$ $t_P = 8/20 \mu s$		11.5	13.5	V
Clamping Voltage	V _C	I_{PP} =2A $t_P = 8/20 \mu s$		13.5	15.5	V
Junction Capacitance	C _j	V _R =0V f = 1MHz		8	12	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Unidirectional Peak Pulse Power	P _{pp}	30	W
Operating Temperature	TJ	-55 to +150	$^{\circ}\!\mathrm{C}$
Storage Temperature	T _{STG}	-55 to +150	$^{\circ}$

Typical Characteristics

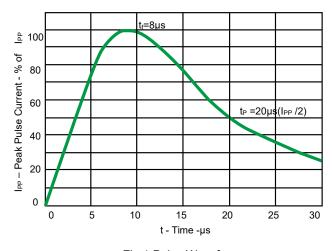


Fig 1.Pulse Waveform

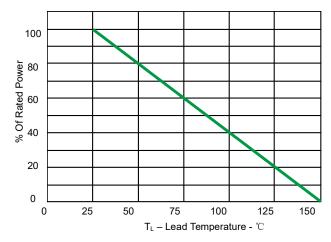


Fig 2.Power Derating Curve

1000

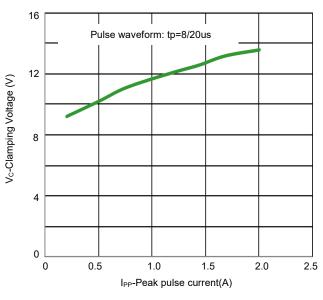


Fig 3. Clamping voltage vs. Peak pulse current

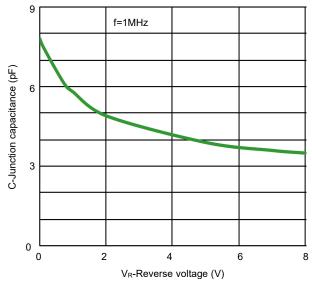
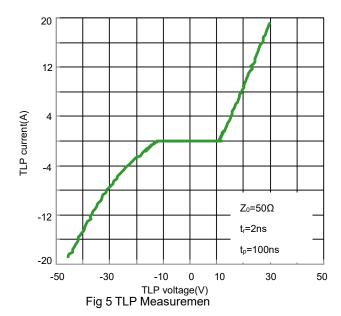


Fig 4. Capacitance vs. Reveres voltage



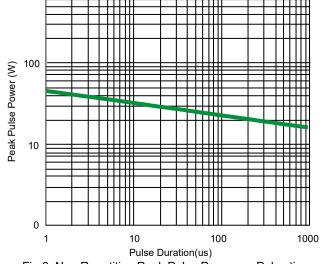
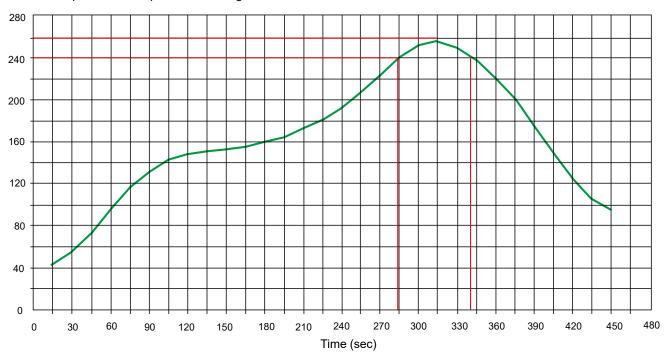


Fig 6. Non Repetitive Peak Pulse Power vs. Pulse time

Solder Reflow Recommendation

Peak Temp=257℃, Ramp Rate=0.802deg. ℃/sec

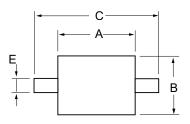


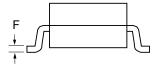
PCB Design

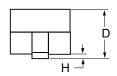
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

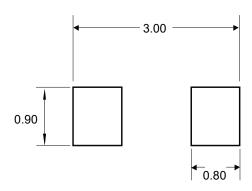
Product dimension (SOD-323)







Dim	Inches		Millimeters		
Dim	MIN	MAX	MIN	MAX	
Α	0.063	0.075	1.60	1.90	
В	0.045	0.057	1.15	1.45	
С	0.090	0.106	2.30	2.70	
D	0.031	0.043	0.80	1.10	
E	0.010	0.01	0.25	0.40	
F	0.004	0.007	0.09	0.18	
Н	0.000	0.004	0.00	0.10	



Suggested PCB Layout

Unit:mm

Marking information



Ordering information

Device	Package	Reel	Shipping	
PESDNC3D8VBL1	SOD-323 (Pb-Free)	7"	3000 / Tape & Reel	

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