

PESDNC2XD3V3U

Uni-directional 3.3V Normal Capacitance ESD Protector

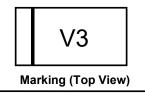
Description

The PESDNC2XD3V3U protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



DFN0603-2L(Bottom View)

Pin 1 Pin 2 Circuit Diagram



Feature

- DFN0603-2L package
- Replacement for MLV(0201)
- Response time is typically < 1 ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30KV(air), ±30KV(contact).

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

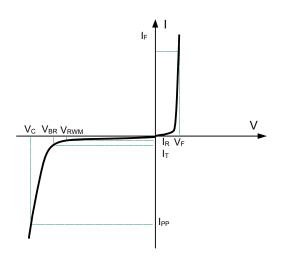
Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- DFN0603-2L without plating

PESDNC2XD3V3U

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
IR	Reverse Leakage Current @ VRWM		
V _{BR}	Breakdown Voltage @ I⊤		
Iτ	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
IF	Forward Current		
VF	Forward Voltage @ I⊧		



Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				3.3	V
Breakdown Voltage	V _{BR}	I _t = 1mA		5		V
Reverse Leakage Current	I _R	V _{RWM} =3.3V T=25℃			1	μA
Clamping Voltage	Vc	I _{PP} =16A t _p =100ns		11.5		A
Clamping Voltage	Vc	I _{PP} = 1Α t _P = 8/20μs		7.0	9.0	V
Clamping Voltage	Vc	I _{PP} = 6A t _P = 8/20μs		9.6	11.5	V
Junction Capacitance	Cj	V _R =0V f = 1MHz			39	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P_{pp}	60	W
Peak Pulse Current (t _P =8/20µs)	Ірр	6	А
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

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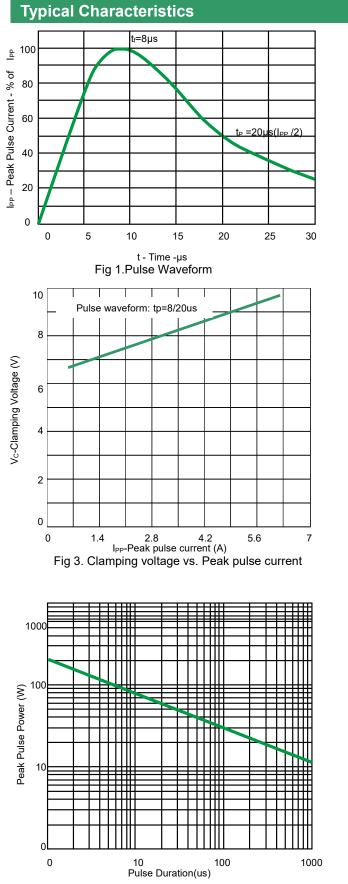
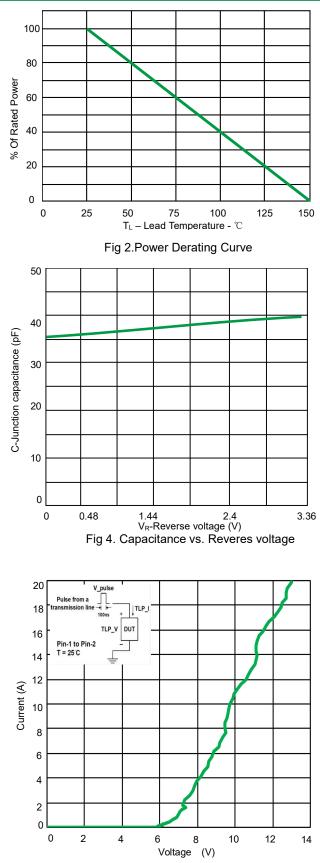


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time



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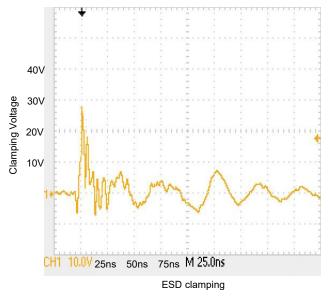
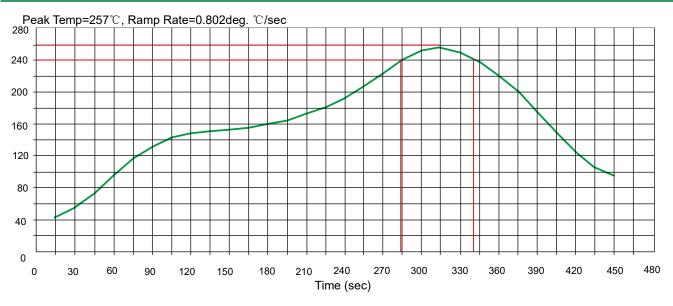


Fig 7. (8kV contact discharge per IEC61000-4-2)





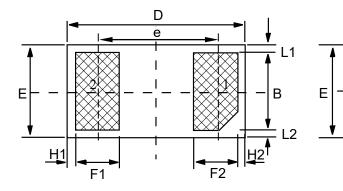
PCB Design

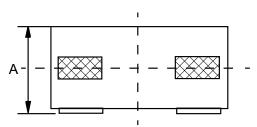
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

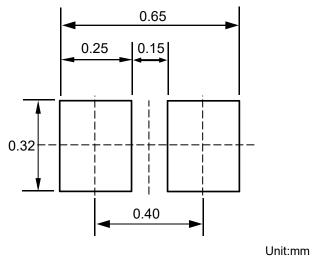
- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

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Product dimension (DFN0603-2L)







Suggested PCB Layout

Dim	Millimeters			
Dim	MIN	Тур.	MAX	
А	0.270	0.300	0.340	
В	0.200	0.250	0.300	
D	0.550	0.600	0.650	
E	0.250	0.300	0.350	
е	-	0.350	-	
F1	0.130	0.180	0.230	
F2	0.130	0.180	0.230	
L1	0.015	0.030	0.045	
L2	0.015	0.030	0.045	
H1	0.030	0.045	0.060	
H2	0.030	0.045	0.060	

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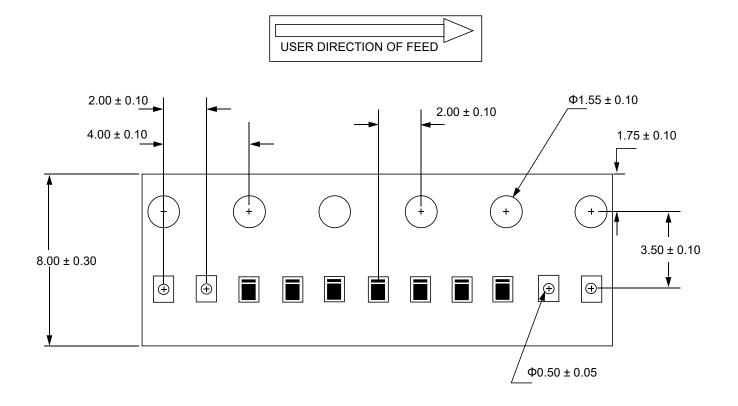
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Ordering information

Device	Package	Reel	Shipping
PESDNC2XD3V3U	DFN0603-2L (Pb-Free)	7"	10000 / Tape & Reel

PESDNC2XD3V3U

Load with information



Unit: mm

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