

Uni-directional 3.3V Normal Capacitance ESD Protector

Description

The PESDNC2FD3V3U ESD protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PESDNC2FD3V3U protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PESDNC2FD3V3U is available in a DFN1006-2L package with working voltages of 3.3 volt. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.



DFN1006-2L(Bottom View)

Feature

- \gt 50W peak pulse power per line (t_P = 8/20µs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Unidirectional configurations
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

Pin 1 Circuit Diagram

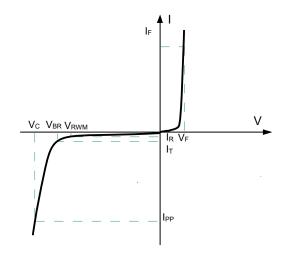


Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- DFN1006-2L without plating

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I⊤		
lτ	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ I _{PP}		
P _{PP}	Peak Pulse Power		
Сл	Junction Capacitance		
IF	Forward Current		
V _F	Forward Voltage @ I _F		



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Working Voltage	V _{RWM}				3.3	V
Breakdown Voltage	V _{BR}	I _t =1mA	4.0			V
Reverse Leakage Current	I _R	V _{RWM} =3.3V			1	μΑ
Forward Voltage	VF	I _F =10mA		0.8		V
Clamping Voltage	Vc	TLP = 16A, tp = 100ns		9.8		V
Dynamic resistance	R _{DYN}			0.2		Ω
Clamping Voltage	Vc	$I_{PP}=1A$ $t_{P}=8/20\mu S$		6.5	8.0	V
Clamping Voltage	Vc	$I_{PP}=5A$ $t_{P}=8/20\mu S$		8.0	10.0	V
Junction Capacitance	C _j	V _R =0V f = 1MHz		35	50	pF
Junction Capacitance	Cj	$V_R=2.5V$ f = 1MHz		20	27	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t₂ = 8/20μS)	P _{pp}	50	W
Lead Soldering Temperature	TL	260 (10 sec)	°C
Operating Temperature	TJ	-55 to 125	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Typical Characteristics

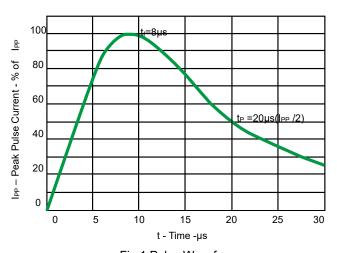


Fig 1.Pulse Waveform

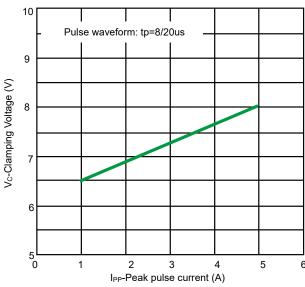


Fig 3. Clamping voltage vs. Peak pulse current

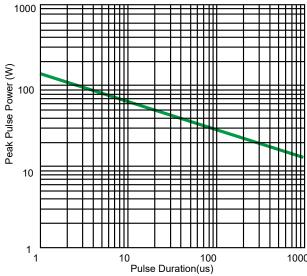


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

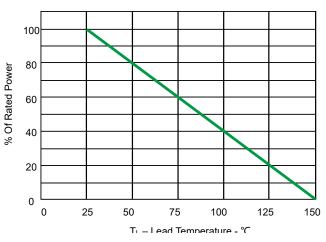


Fig 2.Power Derating Curve

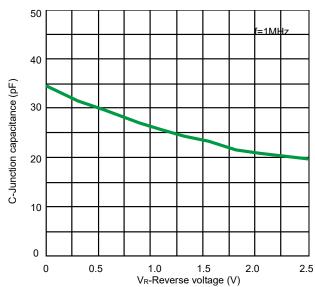


Fig 4. Capacitance vs. Reveres voltage

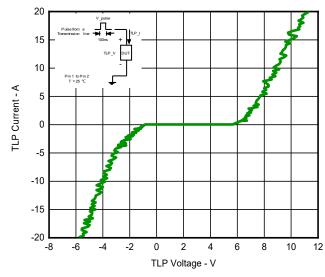
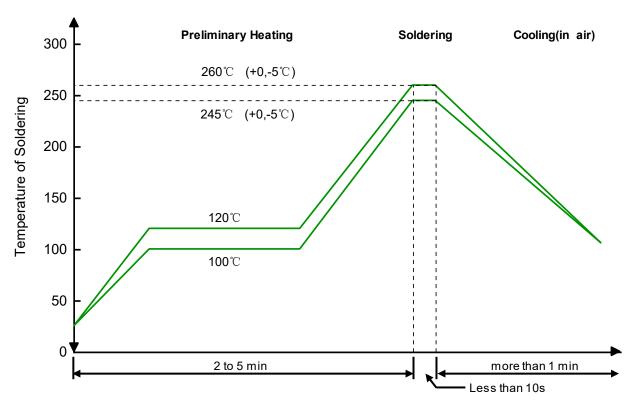


Fig 6. TLP Measurement

Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

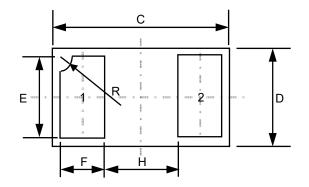
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

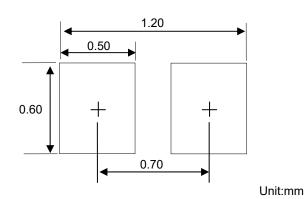
- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN1006-2L)





Dim	Inc	hes	Millimeters		
	MIN	MAX	MIN	MAX	
Α	0.013	0.020	0.34	0.498	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.026	0.55	0.65	
Е	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40Тур.		
R	0.001	0.005	0.05	0.15	

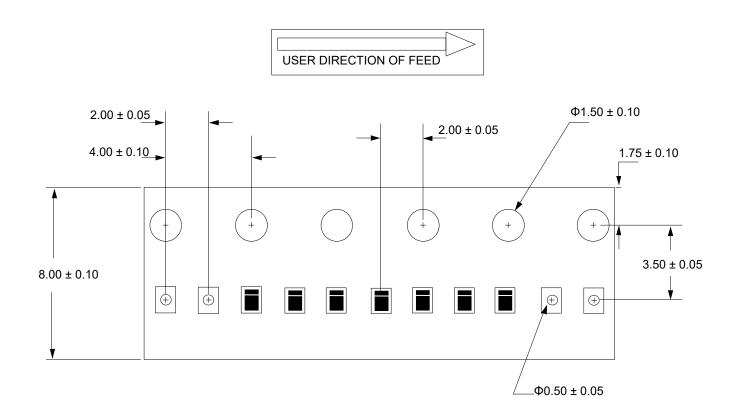


Suggested PCB Layout

Ordering information

Device	Package	Reel	Shipping
PESDNC2FD3V3U	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

Load with information



Unit: mm

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