

## Surface mount transient voltage suppressor power 200 watts

#### **Description**

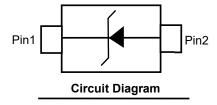
The PTVSHC1DF33VU transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC1DF33VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC1DF33VU is available in a SOD-123FL package with working voltages of 33 volt.



SOD-123FL(Top View)

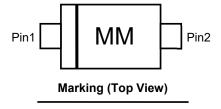
#### **Feature**

- > 200W Peak pulse power per line (t<sub>P</sub> =10/1000µs)
- > 2200W Peak pulse power per line (t<sub>P</sub> = 8/20µs)
- SOD-123FL package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
   ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 80A (5/50ns),
   IEC 61000-4-5 (Lightning) 200A (8/20us)



#### **Applications**

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players



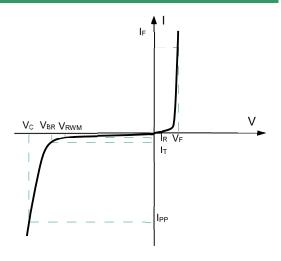
#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- ➤ Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

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## **Electronics Parameter**

Symbol	Parameter
V <sub>RWM</sub>	Peak Reverse Working Voltage
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>
V <sub>BR</sub>	Breakdown Voltage @ I⊤
lτ	Test Current
IPP	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ I <sub>PP</sub>
P <sub>PP</sub>	Peak Pulse Power
CJ	Junction Capacitance
IF	Forward Current
VF	Forward Voltage @ I <sub>F</sub>



## Electrical characteristics per line@25℃( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working	V <sub>RWM</sub>				33.0	V
Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> =1mA	36.7		40.6	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =33V			1.0	μΑ
Clamping Voltage	Vc	I <sub>PP</sub> = 38A t <sub>P</sub> = 8/20μs		58.0	62.0	V
Clamping Voltage	Vc	$I_{PP}$ = 3.8A $t_P$ = 10/1000 $\mu$ s		50.5	53.3	V
Junction Capacitance	Cj	V <sub>R</sub> =0V f = 1MHz		230.0	260.0	pF

## Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power ( $t_P = 10/1000 \mu S$ ) <sup>(1)</sup>	P <sub>PPM</sub>	200	W
Peak Pulse Power ( t <sub>P</sub> =8/20µS ) <sup>(2)</sup>	P <sub>PPM</sub>	2200	°C
Typical Thermal Resistance <sup>(3)</sup>	R <sub>ΘJA</sub>	120	°C/W
Operating Junction and Storage Temperature	$T_{J}$ , $T_{STG}$	-55 to 150	°C

Note1: Non-repetitive current pulse per Fig1

Note2: Non-repetitive current pulse per Fig2

Note3: Mounted on P.C.B. with 5mm² copper pads to each terminal.

## **Typical Characteristics**

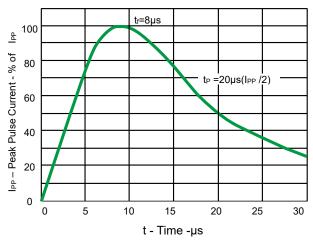


Fig 1.Pulse Waveform

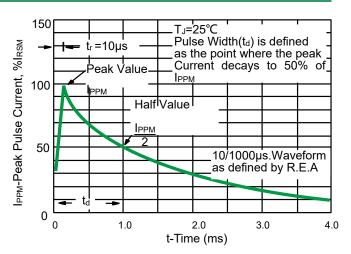
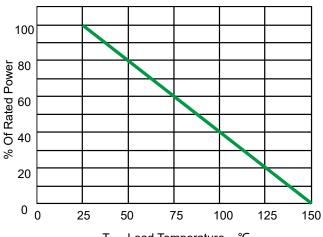


Fig 2.Pulse Waveform



 $T_L$  – Lead Temperature - °C Fig 3.Power Derating Curve

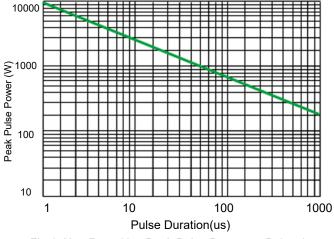


Fig 4. Non Repetitive Peak Pulse Power vs. Pulse time

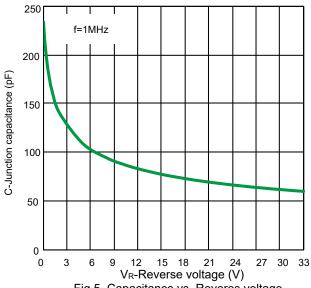
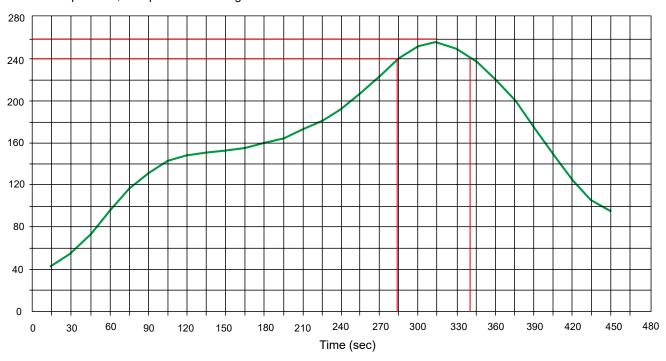


Fig 5. Capacitance vs. Reveres voltage

### **Solder Reflow Recommendation**

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

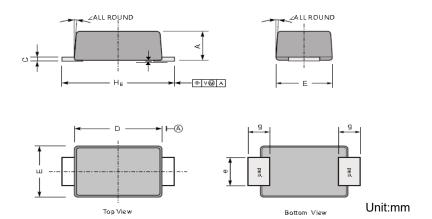


#### **PCB Design**

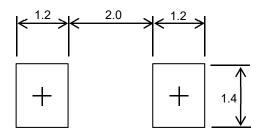
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

## Product dimension (SOD-123FL)



Dim	Inc	hes	Millimeters		
Dilli	MIN	MAX	MIN	MAX	
Α	0.031	0.047	0.80	1.20	
С	0.002	0.010	0.05	0.25	
HE	0.138	0.154	3.50	3.90	
Е	0.061	0.077	1.55	1.95	
D	0.098	0.114	2.50	2.90	
g	0.020	0.043	0.50	1.10	
е	0.024	0.039	0.60	1.00	
k	0.004		0.10		
	7°				



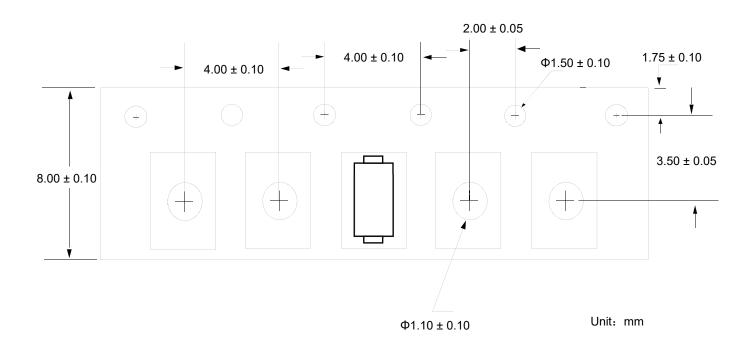
Suggested PCB Layout Unit:mm

## **Ordering information**

Device	Package	Shipping
PTVSHC1DF33VU	SOD-123FL (Pb-Free)	3000 / Tape & Reel

## Load with information





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