

# **Digital Transistor (built-in resistors)**

#### Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

# B C C

Top view



Marking (Top View)

#### **Applications**

- Inverter
- Interface
- Driver

#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- ➤ Pure tin plating: 7 ~ 17 um
- ▶ Pin flatness:≤3mil

#### **Structure**

PNP epitaxial planar silicon transistor (Resistor built-in type)

## Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Input voltage	$V_{I(off)}$	V <sub>CC</sub> =-5V,I <sub>O</sub> =-100μA	-	-	-0.5	V
	$V_{I(on)}$	Vo=-0.3V,lo=-10mA	-3	ı	-	V
Output voltage	$V_{O(off)}$	I <sub>O</sub> /I <sub>I</sub> =-10mA/-0.5mA	-	-0.1	-0.3	V
Input current	II	V <sub>I</sub> =-5V	-	-	-0.88	mA
Output current	I <sub>O(off)</sub>	Vcc=-50V, V <sub>I</sub> =0V	-	-	-0.5	μA
DC current gain	G <sub>1</sub>	Vo=-5V, Io=-5mA	30	-	-	-
Input resistance	R <sub>1</sub>	-	7	10	13	ΚΩ
Resistance ration	R <sub>2</sub> /R <sub>1</sub>	-	0.88	1	1.2	-
Transition frequency	f⊤	V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz	-	250	-	MHz

# Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Supply voltage	Vcc	-50	V
Input voltage	V <sub>IN</sub>	-40 to +10	V
Output ourront	lo	-50	mA
Output current	Ic(MAX.)	-100	mA
Power dissipation	P <sub>d</sub>	150	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	$^{\circ}$ C

# **Typical Characteristics**

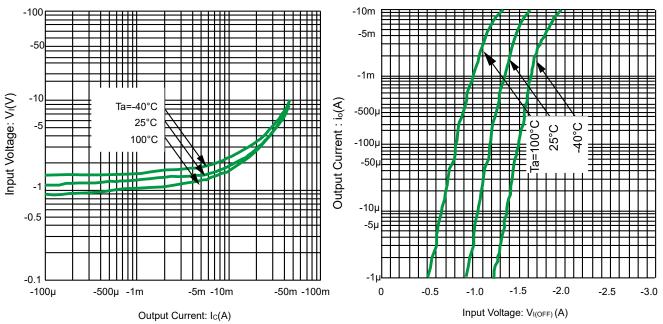
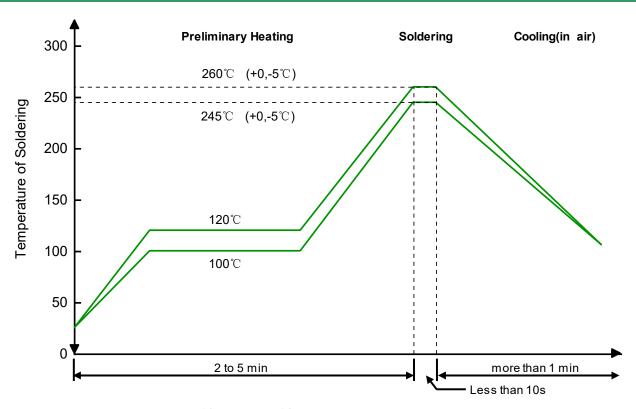


Fig 1. Input Voltage vs. output current @V<sub>C</sub>=-0.3V (ON characteristics)

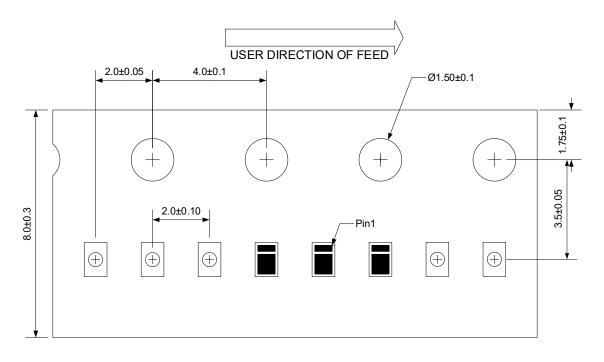
Fig 2.Output current vs. input voltage @Vcc=-5V(OFF characteristics)

## **Solder Reflow Recommendation**



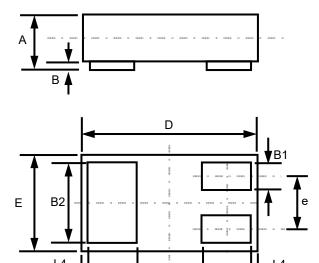
Remark: Pb free for 260°C; Pb for 245°C.

## Load with information

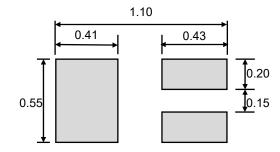


Unit:mm

# Product dimension (DFN1006-3L)



Dim	Millimeters				
Dim	MIN	Тур	MAX		
Α	0.33	0.47	0.50		
В	0.00	0.03	0.05		
B1	0.10	0.15	0.20		
B2	0.45	0.50	0.55		
D	0.85	1.00	1.15		
E	0.45	0.60	0.75		
е	1	0.35	1		
L1	0.20	0.25	0.30		
L2	0.20	0.25	0.30		
L3	-	0.39			
L4		0.05			



Unit: mm

Suggested PCB Layout

# Ordering information

Device	Package	Shipping
PDTA114EN	DFN1006-3L (Pb-Free)	10000 / Tape & Reel

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