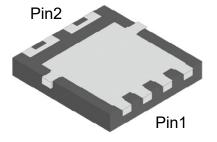


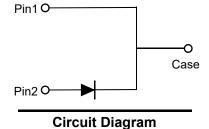
Schoktty Barrier Diode

Feature

- > Negligible reverse recovery
- ➤ Positive Temperature Coefficient
- > Temperature-Independent Switching
- > Fast switching
- > Pb-free / RoHS compliant
- ➤ Low switching loss
- ➤ Higher frequency
- > Low heat dissipation requirements
- > Reduce size and cost of the system
- ➤ High-reliability



DFN5060-8L Bottom View



Applications

- Power inverters
- Uninterruptable power supplies
- ➤ High performance SMPS
- Power Factor Correction

Absolute maximum rating@25°C

Parameter			Value	Units
Repetitive Peak Reverse Voltage		V_{RRM}	650	V
Surge Peak Reverse Voltage		V _{RSM}	650	V
DC Peak Reverse Voltage		V _R	650	V
Continuous Forward Current	T _c =25°C		23	A
	T _c =135°C	I _F	12	
	T _c =162°C		6.0	
Repetitive Peak Forward Surge Current	T _c =25°C,t _p =10ms,Half Sine Pulse		28	А
	T _c =110°C,t _p =10ms,Half Sine Pulse	I _{FRM}	17	
Non-repetitive Forward Surge Current	T _c =25°C,t _p =10ms,Half Sine Pulse		48	А
	T _c =110°C,t _p =10ms,Half Sine Pulse	l _{FSM}	43	
i²t Value	T _c =25°C,t _p =10ms	∫i² dt	11.4	A ² s
	T _c =110°C,t _p =10ms	ון ווי מנ	9.1	
Power Dissipation	T _c =25°C	В	71	w
	T _c =110°C	P _{tot}	30	
Operating Junction Range		T _J	-55~+175	°C
Storage Temperature Range		T _{STG}	-55~+150	°C

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Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Voltage	V _F	$I_F = 6A, T_J = 25^{\circ}C$	-	1.3	1.5	V
		I _F = 6A, T _J =175°C	-	1.5	-	
Reverse Current	I _R	$V_R = 650V, T_J = 25^{\circ}C$	-	-	50	μΑ
		V _R = 650V, T _J =175°C	-	-	200	
Total Capacitive Charge	Q _C	V _R = 400V	-	18	-	nC
Total Capacitance	С	$V_R = 0V, f = 1MHz$	-	358	-	pF
		V _R = 200V,f = 1MHz	-	36	-	
		V _R = 400V,f = 1MHz	-	30	-	

Thermal Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Units
Thermal Resistance (Junction to case)	$R_{ heta JC}$	-	2.10	-	°C/W

Typical Characteristics

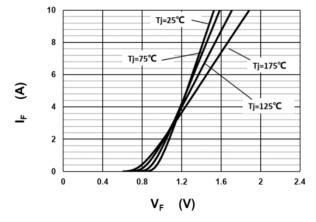


Fig.1 Forward Characteristics

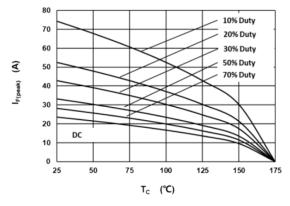


Fig.3 Current Derating

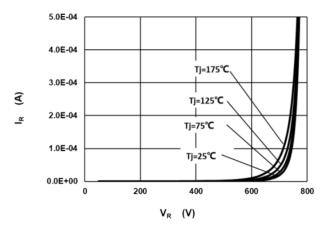


Fig.2 Reverse Characteristics

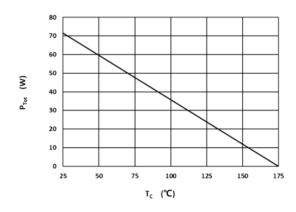


Fig.4 Power Derating

Schoktty Barrier Diode

PSICS8N650V6N

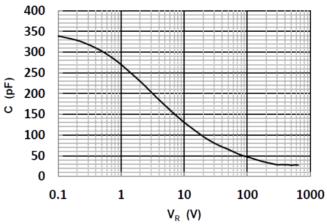


Fig.5 Capacitance vs. Reverse Voltage

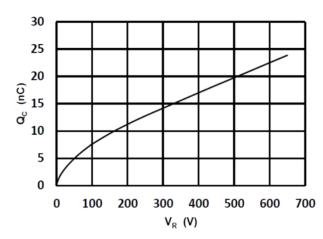


Fig.6 Reverse Charge vs. Reverse Voltage

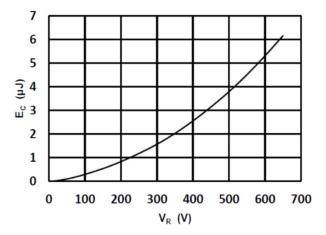


Fig.7 Capacitance Stored Energy

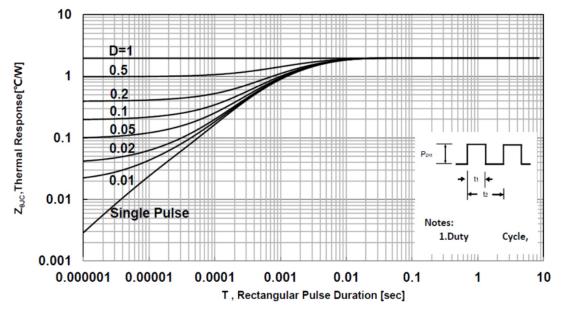
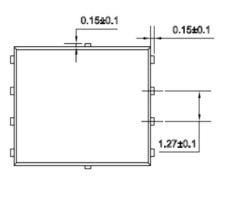
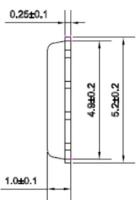
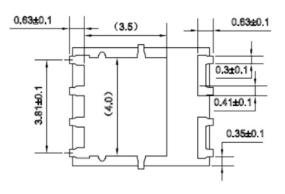


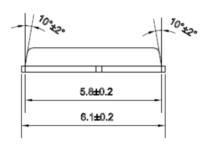
Fig.8 Transient Thermal Impedance

Product Dimension (DFN5060-8L)









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