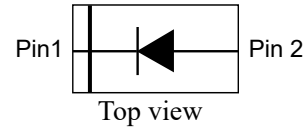


Feature

- Small mold type. (DFN1608-2L)
- Low I_R
- High reliability.



Applications

- Low current rectification

Construction

- Silicon epitaxial planar

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 μm
- Pin flatness : $\leq 3\text{mil}$

Electrical characteristics per line@25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_F	-	0.57	0.63	V	$I_F=1\text{A}$
Forward voltage	V_F	-	0.45	0.50	V	$I_F=0.5\text{A}$
Reverse current	I_R	-	-	0.1	mA	$V_R=40\text{V}$
Junction Capacitance	C_j	-	90	-	pF	$V_R=0\text{V}$ $f=1\text{MHz}$

Absolute maximum rating@25°C

Parameter	Symbol	limits	Unit
Reverse voltage (DC)	V_{RM}	40	V
Average rectified forward current	I_o	1	A
Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I_{FSM}	7	A
Operating Junction temperature Range	T_j	-55 to +125	°C
Storage temperature	T_{stg}	-40 to +125	°C

Typical Characteristics

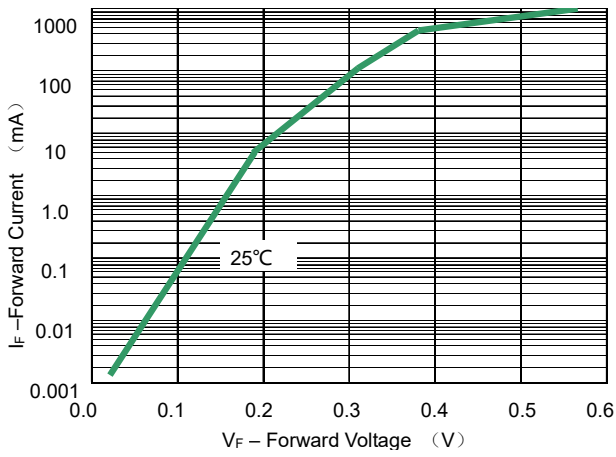


Fig 1. Forward Voltage

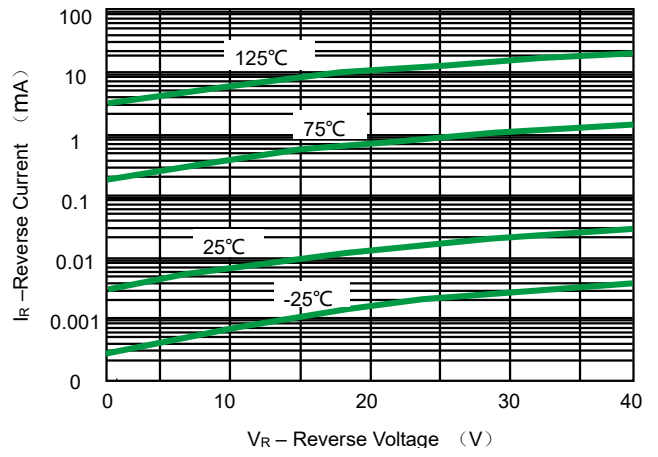
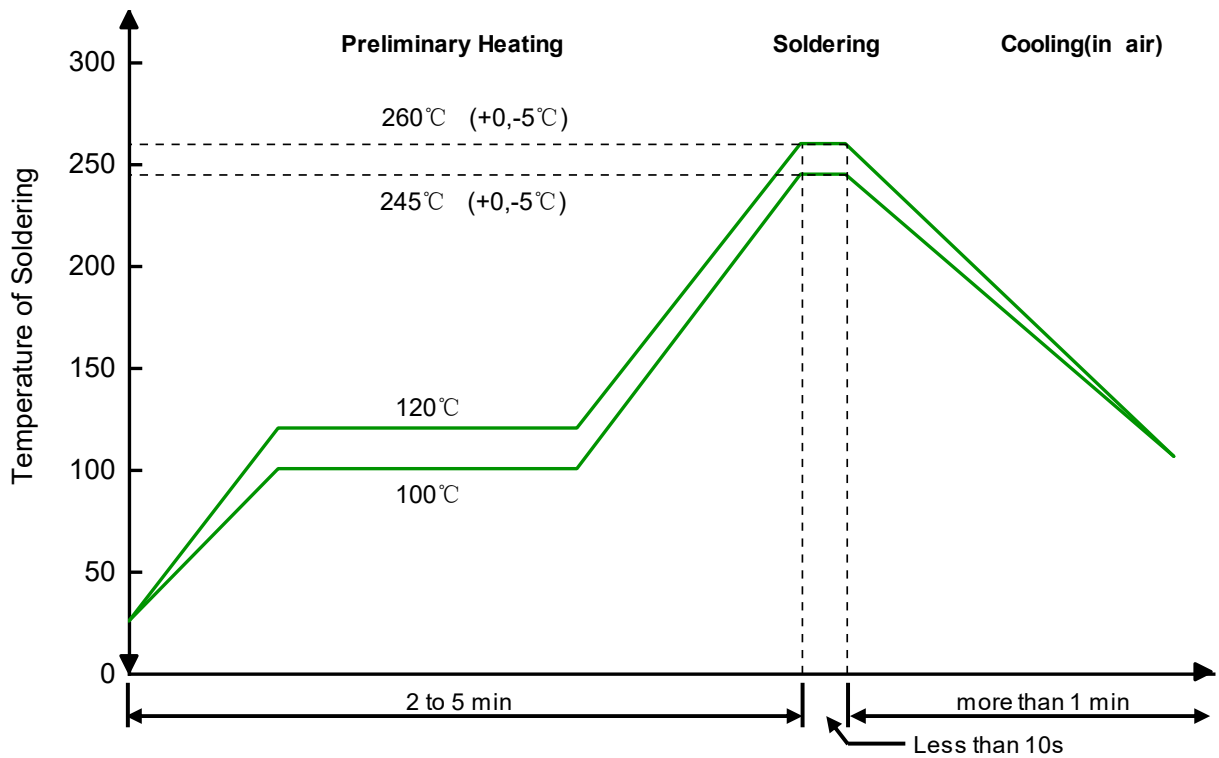


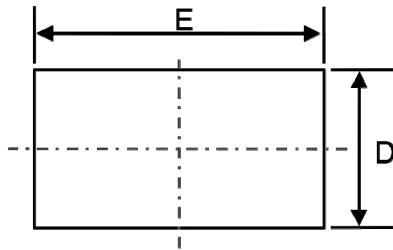
Fig 2. Leakage Current

Solder Reflow Recommendation

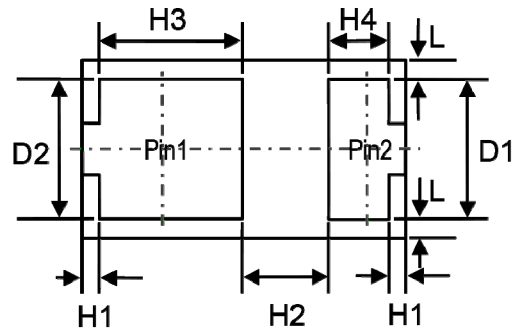


Remark: Pb free for 260°C; Pb for 245°C.

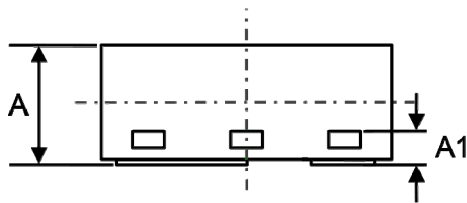
Product dimension (DFN1608-2L)



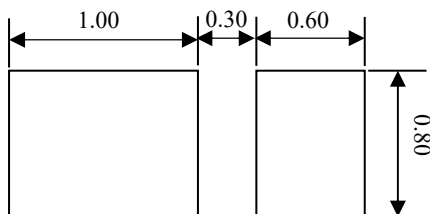
TOP VIEW



BOTTOM VIEW



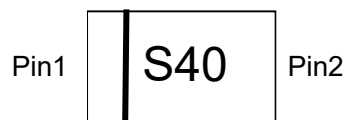
SIDE VIEW



Dim	Millimeters	
	MIN	MAX
A	0.53	0.65
A1	0.13 Ref.	
E	1.55	1.65
D	0.75	0.85
D1	0.60	0.80
D2	0.60	0.80
L	0.025	0.075
H1	0.025	0.075
H2	0.38 Ref.	
H3	0.76 Ref.	
H4	0.36 Ref.	

Notes: This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

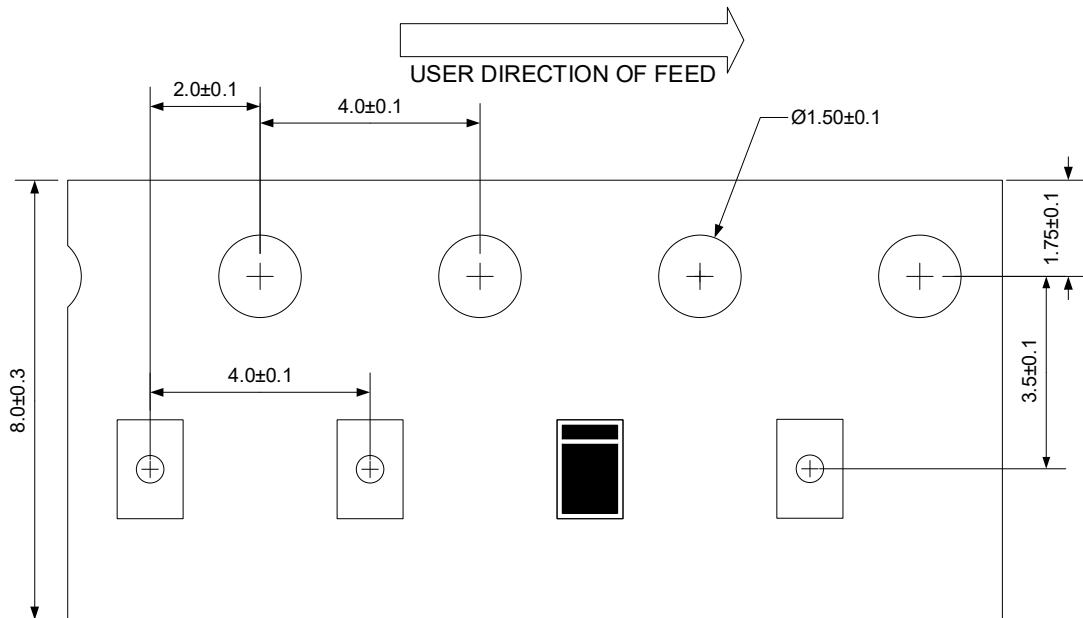
Marking information



Ordering information


Device	Package	Reel	Shipping
PSBD2ED40V1H	DFN1608-2L(Pb-free)	7"	5000 / Tape & Reel

Load with information



Unit:mm


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