

## **650V Enhancement-mode GaN Transistor**

## Description

650V Normally-OFF GaN							
V <sub>DS</sub> (V)	$R_{DS(on)}(m\Omega)$	I <sub>DS</sub> (A)	Q <sub>G</sub> (nC)				
650	160	16	7.9				

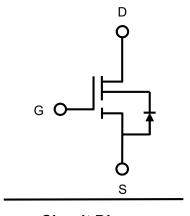
#### **Feature**

- Normally-off device combines high voltage GaN HEMT and low voltage silicon MOSFET
- Normally off power switch
- Low reverse-recovery charge
- ➤ High switching frequency
- ➤ Low gate charge, low output charge
- Qualified for industrial applications according to JEDEC Standards
- > Package:TO-220F

#### **Applications**

- Fast charger
- > Renewable energy
- > Telecom and data-com
- Servo motors
- Industrial
- > Automotive





**Circuit Diagram** 

## Absolute maximum rating@25°C

Rating	Symbol	Value	Units	
Drain-Source Voltage	$V_{DS}$	650	V	
Drain-Source Voltage-transient1)	V <sub>DS(transient)</sub>	800	V	
Gate-Source Voltage	V <sub>GS</sub>	-20 to +20	V	
Duning Commont Constitution 2)	T <sub>C</sub> =25°C		16	Α
Drain Current-Continuous <sup>2)</sup>	T <sub>C</sub> =125°C	I <sub>D</sub>	7.0	А
Pulse Drain Current (pulse width: 100µs)	I <sub>DM</sub>	27	А	
Maximum Power Dissipation	P <sub>D</sub>	78	W	
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	°C	

#### Notes:

- 1. In off-state, spike duty cycle D<0.01, spike duration <1µs
- For increased stability at high current operation.

### Thermal characteristics

Parameter	Symbol	Min.	Тур.	Max.	Units
Thermal Resistance, Junction - Case	$R_{\theta JC}$	-	1.6	-	°C/W

# Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V	650	-	-	V		
T. I. D. C. I. I. O. I.		V <sub>DS</sub> = 650V,V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C	-	-	10	μА		
Total Drain Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 650V,V <sub>GS</sub> = 0V, T <sub>J</sub> =150°C	-	-	100			
Gate Threshold Voltage	V <sub>GS(th)</sub>			4	4.8	V		
Gate Threshold Voltage Temperature Coefficient	$\triangle V_{GS(th)}/T_J$	$V_{GS} = V_{DS}, I_{D} = 1 \text{mA}$	-	-7	-	mV/°C		
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V	-	-	±100	nA		
Static Drain Source On Decistores	В	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A, T <sub>J</sub> =25°C	-	160	220	· mΩ		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A, T <sub>J</sub> =150°C	-	340	-			
Input Capacitance	C <sub>iss</sub>		-	293	-	pF		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 400V,V <sub>GS</sub> = 0V, f= 1MHz	-	17	-			
Reverse Transfer Capacitance	C <sub>rss</sub>		-	3.74	-			
Output Charge	Q <sub>oss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V to 400V, f=1MHz	-	22.2	-	nC		
Total Gate Charge	$Q_g$		-	7.9	-			
Gate-Source Charge	$Q_{gs}$	V <sub>GS</sub> =0 to 10V, V <sub>DS</sub> =400V, I <sub>D</sub> =1A		2.31	-	nC		
Gate-Drain Charge	$Q_{gd}$		-	1.65	-			
Turn-on Delay Time	t <sub>d(on)</sub>		-	3.2	-			
Turn-on Rise Time	t <sub>r</sub>	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V to 10V,	-	5.5	-			
Turn-Off Delay Time	$\begin{array}{c c} & I_D = 2.1A, \ R_{G-on(ext)} = 6.8\Omega, \\ \hline \text{rime} & t_{d(off)} & R_{G-off(ext)} = 2.2\Omega, \ L = 250 \mu H \end{array}$		-	7.4	-	ns		
Turn-Off Fall Time	t <sub>f</sub>		-	27	-	1		
Reverse Device Characteristics								
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V,I <sub>SD</sub> = 10A	-	2.5	-	V		
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =10A, V <sub>DD</sub> =400V,	-	14	-	ns		
Reverse Recovery Charge	Q <sub>rr</sub>	dI <sub>F</sub> /dt=165A/μs	-	6.5	-	nC		

## **Typical Characteristics**

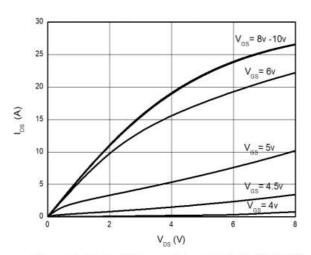


Figure 1. Typical Output Characteristics T<sub>1</sub>=25°C

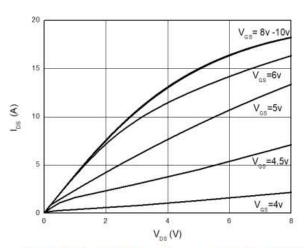


Figure 2. Typical Output Characteristics T<sub>1</sub>=125°C

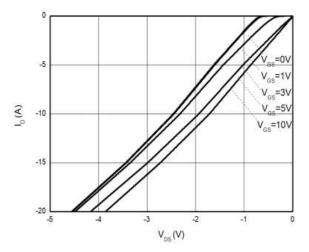


Figure 3. Channel Reverse Characteristics T<sub>J</sub>=25°C

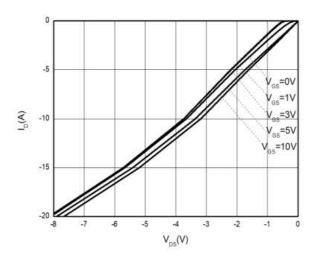


Figure 4. Channel Reverse Characteristics T,=125°C

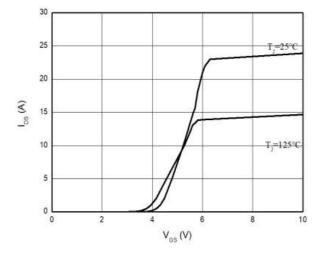


Figure 5. Typical Transfer Characteristics (VDS=5V)

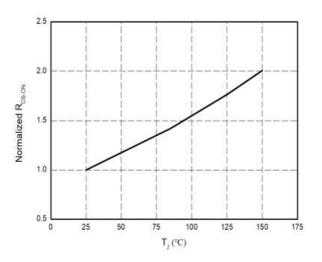


Figure 6. Normalized On-resistance

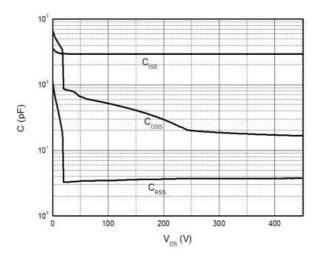


Figure 7. Typical Capacitance (f=1MHz)

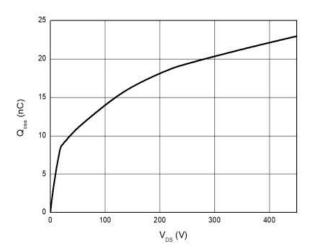


Figure 9. Typical Qoss

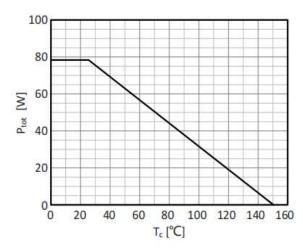


Figure 11. Power Dissipation

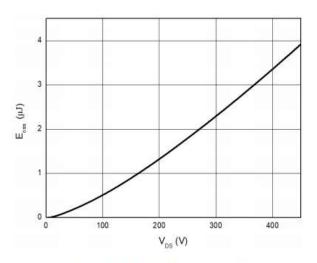


Figure 8. Typical Coss Stored Energy

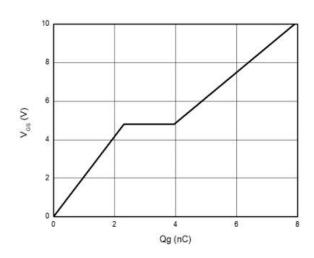


Figure 10. Typical Gate Charge (V<sub>DS</sub>=400V, I<sub>D</sub>=1A)

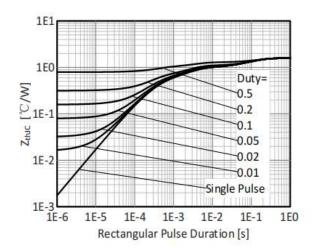


Figure 12. Transient Thermal Resistance

Gallium Nitride PGCTOF65R160A

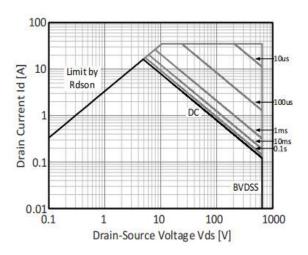


Figure 13. Safe Operating Area T<sub>C</sub>=25°C

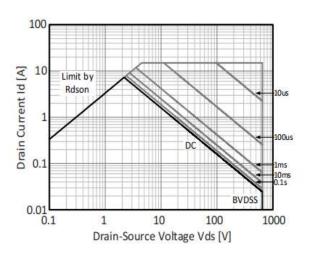
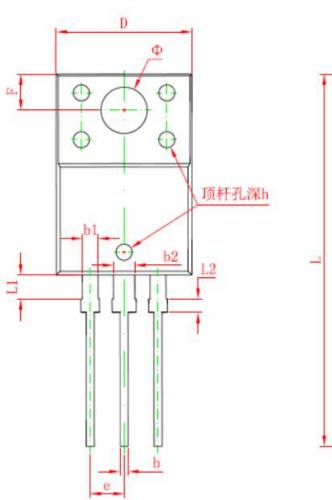


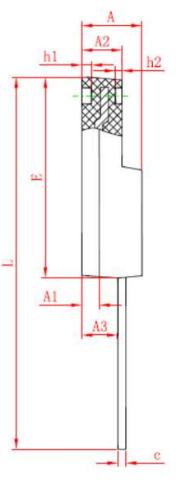
Figure 14. Safe Operating Area T<sub>C</sub>=125°C

# **Product Dimension (TO-220F)**

Top view

Side view





D'	Millimeters Inches		D'	Millimeters		Inches					
Dim	Min	Max	Min	Max	Dim	Min	Max	Min	Max		
А	4.300	4.700	0.169	0.185	е	2.540 TYP.		0.100 TYP.			
A1	1.300	REF.	1.300	REF.	F	2.700REF.		0.106 REF.			
A2	2.800	3.200	0.110	0.126	Φ	3.500 REF.		3.500 REF. 0.		0.138	REF.
A3	2.500	2.900	0.098	0.114	h	0.000	0.300	0.000	0.012		
b	0.500	0.750	0.020	0.030	h1	0.800 REF.		0.031 REF.			
b1	1.100	1.350	0.043	0.053	h2	0.500 REF.		2EF. 0.020 REF.			
b2	1.500	1.750	0.059	0.069	L	28.000	28.400	1.102	1.118		
С	0.500	0.750	0.020	0.030	L1	1.700	1.900	0.067	0.075		
D	9.960	10.360	0.392	0.408	L2	0.900	1.100	0.035	0.043		
E	14.800	15.200	0.583	0.598							

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