

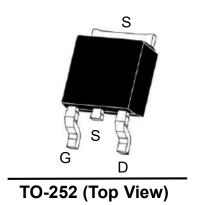
650V Enhancement-mode GaN Transistor

Description

650V Normally-OFF GaN							
V _{DS} (V)	$R_{DS(on)}(m\Omega)$	I _{DS} (A)	Q _G (nC)				
650	270	7.9	7.9				

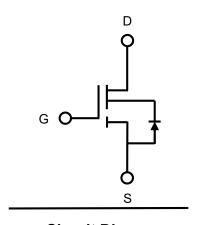
Feature

- Easy to drive—compatible with standard gate drivers
- ➤ Low conduction and switching losses
- > RoHS compliant and Halogen-free



Applications

- Adapter
- Renewable energy
- > Telecom and data-com
- Servo motors
- > Industrial
- > Automotive



Circuit Diagram

Absolute maximum rating@25°C

Rating	Symbol	Value	Units	
Drain-Source Voltage	V_{DS}	650	V	
Drain-Source Voltage-transient1)	V _{DS(transient)}	800	V	
Gate-Source Voltage	V_{GS}	-20 to +20	V	
Due in Course at Courtinous 2)	T _C =25°C		7.9	Α
Drain Current-Continuous ²⁾	T _C =125°C	I _D	3.5	Α
Pulse Drain Current (pulse width: 100µs)	I _{DM}	14	Α	
Maximum Power Dissipation	P_D	32	W	
Junction and Storage Temperature Range	$T_{J,}T_{STG}$	-55~+150	°C	

Notes:

- 1. In off-state, spike duty cycle D<0.01, spike duration <1µs
- 2. For increased stability at high current operation.

Thermal characteristics

Parameter	Symbol	Min.	Тур.	Max.	Units
Thermal Resistance, Junction - Case	$R_{\theta JC}$	-	3.9	-	°C/W

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V	650	-	-	V
Total Duain Laukawa Cumant		V _{DS} = 650V,V _{GS} = 0V, T _J =25°C	-	-	10	μΑ
Total Drain Leakage Current	I _{DSS}	V _{DS} = 650V,V _{GS} = 0V, T _J =150°C	-	-	100	
Gate Threshold Voltage	V _{GS(th)}	V V 1 4 4	3	4	4.8	V
Gate Threshold Voltage Temperature Coefficient	$\triangle V_{GS(th)}/T_J$	$V_{GS} = V_{DS}$, $I_{DS} = 1$ mA	-	-7	-	mV/°C
Gate-Source Leakage Current	I _{GSS}	V_{GS} = $\pm 20V$	1	-	±100	nA
Static Drain-Source On-Resistance	D	V _{GS} = 10V, I _D = 1A, T _J =25°C	1	270	320	mΩ
Static Dialit-Source Off-Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 1A, T _J =150°C	ı	570	ı	
Input Capacitance	C _{iss}		1	293	ı	pF
Output Capacitance	C _{oss}	V_{DS} = 400V, V_{GS} = 0V, f= 1MHz	-	17	-	
Reverse Transfer Capacitance	C _{rss}		-	3.74	-	
Output Charge	Q _{oss}	V_{GS} =0V, V_{DS} =0V to 400V, f= 1MHz	-	22.2	-	nC
Total Gate Charge	Q_g		-	7.9	-	
Gate-Source Charge	Q_{gs}	V_{GS} =0 to 10V, V_{DS} =400V, I_{D} =1A	-	2.31	-	nC
Gate-Drain Charge	Q_{gd}		-	1.65	-	
Turn-on Delay Time	t _{d(on)}		-	3.2	-	
Turn-on Rise Time	t _r	V _{DS} =400V, V _{GS} =0V to 10V,	-	5.5	-	ns
Turn-Off Delay Time	t _{d(off)}	I_D =2.1A, $R_{G-on(ext)}$ =6.8 Ω , $R_{G-off(ext)}$ =2.2 Ω , L=250 μ H	-	7.4	-	
Turn-Off Fall Time	t _f		-	27	-	
Reverse Device Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V,I _{SD} = 5A	-	2.3	-	V
Reverse Recovery Time	t _{rr}	I _F =10A, V _{DD} =400V,	-	14	-	ns
Reverse Recovery Charge	Q _{rr}	dI _F /dt=165A/μs	-	6.5	-	nC

Typical Characteristics

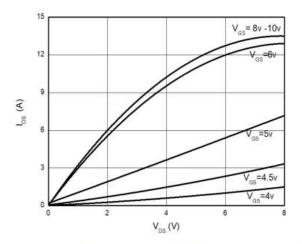


Figure 1. Typical Output Characteristics T_j=25°C

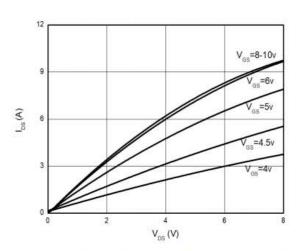


Figure 2. Typical Output Characteristics T_j=125°C

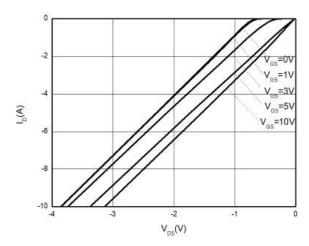


Figure 3. Channel Reverse Characteristics T_j=25°C

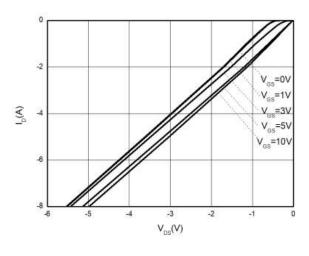


Figure 4. Channel Reverse Characteristics T_j=125°C

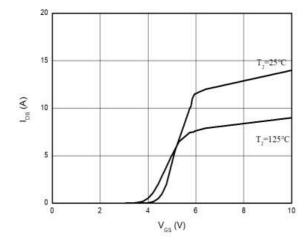


Figure 5. Typical Transfer Characteristics (VDS=5V)

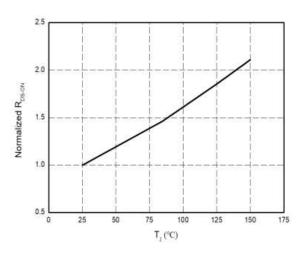


Figure 6. Normalized On-resistance

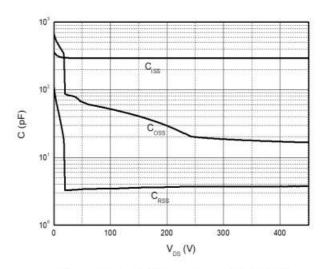


Figure 7. Typical Capacitance (f=1MHz)

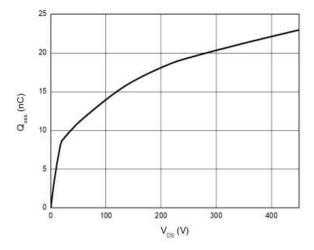


Figure 9. Typical Qoss

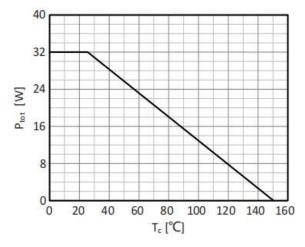


Figure 11. Power Dissipation

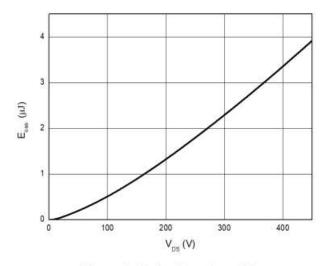


Figure 8. Typical Coss Stored Energy

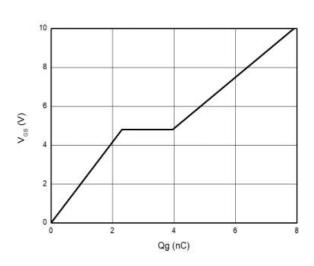


Figure 10. Typical Gate Charge (V_{DS}=400V, I_D=1A)

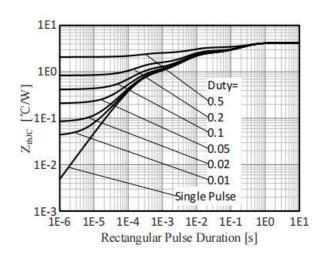


Figure 12. Transient Thermal Resistance

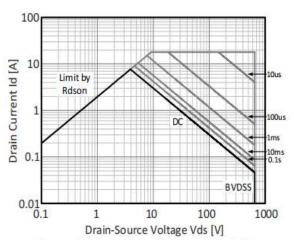


Figure 13. Safe Operating Area T_C=25°C

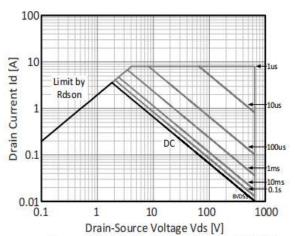
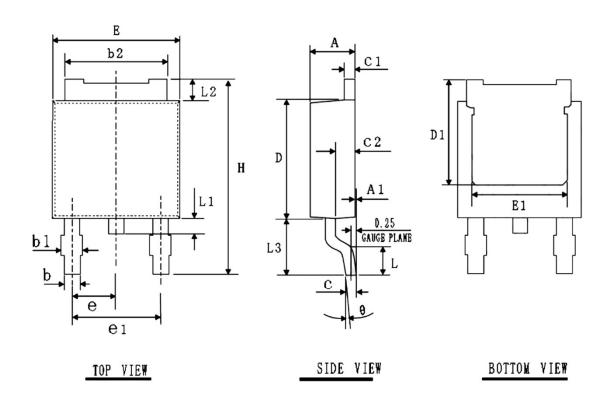
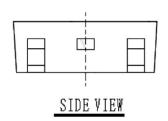


Figure 14. Safe Operating Area T_C=125°C

Product Dimension (TO-252)





SYMB	Millimeters		SYMB	Millimeters		SYMB	Millimeters				
OL	MIN	NOM	MAX	OL	OL MIN NOM	MAX	OL	MIN	NOM	MAX	
А	2.20	2.30	2.40	D1	5.25	5.45	5.65	θ	0°	4°	8°
A1	0.00	0.05	0.10	Н	10.00	10.10	10.20	е	2.285 BSC		
b	0.762	0.812	0.862	E	6.50	6.60	6.70				
b 1			1.10	E1	4.75	4.85	4.95				
b2	5.23	5.33	5.43	e1	4.37	4.57	4.77				
С	0.458	0.508	0.558	L			1.45				
C1	0.458	0.508	0.558	L1	0.60	0.75	0.90				
C2	0.80	1.00	1.20	L2	0.90	1.10	1.30				
D	6.00	6.10	6.20	L3	2.80	3.00	3.20				

IMPORTANT NOTICE

🕜 and Prisemi are registered trademarks of Prisemi Electronics Co., Ltd (Prisemi), Prisemi reserves the right to make changes without further notice to any products herein. Prisemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Prisemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in Prisemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Prisemi does not convey any license under its patent rights nor the rights of others. The products listed in this document are designed to be used with ordinary electronic equipment or devices, Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

Website: http://www.prisemi.com
For additional information, please contact your local Sales Representative.

©Copyright 2009, Prisemi Electronics

Prisemi is a registered trademark of Prisemi Electronics.

All rights are reserved.