

#### **Description**

The PNMTO650V4 is a high voltage MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

MOSFET Product Summary					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
650	2.4 @ V <sub>GS</sub> = 10V	4			

#### Feature

- Fast switching capability
- Avalanche energy tested
- Improved dv/dt capability, high ruggedness

#### **Mechanical Characteristics**

- > Case: TO-220-3L
- Approx. Weight: 2.0g (0.07oz)
- Lead free finish, RoHS compliant
- Case Material: "Green" molding compound, UL flammability classification 94V-0,"Halogen-free".

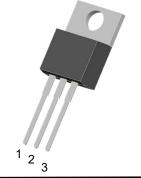
#### Absolute maximum rating@25°C

Rating		Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	650	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Drein Current Centinueue	Tc=25°C		4.0	•
Drain Current-Continuous	Tc=100°C		2.5	A
Pulsed Drain Current <sup>2)</sup>		I <sub>DM</sub>	16	А
Avalanche Energy Single Pulsed <sup>3)</sup>		E <sub>AS</sub>	173	mJ
Peak Diode Recovery dv/dt <sup>4)</sup>		dv/dt	2.1	V/ns
Maximum Power Dissipation		P <sub>D</sub>	109	W
Thermal Resistance , Junction-case		R <sub>eJC</sub>	1.17	°C/W
Thermal Resistance Junction-to-Ambient		R <sub>θJA</sub>	62.5	°C/W
Junction and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55~+150	°C

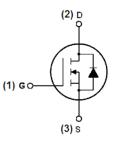
Notes:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature. 3. L = 100mH, I<sub>AS</sub> = 4.1A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25  $\Omega$ , Starting T<sub>J</sub> = 25°C 4. I<sub>SD</sub> ≤ 10A, di/dt ≤ 200A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C



TO-220 (Top View)



#### Schematic diagram



#### **PNMTO650V4**

**N-Channel MOSFET** 

## PNMTO650V4

#### Electrical characteristics per line@25°C (unless otherwise specified) **Symbol** Conditions Min. Units **Parameter** Тур. Max. **Off Characteristics** $V_{GS} = 0V, I_{D} = 250 \mu A$ 650 V Drain-Source Breakdown Voltage $BV_{DSS}$ --Zero Gate Voltage Drain Current $V_{DS} = 650V, V_{GS} = 0V$ IDSS 1.0 μA -- $V_{GS} = \pm 30V, V_{DS} = 0V$ Gate-Body Leakage Current \_ $\pm 100$ nA I<sub>GSS</sub> \_ **On Characteristics** V Gate Threshold Voltage V<sub>GS(th)</sub> $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ 2.0 -4.0 Drain-Source On-State Resistance $V_{GS} = 10V, I_{D} = 2A$ \_ 2.4 2.6 Ω R<sub>DS(ON)</sub> **Dynamic Characteristics** Input Capacitance Clss 560 \_ \_ $V_{\rm DS} = 25 V, V_{\rm GS} = 0 V,$ f = 1.0MHz **Output Capacitance** Coss 55 pF \_ \_ **Reverse Transfer Capacitance** C<sub>rss</sub> 5.0 \_ **Switching Characteristics** Total Gate Charge<sup>1)</sup> $\mathsf{Q}_{\mathsf{g}}$ 13 $V_{DS} = 520V, V_{GS} = 10V$ $I_{D} = 4A, I_{G} = 1mA^{(1)}$ $\mathsf{Q}_{\mathsf{gs}}$ 3.0 Gate-Source Charge nC \_ -Gate-Drain Charge 2.2 $Q_{qd}$ \_ -Turn-on Delay Time<sup>1)</sup> 7.0 t<sub>d(on)</sub> \_ \_ Turn-on Rise Time 16 t<sub>r</sub> $V_{DD} = 325V, V_{GS} = 10V, I_D = 4A, R_G = 25\Omega^{1/2}$ ns Turn-Off Delay Time 36 t<sub>d(off)</sub> 22 **Turn-Off Fall Time** t<sub>f</sub> \_ \_ **Drain-Source Diode Characteristics** 4.0 **Diode Forward Current** $I_{SD}$ А --**Pulsed Drain-Source Current** 16 А I<sub>SM</sub> \_ Diode Forward Voltage<sup>1)</sup> $V_{GS} = 0V, I_{S} = 4A$ 1.4 V $V_{SD}$ \_ \_ Reverse Recovery Time<sup>1)</sup> 250 t<sub>rr</sub> -\_ nS $V_{GS} = 0V, I_{S} = 4A,$ $di/dt = 100A/\mu s$ **Reverse Recovery Charge** Q<sub>rr</sub> 4.5 μC

Notes:

1. Pulse Test: Pulse width  $\leq$  300µs, Duty cycle  $\leq$  2%.

2. Essentially independent of operating temperature.

#### **Typical Characteristics**

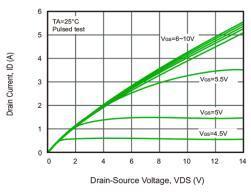


Fig.1 Drain Current vs. Gate-Source Voltage

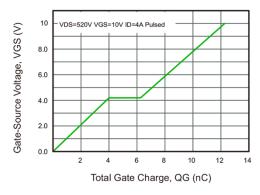


Fig.3 Gate Charge Characteristics

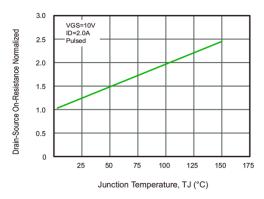


Fig.5 Drain-Source On-Resistance vs. Junction Temperature

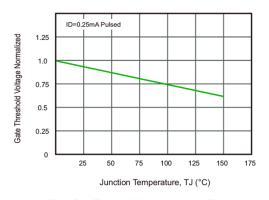


Fig.7 Gate Threshold Voltage vs. Junction Temperature

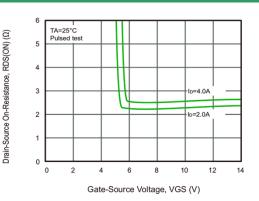


Fig.2 Drain-Source On-Resistance vs. Gate-Source Voltage

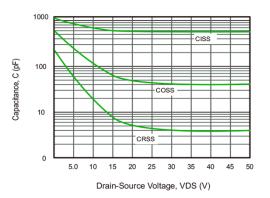


Fig.4 Capacitance Characteristics

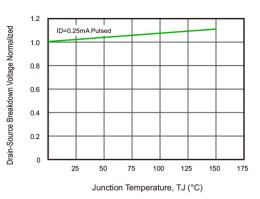


Fig.6 Breakdown Voltage vs. Junction Temperature

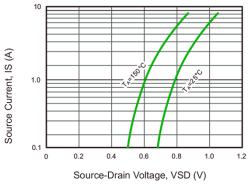


Fig.8 Source Current vs. Source-Drain Voltage

## PNMTO650V4

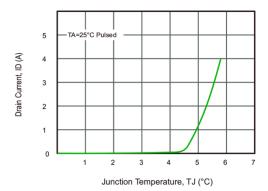


Fig.9 Drain Current vs. Gate-Source Voltage

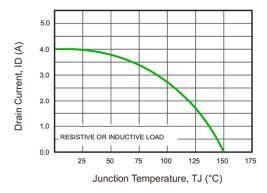
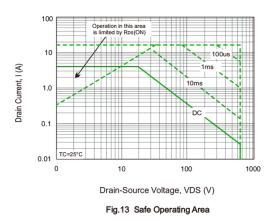


Fig.11 Drain Current vs. Junction Temperature



4.0 TA=25°C VGS=10V Pulsed Drain-Source On-Resistance, RDS(ON) (Ω) 3.5 3.0 2.5 2.0 1.5 1.0 0.5 0 1 2 5 6 3 4 7 Drain Current, ID (A)

Fig.10 Drain-Source On-Resistance vs. Drain Current

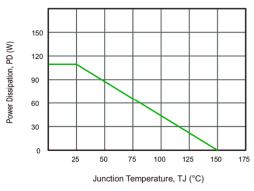
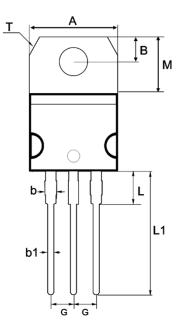


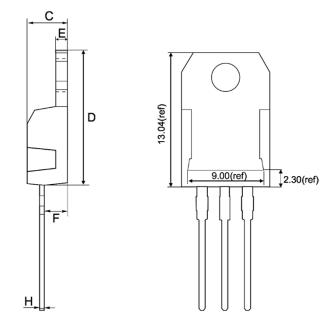
Fig.12 Power Dissipation vs. Junction Temperature

# PNMTO650V4

## **PNMTO650V4**

## Product dimension (TO-220-3L)





Dim	Millim	neters	Inches		
	Min	Мах	Min	Max	
A	10.08	10.28	0.397	0.405	
В	2.64	2.84	0.104	0.112	
b	1.18	1.48	0.046	0.058	
b1	0.70	0.90	0.028	0.035	
С	4.25	4.65	0.167	0.183	
D	15.14	15.54	0.596	0.612	
E	1.17	1.37	0.046	0.054	
F	2.39	2.79	0.094	0.110	
G	2.44	2.64	0.096	0.104	
н	0.40	0.60	0.016	0.024	
L	3.48	3.88	0.137	0.153	
L1	12.73	13.13	0.501	0.517	
М	5.99	6.39	0.236	0.252	
N	3.82 Тур.		0.150 Тур.		
Т	1.19 Тур.		0.047 Тур.		

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