

## Description

The PNMDP500V2 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

MOSFET Product Summary				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
500	5 @ V <sub>GS</sub> = 10V	2.0		

### Feature

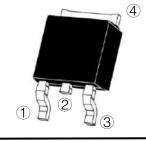
- Fast switching capability
- Avalanche energy tested
- Improved dv/dt capability, high ruggedness

## **Mechanical data**

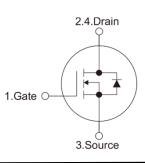
- ➤ Case: TO-252
- Approx. Weight: 0.315g (0.011oz)
- Lead free finish, RoHS compliant
- Case Material: "Green" molding compound, UL flammability classification 94V-0,"Halogen-free".

## Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V <sub>DS</sub>	500	V
Gate-Source Voltage	V <sub>GS</sub>	±30	V
Drain Current-Continuous $\frac{T_{c}=25^{\circ}C}{T_{c}=100^{\circ}C}$	I <sub>D</sub>	2.0 1.3	A
Pulsed Drain Current <sup>2)</sup>	I <sub>DM</sub>	8.0	А
Avalanche Energy, Single Pulsed <sup>3)</sup>	E <sub>AS</sub>	35	mJ
Peak Diode Recovery dv/dt4)	dv/dt	2.1	V/ns
Maximum Power Dissipation	P <sub>D</sub>	54	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 ~ +150	°C
Junction-to-Ambient	$R_{ extsf{ heta}JA}$	63	°C/W
Junction to Case	$R_{ extsf{ heta}JC}$	2.31	°C/W



## TO-252 (Top View)



## Schematic diagram

# PNMDP500V2

**N-Channel MOSFET** 

# PNMDP500V2

Electrical characteristics per line@25°C (unless otherwise specified)						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0V, I_{D} = 250 \mu A$	500	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 500V,V <sub>GS</sub> = 0V	-	-	1.0	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{V}, \text{V}_{DS} = 0 \text{V}$	-	-	±100	nA
On Characteristics			•		•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V,I <sub>D</sub> = 1.0A	-	5.0	7.0	Ω
Dynamic Characteristics						
Input Capacitance	C <sub>lss</sub>		-	156	-	pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	-	24	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	2.2	-	
Switching Characteristics						
Turn-on Delay Time <sup>5)</sup>	t <sub>d(on)</sub>		-	2.7	-	ns
Turn-on Rise Time	t <sub>r</sub>	V <sub>DS</sub> = 250V, V <sub>GS</sub> = 10V,	-	12.1	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{\rm D} = 2.0 {\rm A}, {\rm R}_{\rm G} = 10 \Omega^{5,6)}$	-	16.7	-	
Turn-Off Fall Time	t <sub>f</sub>		-	7.2	-	
Total Gate Charge <sup>5)</sup>	Q <sub>g</sub>		-	7.9	-	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 400V, V_{GS} = 10V, I_{D} = 2.0A, I_{G} = 1mA^{5,6)}$	-	0.9	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	5.4	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>5)</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V,I <sub>S</sub> = 2.0A	-	-	1.5	V
Diode Continuous Current	۱ <sub>s</sub>		-	-	2.0	А
Diode Pulsed Current	I <sub>SM</sub>		-	-	8.0	А
Reverse Recovery Time <sup>5)</sup>	t <sub>rr</sub>	V <sub>GS</sub> = 0V,I <sub>S</sub> = 2.0A,	-	309	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/µs	-	720	-	μC

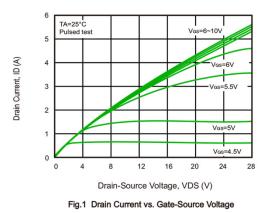
Notes:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature. 3.L = 10mH,  $I_{AS} = 2.6A$ ,  $V_{DD} = 50V$ ,  $R_G = 10\Omega$ , Starting  $T_J = 25^{\circ}C$ 4.  $I_{SD} \le 2A$ , di/dt  $\le 100A/\mu$ s,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$ 5. Pulse Test: Pulse width  $\le 300\mu$ s, Duty cycle  $\le 2\%$ .

6. Essentially independent of operating temperature.

## **Typical Characteristics**



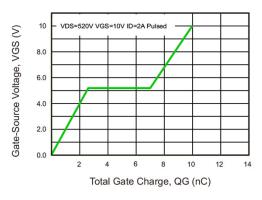


Fig.3 Gate Charge Characteristics

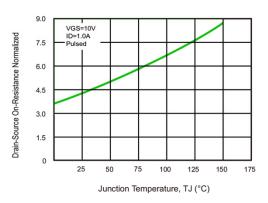
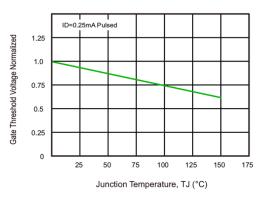
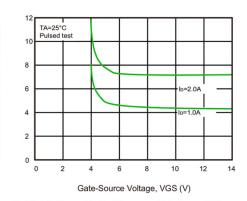


Fig.5 Drain-Source On-Resistance vs. Junction Temperature







Drain-Source On-Resistance, RDS(ON) (Ω)

Fig.2 Drain-Source On-Resistance vs. Gate-Source Voltage

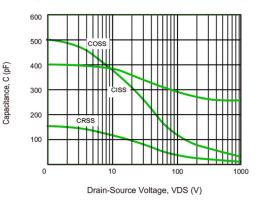


Fig.4 Capacitance Characteristics

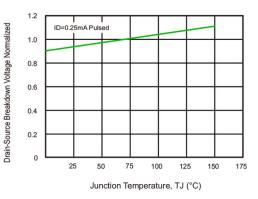


Fig.6 Breakdown Voltage vs. Junction Temperature

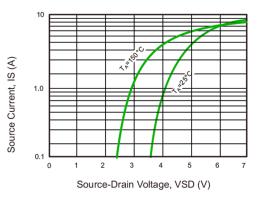
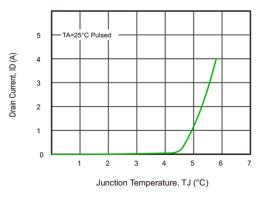


Fig.8 Source Current vs. Source-Drain Voltage

# PNMDP500V2





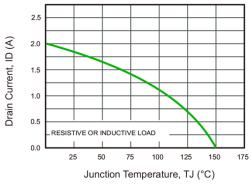
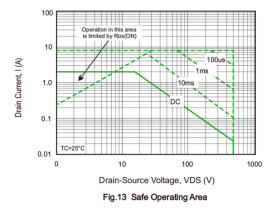


Fig.11 Drain Current vs. Junction Temperature



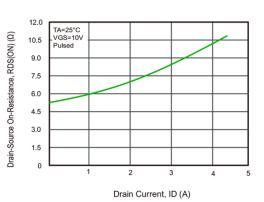


Fig.10 Drain-Source On-Resistance vs. Drain Current

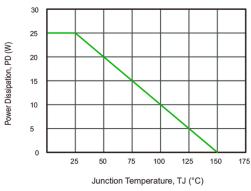
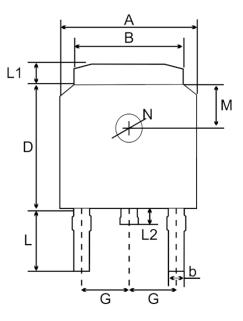
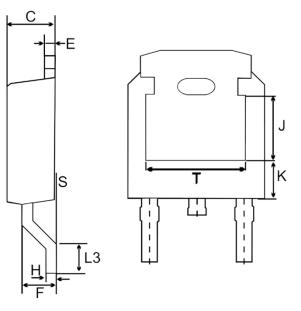


Fig.12 Power Dissipation vs. Junction Temperature

# PNMDP500V2

# Product dimension (TO-252)





Dim	Millimeters		Inches		
Dim	Min	Мах	Min	Max	
A	6.30	6.70	0.248	0.264	
В	5.10	5.50	0.201	0.217	
b	0.30	0.80	0.012	0.031	
С	2.10	2.50	0.083	0.098	
D	5.90	6.30	0.232	0.248	
E	0.40	0.60	0.016	0.024	
F	1.30	1.80	0.051	0.071	
G	2.29 Тур.		0.090 Тур.		
Н	0.45	0.55	0.018	0.022	
L	2.70	3.10	0.106	0.122	
L1	0.80	1.20	0.031	0.047	
L2	0.60	1.00	0.024	0.039	
L3	1.00	1.75	0.039	0.069	
S	0.00	0.23	0.000	0.009	
М	1.80 Тур.		0.071 Тур.		
Ν	1.30 Тур.		0.051 Тур.		
J	3.16 Ref.		0.124 Ref.		
к	1.80 Ref.		0.071 Ref.		
Т	4.83 Ref.		0.190 Ref.		

# PNMDP500V2

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